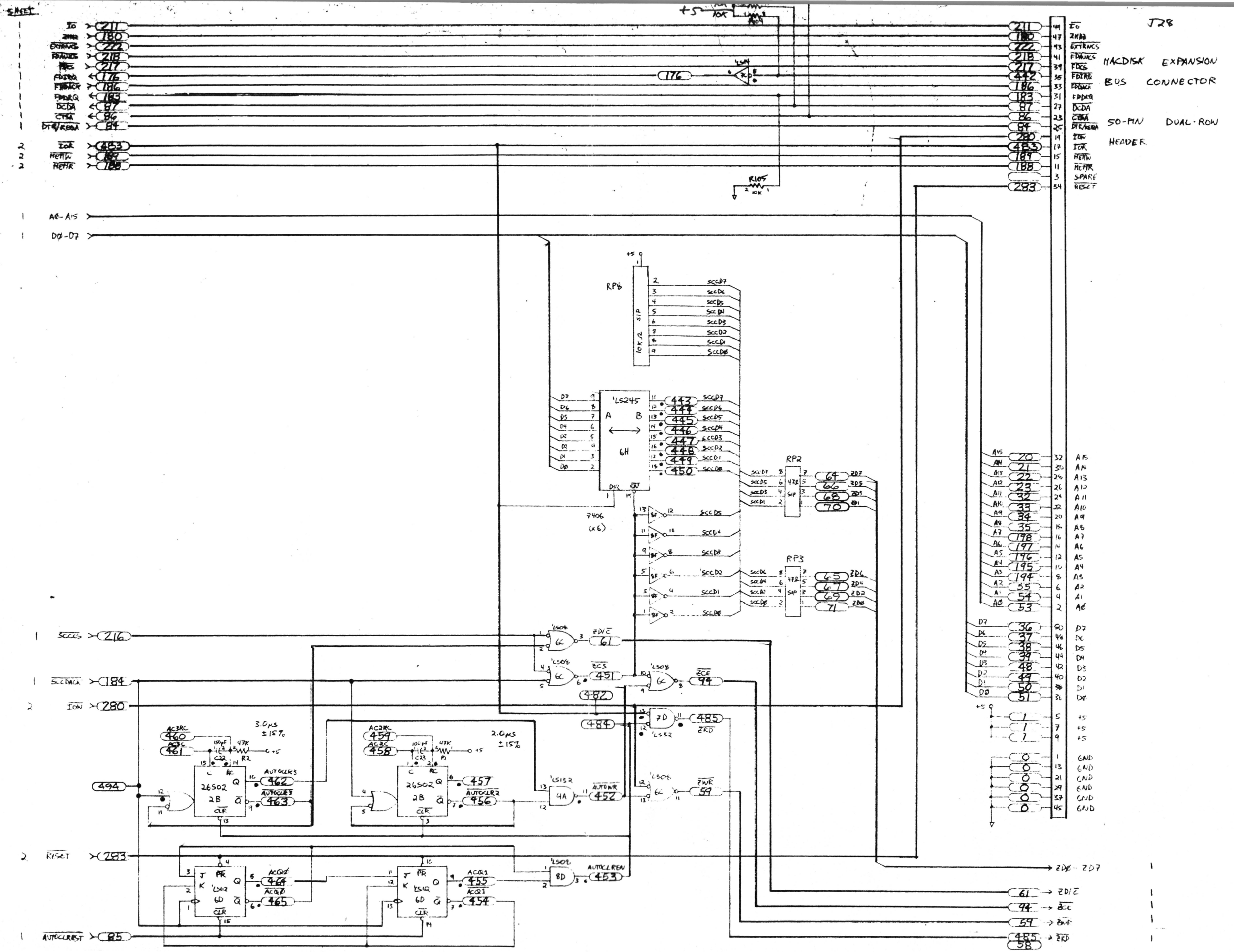
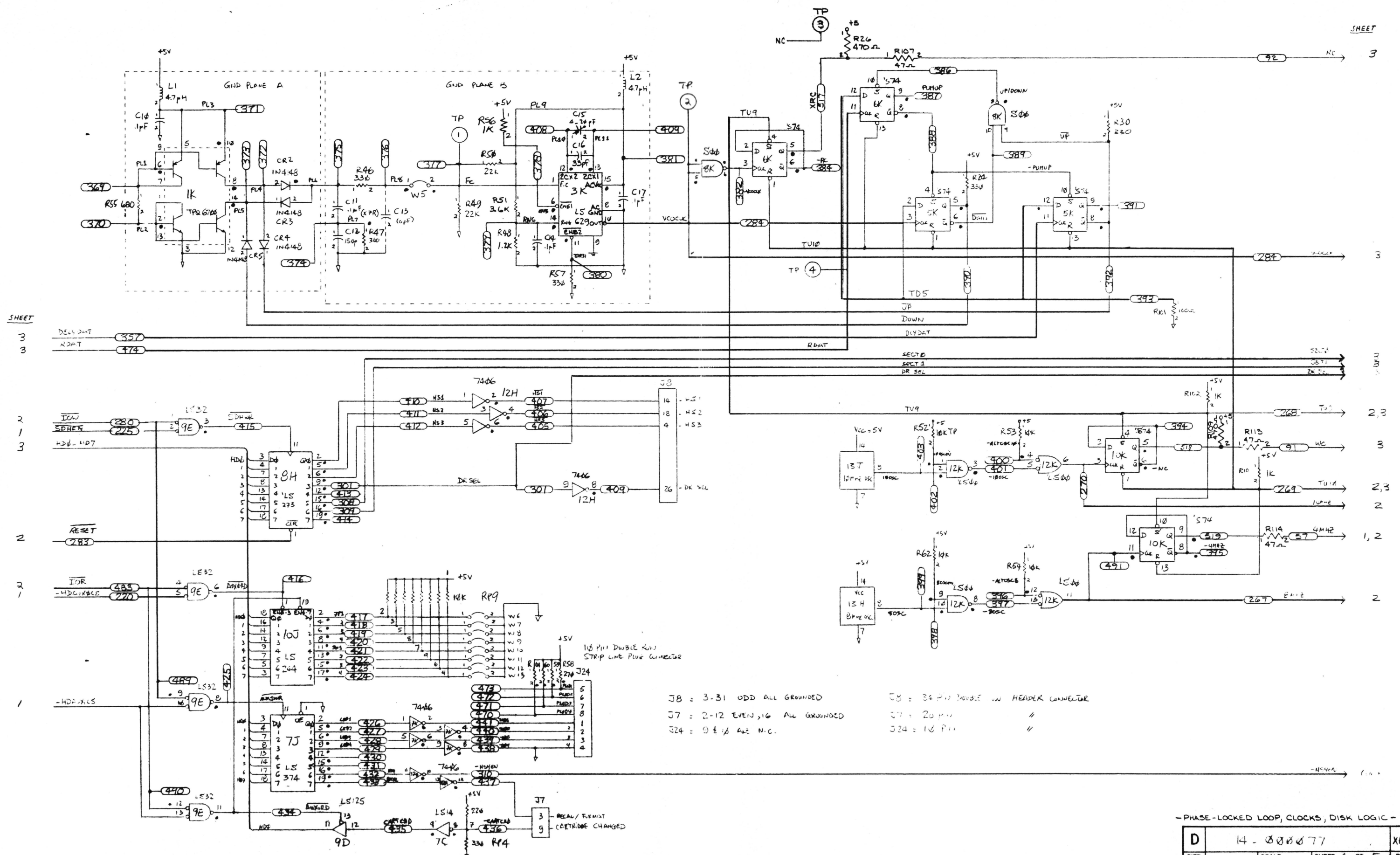






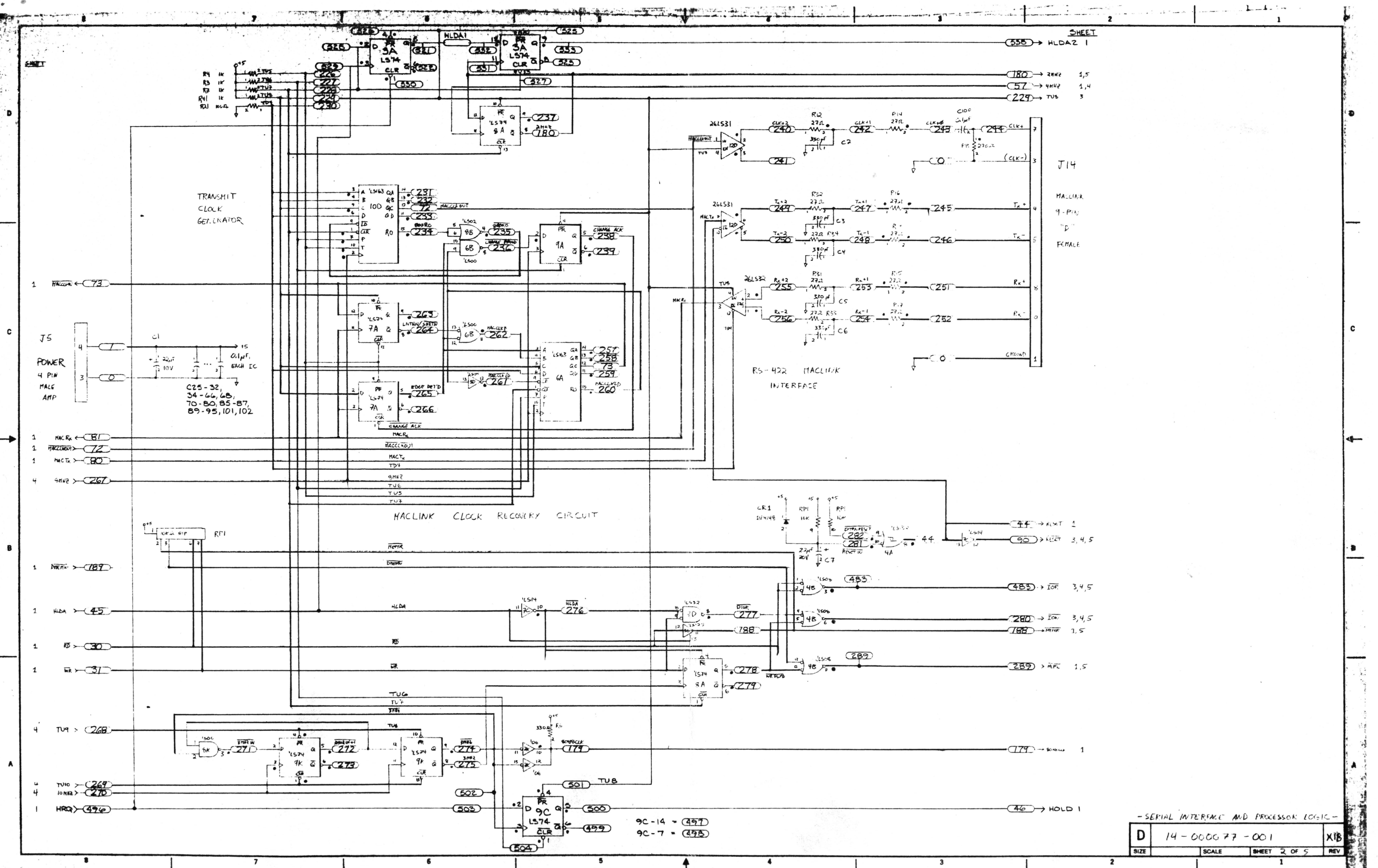
J8 = 3-31 ODD ALL GROUND
J7 = 2-12 EVEN, 16 ALL GROUND
J24 = 0 & 16 ALL N.C.

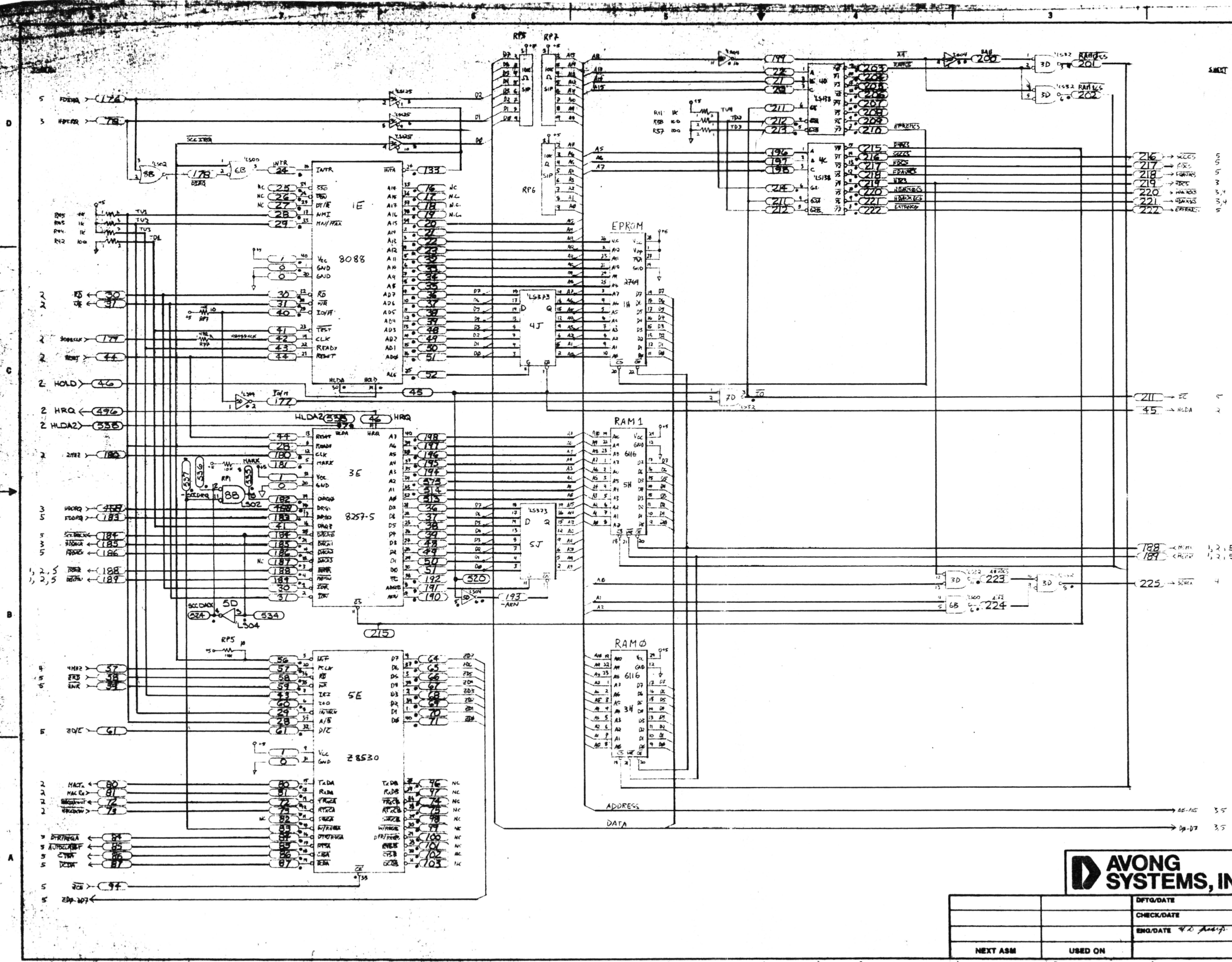
J8 = 34 PIN DOUBLE ROW HEADER CONNECTOR
J7 = 20 PIN
J24 = 16 PIN

PHASE-LOCKED LOOP, CLOCKS, DISK LOGIC

D	14-080877	X1B
SIZE	SCALE	SHEET 4 OF 5
		REV







REV	PCO/ECN	DFTG/DATE	CHECK/DATE	ENG/DATE
X6	699	LAW 5/1/84		7/1/84
X7	725	LAW 5/17/84		5/1/84
X8	734	LAW 5/29/84		5/1/84
X9	747	FA 6/21/84		5/1/84
X10	759	LAW 6/22/84		5/1/84
X11	772	LAW 6/27/84		5/1/84
X12	775	LAW 7/24/84		5/1/84
X13	787	LAW 7/24/84		5/1/84
X14	793	JK 8-1-84		5/1/84
X15	805	JK 8-24-84		5/1/84
X16	836	JK 9-25-84		5/1/84
X17	845	JK 9-1-85		5/1/85
X18	970	JK 9-1-85		5/1-85

AVONG SYSTEMS, INC.

TITLE	
MACDISK INTELLIGENT DISK CONTROLLER	
DFTG/DATE	4/1/83
CHECK/DATE	
ENG/DATE	4/1/83
D	14-000077-001
SIZE	SCALE
SHEET	OF 5
REV	

NEXT ASM	USED ON