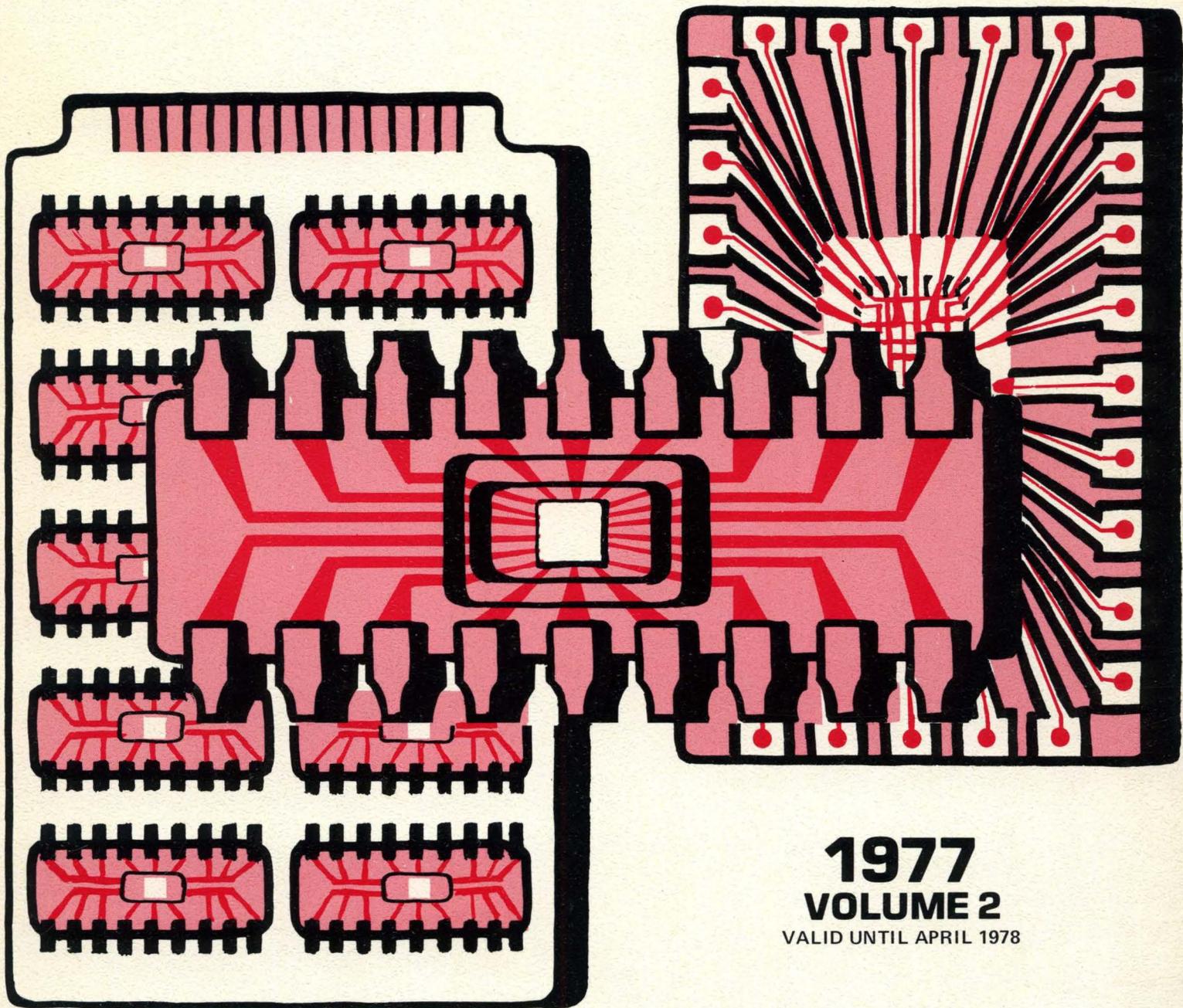


D.A.T.A. BOOK® ELECTRONICS INFORMATION SERIES

# MSI/LSI SEMICONDUCTOR MEMORIES



**1977**  
**VOLUME 2**  
VALID UNTIL APRIL 1978

MSI-LSI MEMORY

D.A.T.A. BOOK®

EIS

77  
2

# Announcing - the most comprehensive Digital IC information service!

## Digital Logic/Computational IC D.A.T.A.BOOK Interface IC D.A.T.A.BOOK

A new, important Digital Integrated Circuits service with superior, definitive technical content, drawings, and easy-reference features will be published in February 1977.

**NECESSARY.** Keeping pace with the information demands of the proliferating and fast-changing IC technology is vital to the design and production of tomorrow's equipment and products. A bold new service has been designed. It captures the breadth and depth of the worldwide IC industry data needs of today and will keep you aware as the industry progresses into the future.

Check the content lists shown here. See that the two-volume organization gives you 30 new technical sections; a total of 37 in all. The present one-volume Digital IC D.A.T.A.BOOK contains only seven.

**TAILORED TO YOUR NEEDS.** You will find the specific Digital IC information you require — quickly, easily. Each volume details a separate major segment of the IC market.

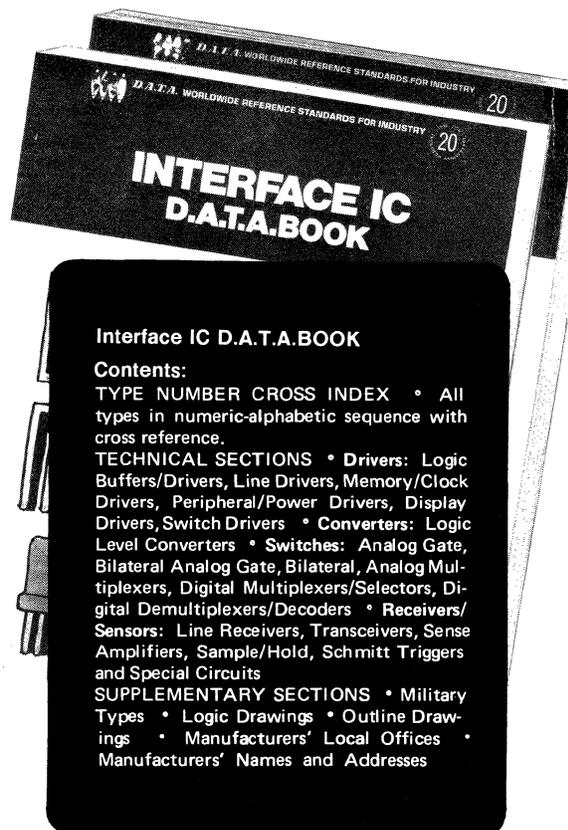
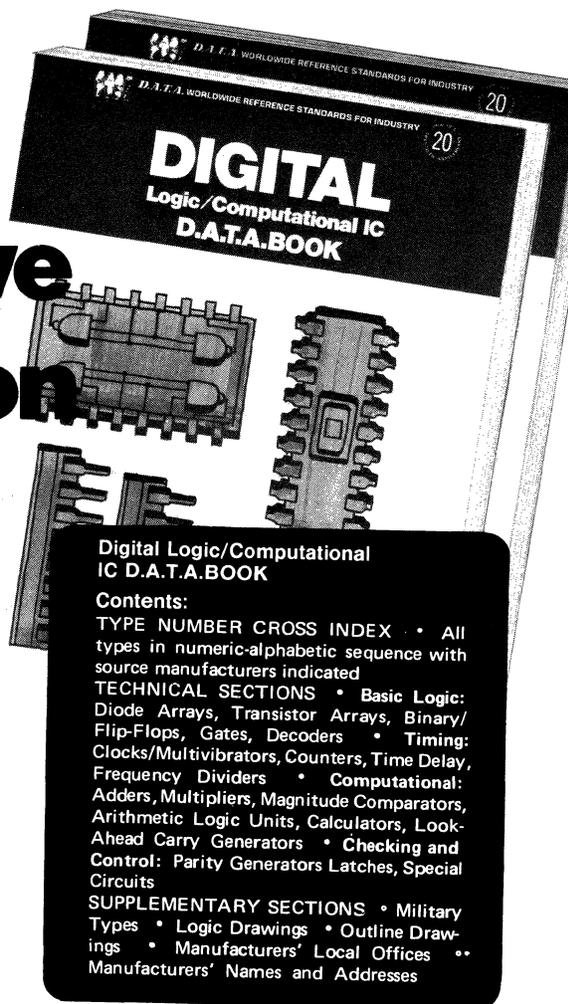
- Basic device characteristics are arranged for easy reference in the logic and computational volume.
- Complex device characteristics are uniquely indexed in the interface volume.

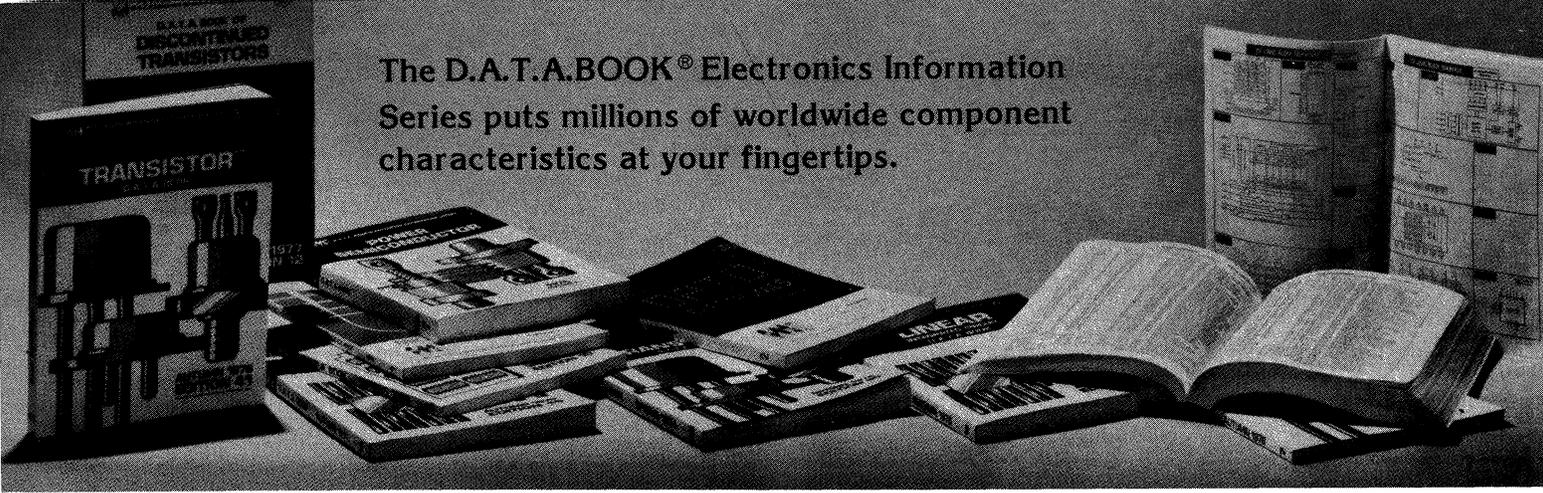
We know you will probably want both volumes. You may require only one. The choice will be yours — and you need only subscribe according to your needs.

**SEE ORDER CARD IN FRONT OF BOOK**

D.A.T.A., INC.

A Cordura Company 45 U.S. Highway 46, P.O. Box 602,  
Pine Brook, New Jersey 07058 Telephone (201) 227-3740





The D.A.T.A.BOOK® Electronics Information Series puts millions of worldwide component characteristics at your fingertips.

### Discrete Device Services

**TRANSISTORS:** Thirteen (13) technical sections listing over 20,400 type numbers from 120 manufacturers, arranged for immediate comparison . . . plus, outline drawings, more than 490 MIL Spec type numbers, over 4300 JEDEC types, and the two most popular replacement series. Two (2) completely revised volumes per yearly subscription.

**SEMICONDUCTOR DIODES:** Gives detailed information on over 50,600 diodes from 148 manufacturers, including the two most popular replacement series. Dimensional outline drawings and military types with their QPL manufacturers, plus 6280 JEDEC type numbers complete this valuable reference source. Published semi-annually.

**THYRISTORS:** Covers SCRs, TRIACs, Schokly Diodes, Gate Turn-off devices, SCSs, Triggers, etc. The world's only comprehensive source of technical information from 79 worldwide manufacturers of Thyristors. Contains 18,600 types with lead designation and outline drawings, plus JEDEC types and MIL Spec types. Published semi-annually.

**DISCONTINUED TRANSISTORS:** Facilitates substitutions when used with the TRANSISTOR D.A.T.A.BOOK. Lists over 11,500 types which have become obsolete since 1956. A "must" for complete replacement data. Published annually.

**DISCONTINUED DIODES:** Facilitates substitution when used with the Semiconductor Diode D.A.T.A.BOOK. Lists over 23,000 types which have become obsolete since 1969. Published annually.

**DISCONTINUED THYRISTORS:** Describes more than 10,000 manufacturer-discontinued types. The format matches corresponding sections in the Thyristor D.A.T.A.BOOK, making this a "must" when looking for replacements. All ex-manufacturers identified. Published annually.

### Integrated Circuits Services

**NEW! DIGITAL LOGIC/COMPUTATIONAL ICs:** The most versatile, comprehensive presentation of basic building block integrated circuits available anywhere - more than 14,000 types from worldwide manufacturers arranged by primary device parameters: Basic Logic, Timing, Computational, Parity and Latch functions. Thousands of Logic and Outline Drawings . . . plus MIL M38510 devices with cross references to commercial types. Published semi-annually.

**NEW! INTERFACE ICs:** Electrical, physical, logic, and connection information on more than 5,000 state-of-the-art interface integrated circuits from worldwide sources. Conveniently arranged by major device parameters within functional groupings: Logic/Peripheral Drivers, A/D, D/A, Level Converters, Switches/Multiplexers, Receivers, and Specialty Sensors. Detailed Logic Outline Drawing section . . . plus MIL Spec references. Published semi-annually.

**MSI-LSI MEMORIES:** Covers 5,100 types from 56 manufacturers in six major categories. ROMs, RAMs, character generators, code converters, and shift registers with logic and outline drawings. Now includes military types with their QPL manufacturers and specification references. Published semi-annually.

**LINEAR ICs:** Provides characteristics for over 9500 types from 80 manufacturers in the following categories: operational, differential, audio, wideband and RF/IF amplifiers; voltage regulators and comparators. Includes schematic and outline drawings, military types with associated military information and the two wellknown replacement series. Two up-dated volumes per year.

**MICROCOMPUTERS:** Comprehensive hardware/software data on systems, cards, chips . . . microcomputers, microprocessors . . . family RAMs, ROMs, and Interface components. Detailed instruction sets, software package descriptions. System, logic and outline drawings, CPU internal architecture, instruction formats. Complete hardware & software product lines for 62 manufacturers. Easy comparison of major operational features. Published semi-annually.

**DISCONTINUED INTEGRATED CIRCUITS:** The only all-inclusive source of data on ICs that have become obsolete since 1965. Follows format of current IC D.A.T.A.BOOKS to facilitate substitution and replacement. Contains over 17,800 types, including major series no longer manufactured. Identifies all ex-manufacturers and is updated with each annual edition. Published annually.

### Special Applications Services

**OPTOELECTRONICS:** 22 technical sections list over 7,000 types from 95 worldwide manufacturers, arranged by primary device parameter; emitters, sensors, photocells, couplers and displays. World's only comprehensive listing of optoelectronic devices. Includes schematic and outline drawings, JEDEC and Military types. Published semi-annually.

**POWER SEMICONDUCTORS:** Power semiconductor applications information in a single volume. Electrical and physical characteristics of over 32,500 power devices from 145 manufacturers. Standard and fast recovery rectifiers (10 Amps and up), power zeners (10 Watts and up), power transistors (1 Amp and up), general purpose and inverter SCRs (10 Amps and up), triacs and miscellaneous thyristors (10 Amps and up) . . . from worldwide manufacturers, plus . . . 612 package outline drawings with leads identified, plus . . . 1N, 2N and 3N JEDEC devices and U.S. MIL spec types. Published semi-annually.

**SEMICONDUCTOR APPLICATION NOTES REFERENCE:** Offers easy access to the application notes on over 4,300 circuits from 56 manufacturers. Notes are tabulated in Analog and Digital circuit categories, Microcomputer/Microprocessor notes, plus Discrete and IC Device categories, with subcategories providing application details. Principal device types referenced for each circuit application. Subscribers can order notes published by these manufacturers from a single source. D.A.T.A. We process and forward requests to the manufacturers, who send notes directly to subscribers.

**MICROWAVE TUBES:** Indexes 5,200 types from 36 manufacturers for military and commercial application. Includes BWTs, TWTs, Crossed-Field Amplifiers, Noise Generators, Magnetrons and TRs and ATRs. Arranged in order of tube type, center frequency, power output and type number. Identifies QPL manufacturers and military specs, plus JEDEC type numbers. Now includes 2,900 discontinued types. Published semi-annually.

**RELAYS:** Makes it possible to review, compare and select from the more than 10,000 devices approximately one (1) cubic inch or less in volume and compatible with semiconductor packaging requirements. Covers: Miniature Armature - Subminiature Armature - Dry Reed - Mercury-wetted Reed - Solid State - Hybrid and Time-delay relays. Shows pin connections and dimensional outlines; MIL types with their QPL manufacturers and specification references. An annual.

For special combination offers and ordering information turn page.

# Combination D.A.T.A.BOOK® Orders Save Money

## Complete 17-title Service

**SAVE 10%**

You receive the complete D.A.T.A. BOOK Electronics Information Series. A one-year subscription, including all semi-annual and annual volumes as published, and save 10% from the price you would pay if you ordered each subscription separately. All 17 titles—29 volumes.

## Integrated Circuits Service

**SAVE 8%**

You receive one-year subscriptions (two semi-annual volumes) to the Digital Logic/Computational IC, Interface IC, Linear IC, Microcomputer, and MSI-LSI Memory D.A.T.A. BOOKS, plus...a one-year subscription (single, annual volume) to the companion Discontinued Integrated Circuits D.A.T.A. BOOK. All six titles—11 volumes. You save 8% from the individual subscription price, if purchased separately.

## Discrete Devices Service

**SAVE 8%**

You receive one-year subscriptions (two semi-annual volumes) to the Transistor, Semiconductor Diode, and Thyristor D.A.T.A. BOOKS, plus...one-year subscriptions (single, annual volume) to the three companion books of discontinued transistors, semiconductor diodes, and thyristors. All six titles—9 volumes. You save 8% from the individual subscription price, if purchased separately.

## WORLDWIDE REPRESENTATIVES OFFER CONVENIENT D.A.T.A.BOOK SERVICE

**International Customers: Send your D.A.T.A.BOOK orders to the D.A.T.A. representative in your country from the list below for fastest service...use local currency.**

### AUSTRALIA, PAPUA NEW GUINEA and NEW ZEALAND

J.H. BOOK SERVICES PTY. LTD.  
Sydney

75 Archer Street (P.O. Box 311)  
Chatswood, N.S.W., Australia 2067  
Telephone: 41 0391 or 419 2386

### Melbourne

Suite 6B, Trak Centre  
445 Toorak Road  
Toorak, VIC. Australia 3142  
Telephone: 24 3398

### BELGIUM, NETHERLANDS and LUXEMBURG

KREISLER IMPORT B.V.  
P.O. Box 2053  
Joan Maetsuyckerstraat 257  
The Hague, Netherlands  
Telephone: 85 65 55

### BRAZIL

PUBLICACOES TECNICAS  
INTERNACIONAIS LTDA  
Rua Peixoto Gomide 209  
02409 Sao Paulo, SP Brazil  
Caixa Postal 1703  
Telephone: (011) 257 1640  
(011) 258 8167

### ENGLAND, SCOTLAND and WALES

LONDON INFORMATION  
(ROWSE MUIR) LIMITED  
Index House  
Ascot, Berkshire, England  
Telephone: 0990-23377

### FRANCE

RADIO TELEVISION FRANCAISE  
73, Ave. Charles DeGaulle  
92202 Neuilly-sur-Seine, France  
Telephone: 747-11-01

### GERMANY

NUCLETRON VERTRIEBS-GMBH  
8 Munchen 50  
Gartnerstrasse 60  
Federal Republic of Germany  
Telephone: (089) 146081-85  
Telex: 5215297  
Cable: NUCLETRON

### ITALY

C.E.T.I.—CENTRO EDIZIONI  
TECNICHE INTERNAZIONALI  
Torino  
Luongo Po Antonelli, 205  
10153 Italy  
Telephone: 896-982  
Milano  
Via Pordenone 17  
20132 Italy  
Telephone: 2152378

### JAPAN and FAR EAST ASIA

DEMPA PUBLICATIONS, INC.  
11-15, Higashi Gotanda 1-chome  
Shinagawa-ku  
Tokyo 141, Japan  
Telephone: (03) 445-6111  
Telex: 02424461 PRSDNP J  
Cable: DEMPASHINBUN TOKYO

### SOUTH AFRICA

ALLIED ELECTRIC (PTY.) LTD.  
Van Dyk Road P.O. Box 6090  
Boksburg East Dunsbart 1508  
South Africa South Africa  
Telephone: 892-1001  
Telex: 43-7823  
Cable: "SOLID STATE" DUNSWART

### SWEDEN, DENMARK, NORWAY FINLAND and ICELAND

INTERNATIONAL PUBLICATIONS  
& DOCUMENTATION SERVICE  
Post Office Box 73  
122 21 Enskede, Sweden  
Telephone: 08/49 96 10  
Cable: INTERSODAS

### SWITZERLAND

CESSA-COMPOSANTS  
ELECTRONIQUES SA  
Place des Charmilles 1  
P.O. Box 105  
1211 Geneva, Switzerland  
Telephone: (022) 44 94949

## THE FAMOUS D.A.T.A. BOOK GUARANTEE

Prove to yourself why over 100,000 professionals rely on D.A.T.A. BOOKS...why this service pays for itself many times over. If, after using them on your job for 30 days, they haven't saved hours of your valuable time...if you don't consider D.A.T.A. BOOKS the quickest, simplest and most accurate way to select components...then return the books in resalable condition for a full refund! - D.A.T.A. Inc.

How Many Subs?	Book Code	D.A.T.A. BOOK DESCRIPTIONS (If your company requires a P.O., please include book code and description.)	Vols Per Year	UNITED STATES	INTERNATIONAL — U.S. DOLLARS Check box for type of shipping preferred		
					SURFACE <input type="checkbox"/> Worldwide	AIR MAIL <input type="checkbox"/> Europe, So. America, Medit. Africa	AIR MAIL <input type="checkbox"/> Asia, Pacific USSR, Africa
	ES	COMPLETE 17-Title D.A.T.A. BOOK Service (10% Savings Included)	29	\$562.95	\$597.90	\$687.30	\$736.45
	DS	6-Title Discrete Device Service (8% Savings Included)	9	171.10	181.95	211.00	226.35
	TR	TRANSISTORS	2	44.00	46.90	54.80	59.20
	DI	SEMICONDUCTOR DIODES	2	50.00	52.90	64.30	70.20
	TY	THYRISTORS	2	35.50	37.80	43.40	46.50
	XT	DISCONTINUED TRANSISTORS	1	20.50	21.30	22.70	23.50
	XR	DISCONTINUED DIODES	1	20.50	21.65	23.30	24.30
	XD	DISCONTINUED THYRISTORS	1	15.50	16.30	17.40	17.55
	CS	6-Title Integrated Circuits Service (8% Savings Included)	11	216.20	230.05	267.00	287.60
	LC	DIGITAL LOGIC/COMPUTATIONAL ICs	2	44.00	46.90	54.75	59.15
	IF	INTERFACE ICs	2	39.00	41.30	48.60	52.50
	LN	LINEAR ICs	2	41.50	44.40	52.30	56.70
	SM	MSI-LSI MEMORIES	2	35.50	37.80	42.80	45.70
	MC	MICROCOMPUTERS	2	54.50	56.80	63.50	67.20
	XC	DISCONTINUED ICs	1	20.50	21.65	23.85	25.15
	OE	OPTOELECTRONICS	2	54.50	56.80	64.10	68.00
	PW	POWER SEMICONDUCTORS	2	48.50	51.40	61.00	66.10
	AN	APPLICATION NOTES REFERENCE	2	28.00	29.60	31.20	32.30
	MW	MICROWAVE TUBES	2	35.00	37.30	40.00	41.80
	RL	RELAYS	1	38.50	39.65	41.85	43.15

Prices are subject to change without notice. Prices shown include discounts calculated on basic subscription price before addition of international delivery charges. PLEASE RETURN THIS CARD WHEN ORDERING.

YES:

B77

Enter my order for D.A.T.A. BOOKS as indicated. (Please print)

NAME \_\_\_\_\_

TITLE \_\_\_\_\_

COMPANY \_\_\_\_\_

ADDRESS \_\_\_\_\_

CITY \_\_\_\_\_

STATE/Zip \_\_\_\_\_

SIGNATURE \_\_\_\_\_

P.O. No. \_\_\_\_\_

DATE \_\_\_\_\_

No. employees at this location \_\_\_\_\_

End product at this location \_\_\_\_\_

Payment Enclosed

(No handling charge - full refund privilege)

Bill

(U.S. orders only - Add handling charge of \$2.00 PER SUBSCRIPTION to above prices)

International Subscribers: In countries where no representative is named, send order with payment to D.A.T.A. in U.S.A.

How Many Subs?	Book Code	D.A.T.A. BOOK DESCRIPTIONS (If your company requires a P.O., please include book code and description.)	Vols Per Year	UNITED STATES	INTERNATIONAL — U.S. DOLLARS Check box for type of shipping preferred		
					SURFACE <input type="checkbox"/> Worldwide	AIR MAIL <input type="checkbox"/> Europe, So. America, Medit. Africa	AIR MAIL <input type="checkbox"/> Asia, Pacific USSR, Africa
	ES	COMPLETE 17-Title D.A.T.A. BOOK Service (10% Savings Included)	29	\$562.95	\$597.90	\$687.30	\$736.45
	DS	6-Title Discrete Device Service (8% Savings Included)	9	171.10	181.95	211.00	226.35
	TR	TRANSISTORS	2	44.00	46.90	54.80	59.20
	DI	SEMICONDUCTOR DIODES	2	50.00	52.90	64.30	70.20
	TY	THYRISTORS	2	35.50	37.80	43.40	46.50
	XT	DISCONTINUED TRANSISTORS	1	20.50	21.30	22.70	23.50
	XR	DISCONTINUED DIODES	1	20.50	21.65	23.30	24.30
	XD	DISCONTINUED THYRISTORS	1	15.50	16.30	17.40	17.55
	CS	6-Title Integrated Circuits Service (8% Savings Included)	11	216.20	230.05	267.00	287.60
	LC	DIGITAL LOGIC/COMPUTATIONAL ICs	2	44.00	46.90	54.75	59.15
	IF	INTERFACE ICs	2	39.00	41.30	48.60	52.50
	LN	LINEAR ICs	2	41.50	44.40	52.30	56.70
	SM	MSI-LSI MEMORIES	2	35.50	37.80	42.80	45.70
	MC	MICROCOMPUTERS	2	54.50	56.80	63.50	67.20
	XC	DISCONTINUED ICs	1	20.50	21.65	23.85	25.15
	OE	OPTOELECTRONICS	2	54.50	56.80	64.10	68.00
	PW	POWER SEMICONDUCTORS	2	48.50	51.40	61.00	66.10
	AN	APPLICATION NOTES REFERENCE	2	28.00	29.60	31.20	32.30
	MW	MICROWAVE TUBES	2	35.00	37.30	40.00	41.80
	RL	RELAYS	1	38.50	39.65	41.85	43.15

Prices are subject to change without notice. Prices shown include discounts calculated on basic subscription price before addition of international delivery charges. PLEASE RETURN THIS CARD WHEN ORDERING.

YES:

B77

Enter my order for D.A.T.A. BOOKS as indicated. (Please print)

NAME \_\_\_\_\_

TITLE \_\_\_\_\_

COMPANY \_\_\_\_\_

ADDRESS \_\_\_\_\_

CITY \_\_\_\_\_

STATE/Zip \_\_\_\_\_

SIGNATURE \_\_\_\_\_

P.O. No. \_\_\_\_\_

DATE \_\_\_\_\_

No. employees at this location \_\_\_\_\_

End product at this location \_\_\_\_\_

Payment Enclosed

(No handling charge - full refund privilege)

Bill

(U.S. orders only - Add handling charge of \$2.00 PER SUBSCRIPTION to above prices)

International Subscribers: In countries where no representative is named, send order with payment to D.A.T.A. in U.S.A.

For fastest international service, order D.A.T.A.BOOKS direct from the D.A.T.A. Representative serving your country. See address and telephone number listed on preceding page.

---

FIRST CLASS  
PERMIT NO. 49  
PINE BROOK, N.J.

---

**BUSINESS REPLY MAIL** No postage stamp necessary if mailed in United States

---

Postage will be paid by:

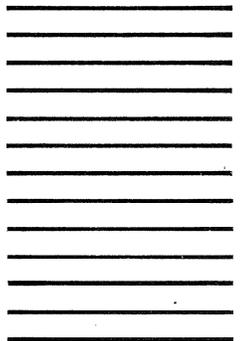
**D.A.T.A., INC.**

A Cordura Company

45 U.S. Highway 46

P.O. Box 602

Pine Brook, New Jersey 07058



---

FIRST CLASS  
PERMIT NO. 49  
PINE BROOK, N.J.

---

**BUSINESS REPLY MAIL** No postage stamp necessary if mailed in United States

---

Postage will be paid by:

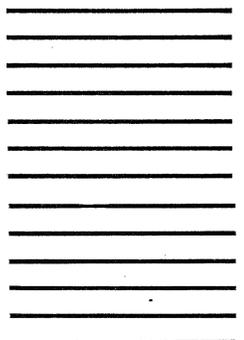
**D.A.T.A., INC.**

A Cordura Company

45 U.S. Highway 46

P.O. Box 602

Pine Brook, New Jersey 07058



# D.A.T.A., INC.

A Cordura Company

45 U.S. Highway 46

P.O. Box 602

Pine Brook, New Jersey 07058

Tel.: (201) 227-3740 TWX: 710-734-4339

## PUBLISHER

E. H. Jacobs, Vice President  
*Susan MacPeck, Staff Coordinator*

## EDITOR

E. Hugh Marriott, Operations Manager

## ENGINEERING / PRODUCTION

Jerry D'Allegro, Senior Engineer  
*Valdora Adams, Valeria Arnold, Ted Carides,  
Nettie Carramao, Chuck Chakravarty, Val DeGeiso,  
Isabelle Monk, Mary Stier, Raymond XBryant*

## GRAPHICS

Clint Leinweber, Manager  
*Ellen Gilligan, Evelyn Muller*

## DATA PROCESSING

Jerry Cohen, Manager  
*Patricia Bogart, Patti Hahn, Gwen Mitchell,  
Gail Sullivan*

## ACCOUNTING & FULFILLMENT

Steve Degenhardt, Controller  
*Irene Boykewich, Sid Gittler,  
Diane Kearns, Sherry Knarr, Cynthia McDonnell*

## PURCHASING & SERVICES

Paul Ehrlich, Manager  
*Doris Gerry, Gregory Uzzoloni*

## MARKETING

Harvey Sanderson, Director  
*Louise Otten, Joan Pirozzi*

## CUSTOMER SERVICES

Pat McGill

## ADVERTISING SALES

Home Office: Geraldine Purdy, Manager  
(201) 227-3740

D.A.T.A., Inc. is a subsidiary of CORDURA PUBLICATIONS, INC., 1200 Prospect Street, La Jolla, CA 92037  
President — Cal Kobrin  
Vice President and Publisher — E. H. Jacobs  
Vice President, Finance — John Opelt  
Vice President, Operations — Malcolm Ferrier  
Director of Marketing — Jim Rosenfield  
Director of Data Processing — Fred Lepow

Electronics Information Series D.A.T.A. BOOKS are published 29 times a year by Derivation and Tabulation Associates, Inc., A Cordura Company, 45 U.S. Highway 46, Pine Brook, N.J. 07058. SERIES subscription price is \$562.95 a year in the U.S.A.

MSI-LSI MEMORY Editions are published in April and October; \$35.50 annually (two editions) in the U.S.A. See Order Card for international prices. Application to mail at controlled circulation rates pending at Kingsport, TN 37662.

Change of Address: When sending change of address, please include old address; preferably, the label from the latest edition.

COPYRIGHT © 1977 by Derivation and Tabulation Associates, Inc., all rights reserved. Reproduction in whole or in part, without written permission, is prohibited.

# MSI/LSI SEMICONDUCTOR MEMORIES

13th Edition

1977 Volume 2

## TABLE OF CONTENTS

### HOW TO USE INFORMATION

How To Make Maximum Use Of This D.A.T.A. BOOK .....	iii
Use Of Powers-Of-Ten Multipliers and Symbols & Codes	
In The Technical Sections .....	iv
How Type Numbers Are Sequenced In The Type No. Cross Index ....	iv
How Type Numbers Are Arranged In The Technical Section —	
Sequencing Parameters .....	v
D.A.T.A.'s Approach To MSI-LSI Memory Specifications .....	vi - viii

### TYPE NO. CROSS INDEX

1. All Types .....	2 - 15
--------------------	--------

### TECHNICAL SECTIONS

2. Read-Write Memories (RAM'S) .....	16 - 30
3. Read-Only Memories (ROM'S) .....	31 - 42
4. Character Generators .....	43 - 44
5. Content Addressable Memories (CAM'S) — See Section 20	
6. Code Converters .....	45 - 46
7. Shift Registers.....	47 - 79
8-19. (Reserved)	
20. Special Memory Devices .....	80
(Including Trigonometric ROM'S-ATN, COS, SCN, SIN; CAM'S; PLA'S; Rhythm-Generators; Transistor Arrays)	

### SUPPLEMENTARY SECTIONS

21. Types With U.S. Military Specifications .....	81 - 85
21A. Commercial-To-Military Type No. Cross Index .....	86
22. Logic/Block Drawings .....	87 - 315
23. Outline Drawings.....	316 - 375
24. Manufacturers Local Offices .....	376 - 390
25. Manufacturer Codes, Names and Addresses .....	391 - 392
26. Manufacturers Logos .....	393 - 396

INTERPRETER — Symbols & Codes Explained .....	See Cards Back of Book
---	---------------------------

# EDITORIAL POLICY & PROCEDURES

<b>Purpose</b>	This D.A.T.A.BOOK is designed to report comprehensively on what is presently being produced throughout the world in the field of MSI-LSI MEMORIES. While a book such as this cannot provide 100% of the information you might need, its primary aims are those of facilitating the selection of types suitable to your technical requirements, and of directing you to the sources of their manufacture.
<b>Technical Data Acquisition</b>	D.A.T.A. acquires and processes the information presented in this D.A.T.A.BOOK with the cooperation of the participating manufacturers who supply us with their latest technical information. Manufacturers are not charged for the listing of their products.
<b>JEDEC Outlines</b>	At the time this D.A.T.A.BOOK was prepared, there were no JEDEC type numbers; however, some of the devices have the JEDEC-designated MO- and TO- outlines which are included as applicable in the Outline Drawing Section.
<b>Military Type Numbers</b>	The electrical, mechanical and environmental information tabulated for the military types in the technical sections is derived directly from the applicable military specifications and standards. The source information, showing the particular manufacturers qualified for each type, is derived from the QPL (Qualified Parts List) associated with the governing specification, or from the manufacturers Qualification Test Letters.
<b>Substitute Types And Compatibility</b>	This D.A.T.A.BOOK can not truly claim to be an interchangeability chart; however, because of the sequencing arrangement of selected characteristics in the technical sections, types with the same or similar characteristics are grouped together. For purposes of replacement, this means of thorough, convenient technical comparison should prove superior to, and safer than, a mere listing of possible substitute type numbers.
<b>Price And Availability</b>	Because of the rapidly-changing and complex nature of this field, current price and delivery information should be obtained direct from the manufacturers. The list of manufacturers and the Local Offices Section in back of the book will assist you in this.
<b>Manufacturers' Specifications</b>	This book includes currently-manufactured devices with their major characteristics, drawings and manufacturers. Every effort is made to ensure the accuracy of the entries herein; however, the publisher can not be held responsible nor guarantee against the possibility of error or omission. Only the manufacturers or their authorized representatives can provide you with complete technical details.

# HOW TO MAKE MAXIMUM USE OF THIS D.A.T.A.BOOK

To make maximum use of this D.A.T.A.BOOK, select the particular known-unknown situation below that defines your problem, and follow the instructions as indicated.

1	<p>KNOWN: Electrical and Mechanical Requirements            UNKNOWN: Suitable Type Numbers</p> <ol style="list-style-type: none"> <li>Turn to the Table of Contents (first page) and select the technical data section corresponding to the subject device type.</li> <li>Turn to any page in the selected section. Note the sequencing parameters (those characteristics for which the data is arranged in order) indicated at the top corner of the page.</li> <li>Using the sequencing parameters, locate the type numbers that are in general agreement with your requirements. (Because of the sequencing arrangement, these types will appear together, in groups and sub-groups.) From among these, select the one or ones most suitable.</li> <li>To identify the manufacturer of the selected type number(s), follow the instructions in Block 2 below.</li> </ol>
2	<p>KNOWN: Type Numbers (SN5494J, 10139F)            UNKNOWN: Manufacturer(s), Address, Local Offices</p> <ol style="list-style-type: none"> <li>Turn to Type No. Cross-Index (Section 1) and locate the subject type number. (Refer to 'HOW TYPE NUMBERS ARE SEQUENCED' in front of the book as a guide for this.)</li> <li>Note the 3- or 4-letter manufacturer's code(s), e.g., TII, MULB indicated for each of the subject types.</li> <li>Use the listing of 'MANUFACTURERS &amp; THEIR CODES' in back of the book to identify the codes. (Note: Local Offices for manufacturers shown in bold print on this listing are indicated in a special section in back of the book.)</li> </ol>
3	<p>KNOWN: Type Number (SN5494J)            UNKNOWN: Its Electrical Characteristics, And/Or Logic And Outline Drawings</p> <ol style="list-style-type: none"> <li>Turn to Type No. Cross-Index and locate the subject type number.</li> <li>Note the page and line number, e.g., 59-75, alongside the type number.</li> <li>Locate the type number as noted, in the technical sections. (Note: Along with the electrical and performance characteristics listed for each type number are references to the logic/block and outline drawings, located in Section 22 and Section 23 respectively.)</li> </ol>
4	<p>KNOWN: Type Number            UNKNOWN: Equivalent Types for Replacement</p> <ol style="list-style-type: none"> <li>Follow the instructions in Block 3 above.</li> <li>Survey the type numbers surrounding the subject number to determine the suitable alternatives.</li> </ol>
5	<p>KNOWN: Military Requirements            UNKNOWN: Suitable Type Number(s)</p> <ol style="list-style-type: none"> <li>Scan the military type numbers in Section 21 (TYPES WITH MILITARY SPECIFICATIONS) to determine the military identifying prefix(es); e.g., M38510.</li> <li>Follow the instructions in Block 1, to determine the general type numbers that meet the military requirements. From among these, select the military types by means of the identifying prefix.</li> <li>To identify the manufacturers, follow the instructions in Block 6.</li> </ol>
6	<p>KNOWN: Military Type Number            UNKNOWN: Qualified Manufacturers And/Or Applicable Military Standard Or Specification</p> <ol style="list-style-type: none"> <li>Turn to Section 21 (TYPES WITH U.S. MILITARY SPECIFICATIONS), and locate the subject type number. (Type numbers are arranged in alpha-numeric order, by individual specifications as indicated in the column headings.)</li> <li>Note the manufacturer's code(s) listed alongside the type number.</li> <li>Follow the instructions in Block 1 to identify the manufacturers.</li> </ol>
7	<p>KNOWN: Type Number Not Included In Book            UNKNOWN: What Happened To It?</p> <ol style="list-style-type: none"> <li>Consult D.A.T.A.BOOK OF DISCONTINUED INTEGRATED CIRCUITS.</li> </ol>

# USE OF POWERS-OF-TEN MULTIPLIERS AND SYMBOLS & CODES IN THE TECHNICAL SECTIONS

To present a maximum amount of information in a minimum amount of space, use is made in this book of the following data modifiers:

## POWERS-OF-TEN MULTIPLIERS

The powers-of-ten multipliers shown below are used in numeric columns when the value being entered is many times greater or smaller than the units of measure indicated in the column heading. Usually, the latter are the so-called 'basic' units; such as V (volts), A (amperes) and s (seconds). The multipliers and an explanation of their use are given below:

MULTIPLIERS									EXPLANATION		
PREFIXES & SYMBOLS			Recommended by International Committee on Weights and Measures						Value of Data To Be Entered	Basic Unit In Column Heading	Actual Entry
Indicating Powers of Ten			Adopted by National Bureau of Standards								
Power	Prefix	Symbol	Power	Prefix	Symbol	Power	Prefix	Symbol			
10 <sup>12</sup>	tera	T	10	deka	da	10 <sup>-9</sup>	nano	n	3 milliamperes	A (amperes)	3.0m
10 <sup>9</sup>	giga	G	10 <sup>-1</sup>	deci	d	10 <sup>-12</sup>	pico	p	9 megaohms	Ω (ohms)	9.0M
10 <sup>6</sup>	mega	M	10 <sup>-2</sup>	centi	c	10 <sup>-15</sup>	femto	f	0.5 volt	V (volts)	500m *
10 <sup>3</sup>	kilo	k	10 <sup>-3</sup>	milli	m	10 <sup>-18</sup>	atto	a	10 amperes	A (amperes)	10
10 <sup>2</sup>	hecto	h	10 <sup>-6</sup>	micro	μ				* May also be written as 0.5, with no multiplier		

## SYMBOLS & CODES

**Symbols** — Symbols such as #, ^, and \$ are used in all columns, numeric or otherwise, whenever the data entries differ in some way from the entity defined in the column heading. For instance, if a given heading specifies Max. Power (in Watts) and the numeric value being entered for a given type represents the minimum power instead, the variance is denoted by the appearance of a special symbol alongside the numeric entry.

NOTE: The symbols and codes used herein are explained on the cards in back of the book.

**Codes** — Codes are used in some columns as means to abbreviate the data being entered. The codes may be alphabetic (A,B,C, etc.) numeric (1,2,3, etc.) or some combination of both.

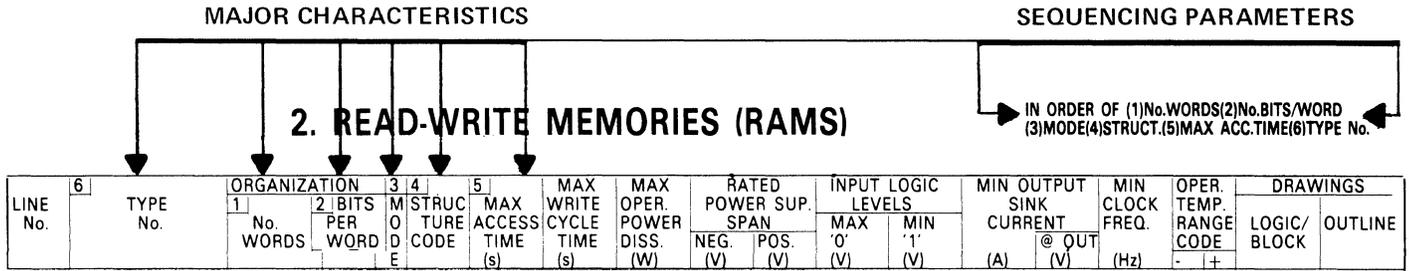
# HOW TYPE NUMBERS ARE SEQUENCED IN THE TYPE NUMBER CROSS INDEX

Sequencing of type numbers in the Type Number Cross-Index is governed by the following rules:

	EXAMPLES
<b>Rules:</b> 1) Type numbers are listed in numeric-alphabetic sequence; i.e., type numbers beginning with a number (decimal, fraction, or whole) precede type numbers beginning with a letter.	13A01 143 1202 A147 AN127 B2000
2) Decimals and fractions precede whole numbers. An equivalent decimal precedes the fraction when the remainder of type number is identical.	.25Z150 1/4Z150 3/4M12Z 1T3
3) Zeros are ignored in sequencing except when the zero is the only basis for distinguishing one type number from another. In this case the type number containing the zero is listed first.	0112 112 0113 00115 AP01 AP1 AP02
4) Number and/or letter groupings preceding hyphens or slashes are the controlling factors in sequencing. The hyphens and slashes themselves precede any identically positioned letters also having the same beginning number/letter groupings.	66-0706 66M1 70/10 70A9

# HOW TYPE NOS. ARE ARRANGED IN THE TECHNICAL SECTION – SEQUENCING PARAMETERS

The arrangement of types in the technical sections is keyed to a set of special characteristics selected for their importance from among the general group of characteristics tabulated in each section. These selected characteristics, or sequencing parameters, differ from one section to another, and are identified at the top corner of each page, as shown in the sample below.



The different types within a section are first arranged in ascending numeric (or alphabetic) order of the first such parameter. Groups of types having a common value for the first parameter are then arranged in ascending order of the second parameter. This process continues for each parameter in turn, up to and including the last parameter which, in every instance, is the type number itself. The final arrangement, by type number, is done in accordance with the sequencing of type numbers in the cross-index, as explained on the preceding page.

A simplified model of the arrangement as described is shown below.

4 Type Number	Characteristics			
	1 A	2 B	C	3 D
A13	100		325	
A4	100		1000	20
A9	100	A	20	25
A10	100	A	200	25
A3	100	B	40	15
A1	100	C	80	10
A8	100	C	900	15
A7	100	D	35	30
A11	110	A	60	25
A2	120	A	300	15
A5	120	B	150	20
A6	120	B	200	20
A12	120	B	475	25

Last Seq. Par.
1st Seq. Par.
2nd Seq. Par.
(Not Seq.)
3rd Seq. Par.

Note that the absence of an entry for any sequencing parameter is regarded as a zero, and precedes any actual entries in the sequencing.

# D.A.T.A.'S APPROACH TO MSI/LSI MEMORY SPECIFICATIONS

The MSI/LSI Memory industry is still in its infancy. It has not as yet matured to the point whereby standardized data presentation or standardized definitions have evolved. In general the data supplied by one manufacturer is difficult, if not impossible, to compare with that of another manufacturer.

It is for the above reasons that D.A.T.A. has prepared all manufacturers data in D.A.T.A.'s "Standardized Format". The "Standardized Format" presents a given set of device characteristics in a sequential order of either ascending or descending values. This orderly presentation now allows the reader to select one or more MSI/LSI Memory circuits from a group of devices which satisfies the reader's specification requirements. The "Standardized Format" presentation permits the reader to analyze, select and trade between groups of devices available from all known manufacturers.

The characteristics for MSI/LSI Memory presented in the D.A.T.A.BOOK are generally specified under "worst case" conditions. Thus, the minimum or maximum values (whichever is applicable) is recorded instead of the typical value. When only the typical value is available, it is accompanied by a symbol to alert the reader to exercise caution in interpreting the data.

What follows is, from D.A.T.A.'s point of view, the technical reasoning and guidelines used in the preparation of the technical sections of the MSI/LSI Memory D.A.T.A.BOOK. We would certainly welcome your comment's about this, or any other D.A.T.A.BOOK.

- Access Time (Sect. 2,3,4,5,6) – the speed at which a device can read-out information from its memory. It is defined as the time difference between the addressing of the memory and the appearance of a valid output.
- Clock Freq. (Sect 2,3,7) – is given for dynamic devices for the reason explained under "modes". For static devices it is assumed to be dc. In Section 3 it is specified in the Description column when applicable.
- Conversion Code: "From" and "To" (Sect. 6)– indicates the input and output codes of the device. Those devices having reversible code capability are listed twice, once for each direction change.
- Input Logic Levels (All sections) – the max. input voltage at which the input is in the "off" or "0" state, and the min. input voltage at which the input is in the "on" or "1" state. The difference between the two input levels indicates the relative noise immunity of the device. For cases where the input logic levels are not specified, the output levels are, and a symbol is used to indicate this condition.

The input logic levels apply to the address inputs for the "memory" devices in Sections 2 to 6, and to the data inputs for shift registers in Section 7. If the device can be adjusted for compatibility with both MOS and Bipolar systems, then Bipolar levels are specified in the column. A symbol is used to indicate this condition.

- Logic/Block Drawings (All sections) – separated and coded according to functional classification, i.e., RAM's, ROM's, etc. The block drawing was considered more descriptive than the circuit schematic in showing the overall operation of the device from a system aspect.
- Mode: Static or Dynamic (Sect. 2 and 5) – represents the basic storage method of the device. Static types use flip-flops that retain their state indefinitely as long as the supply voltage is maintained. Their frequency of operation extends down to dc. Dynamic types use the inherent interelectrode capacitance of MOS devices to store a charge which determines the state of a memory bit. Since this charge cannot be held indefinitely, these types must be refreshed periodically; this restricts the lower limit of their operating frequency.
- No. of Bits Per Character (Sect. 4) – the number of bits in the display matrix, obtained by multiplying the number of rows in the display matrix times the number of columns. The number provides a measure of the resolution of the display.

## D.A.T.A.'S APPROACH TO MSI/LSI MEMORY SPECIFICATIONS (Cont'd)

- No. of Bits Per Register (Sect. 7) – the bit capacity of the individual registers which can be used separately in the device. If the number of bits varies for the different registers, then the highest capacity is specified, and the user is referred to the logic/block drawing for more specific information.
- No. of Characters (Sect 4) – the character capacity of the device. For a standard code with a given number of characters such as ASCII, the input code for any character is fixed. Some devices must be used in pairs to supply the complete code, in which case a symbol is used in the technical section column.
- No. of Code Inputs and Outputs (Sect. 6) – determines the code input and output character capacity.
- No. of Outputs (Sect. 4) – indicates the number of outputs to the display. For row-and-column-scanning devices, the array of the display matrix is easily determined by dividing this number into the number of bits per character.
- No. of Registers (Sect. 7) – together with the number of bits/register determine the total bit capacity of the device. Some registers contain inputs to intermediate stages; this is noted with a symbol.
- Oper. Mode and Prog. Code (Sect 3) – describes: 1)the mode of operation of the device (dynamic or static); and 2)the type of program available (standard or custom). If a standard program is indicated in the Code, it is defined in the technical section Description column.
- Operating Power Diss. (All sections) – the “worst-case” power dissipation of the device under operating conditions. A manufacturer may indicate the “quiescent” or the “absolute maximum” power dissipations; these values vary significantly from “worst case”. For this reason the user is cautioned not to use the quiescent or absolute maximum power dissipation in comparing the operating power dissipation of different devices. All conditions other than “worst case” are distinguished by the use of a symbol following the value.
- Oper. Temp. Range Code (All sections) – the temperature range over which the manufacturer indicates that the device will operate. Unless otherwise noted by a symbol in the appropriate column or columns, all specified characteristics apply over the operating temperature range of the device.
- Organization: No. of Words and No. Bits/Word (Sect. 2,3,5) – represents the capacity of the memory. By connecting the outputs of two or more devices in parallel, the total number of words may be expanded; similarly, by connecting the address inputs in parallel, the number of bits/word can be expanded.
- Outline Drawings (All sections) – separated and coded in the Outline Drawing Section according to package configuration. In this way the user can easily determine the types of package and the associated dimensions available for memory circuits.
- Output Sink Current (All sections) –negative current that the output of a device can sink at a specified “0” level. This is especially important in determining a device’s compatibility to Bipolar circuits. Where the sink current is not specified or not applicable, an alternate output current characteristic is specified, and is identified by an appropriate symbol.
- Propagation Delay (Sect. 7) – the time required to shift information one bit through the register. It is defined as the time between the initiating clock pulse at the input of a storage element, to the occurrence of a valid output from the same element.
- Rated Power Supply Span (All sections) – the range of positive and negative supply voltages at which the characteristics are specified by the manufacturer. If more than one negative or positive voltage is necessary for the operation of the device, the maximum negative or positive value is specified. The logic/block drawing should then be consulted for the actual voltages required to operate the device.

# D.A.T.A.'S APPROACH TO MSI/LSI MEMORY SPECIFICATIONS (Cont'd)

- Search Time (Sect. 5) – the time required to match information in the memory once a search is initiated. It is defined as the time difference between the enabling of the associate control input and the receiving of a mismatch or match condition at the output.
- Structure Code (All sections) – relates the device to the two main developing semiconductor technologies for memory devices: Bipolar and MOS. Presently, the two technologies are characterised by distinctive advantages and disadvantages in comparison to each other. Generally, Bipolar devices are faster, but MOS devices dissipate less power. A third technology, thin film amorphous, features non-volatile and non-destructive crystalline memory bits.
- Use Code (Sect. 4) – describes 1) the mode of operation of the device (dynamic or static); 2) the type of code stored in the device; and 3) the type of display used with the device. Knowing these three characteristics greatly narrows the search for a character generator.
- Use Code (Sect. 7) – describes 1) the type (serial or parallel) of input and output terminals available on the device; and 2) the operating mode of the device (static or dynamic). For devices that operate in both the serial and parallel modes on the input and/or output, the parallel capability is specified, since parallel devices can operate in both the serial and parallel modes.
- Worst Case Operating Frequency (Sect. 7) – the highest guaranteed operating frequency of the device. Alternatively, the data-rate frequency is specified in the case where it differs from the clock frequency.
- Write-Cycle Time (Sect. 2,5) – the time required to write a data-word into a given memory address. The max. read-write time is similarly defined, except that it includes both the read and write portions of the cycle. The min. write-pulse width is specified in the column when the cycle times are not given. It is defined as the min. pulse width required at the write input to insure that valid information is stored at the memory address.













# 1. TYPE No. CROSS INDEX

IN TYPE NUMBER SEQUENCE

TYPE No.	MFRS	Pg&Line	TYPE No.	MFRS	Pg&Line									
JANM38510/05704CFA	NSC	66-53	JANM38510/07602AEA	none	58-91	JANM38510/20101AKA	none	32-5	JANM38510/20201BEA	none	35-2	JANM38510/23502BVA	none	28-24
JANM38510/05704CFB	NSC	66-54	JANM38510/07602AEB	none	59-1	JANM38510/20101AKB	none	32-6	JANM38510/20201BEB	none	35-3	JANM38510/23502BVB	none	28-25
JANM38510/05704CFC	NSC	66-55	JANM38510/07602AEC	none	59-2	JANM38510/20101AKC	none	32-7	JANM38510/20201BEC	none	35-4	JANM38510/23502BVC	TII	28-26
JANM38510/05705AEA	none	75-32	JANM38510/07602AFA	none	59-3	JANM38510/20101AZA	none	32-8	JANM38510/20201BFA	none	35-5	JANM38510/23502CUA	none	28-27
JANM38510/05705AEB	none	75-33	JANM38510/07602AFB	none	59-4	JANM38510/20101AZB	none	32-9	JANM38510/20201BFB	none	35-6	JANM38510/23502CUB	none	28-28
JANM38510/05705AEC	none	75-34	JANM38510/07602AFC	none	59-5	JANM38510/20101AZC	none	32-10	JANM38510/20201BFC	none	35-7	JANM38510/23502CUC	none	28-29
JANM38510/05705AFA	none	75-35	JANM38510/07602BEA	none	59-6	JANM38510/20101BJA	none	32-11	JANM38510/20201CEA	none	35-8	JANM38510/23502CVA	none	28-30
JANM38510/05705AFB	none	75-36	JANM38510/07602BEB	none	59-7	JANM38510/20101BJB	HAS	32-12	JANM38510/20201CEB	none	35-9	JANM38510/23502CVB	none	28-31
JANM38510/05705AFC	none	75-37	JANM38510/07602BEC	none	59-8	JANM38510/20101BJC	none	32-13	JANM38510/20201CEC	none	35-10	JANM38510/23502CVC	TII	28-32
JANM38510/05705BEA	NSC	75-38	JANM38510/07602BFA	none	59-9	JANM38510/20101BKA	none	32-14	JANM38510/20201CFA	none	35-11	JANM38510/23503AUA	none	28-33
JANM38510/05705BEB	NSC	75-39	JANM38510/07602BFB	none	59-10	JANM38510/20101BKB	none	32-15	JANM38510/20201CFB	none	35-12	JANM38510/23503AUB	none	28-34
JANM38510/05705BEC	NSC	75-40	JANM38510/07602BFC	none	59-11	JANM38510/20101BKC	none	32-16	JANM38510/20201CFC	none	35-13	JANM38510/23503AUC	none	28-35
JANM38510/05705BFA	NSC	75-41	JANM38510/07602CEA	none	59-12	JANM38510/20101BZA	none	32-17	JANM38510/20202AEA	none	35-14	JANM38510/23503AWA	none	28-36
JANM38510/05705BFB	NSC	75-42	JANM38510/07602CEB	none	59-13	JANM38510/20101BZB	none	32-18	JANM38510/20202AEB	none	35-15	JANM38510/23503AWB	none	28-37
JANM38510/05705BFC	NSC	75-43	JANM38510/07602CEC	none	59-14	JANM38510/20101BZC	none	32-19	JANM38510/20202AEC	none	35-16	JANM38510/23503AWC	none	28-38
JANM38510/05705CEA	NSC	75-44	JANM38510/07602CFA	none	59-15	JANM38510/20101CJA	none	32-20	JANM38510/20202AFA	none	35-17	JANM38510/23503BUA	none	28-39
JANM38510/05705CEB	NSC	75-45	JANM38510/07602CFB	none	59-16	JANM38510/20101CJB	HAS	32-21	JANM38510/20202AFB	none	35-18	JANM38510/23503BUB	none	28-40
JANM38510/05705CEC	NSC	75-46	JANM38510/07602CFC	none	59-17	JANM38510/20101CJC	none	32-22	JANM38510/20202AFC	none	35-19	JANM38510/23503BUC	none	28-41
JANM38510/05705CFA	NSC	75-47	JANM38510/15901AEA	none	50-16	JANM38510/20101CKA	none	32-23	JANM38510/20202BEA	none	35-20	JANM38510/23503BWA	none	28-42
JANM38510/05705CFB	NSC	75-48	JANM38510/15901AEB	none	50-17	JANM38510/20101CKB	none	32-24	JANM38510/20202BEB	none	35-21	JANM38510/23503BWB	none	28-43
JANM38510/05705CFC	NSC	75-49	JANM38510/15901AEC	none	50-18	JANM38510/20101CKC	none	32-25	JANM38510/20202BEC	none	35-22	JANM38510/23503BWC	none	28-44
JANM38510/05706AJA	none	64-53	JANM38510/15901AFA	none	50-19	JANM38510/20101CZA	none	32-26	JANM38510/20202BFA	none	35-23	JANM38510/23503CUA	none	28-45
JANM38510/05706AJB	none	64-54	JANM38510/15901AFB	none	50-20	JANM38510/20101CZB	none	32-27	JANM38510/20202BFB	none	35-24	JANM38510/23503CUB	none	28-46
JANM38510/05706AJC	none	64-55	JANM38510/15901AFC	none	50-21	JANM38510/20101CZC	none	32-28	JANM38510/20202BFC	none	35-25	JANM38510/23503CUC	none	28-47
JANM38510/05706AKA	none	64-56	JANM38510/15901BEA	none	50-22	JANM38510/20102AJA	none	32-29	JANM38510/20202CEA	none	35-26	JANM38510/23503CWA	none	28-48
JANM38510/05706AKB	none	64-57	JANM38510/15901BEB	none	50-23	JANM38510/20102AJB	none	32-30	JANM38510/20202CEB	none	35-27	JANM38510/23503CWB	none	28-49
JANM38510/05706AKC	none	64-58	JANM38510/15901BEC	none	50-24	JANM38510/20102AJC	none	32-31	JANM38510/20202CEC	none	35-28	JANM38510/23503CWC	none	28-50
JANM38510/05706BJA	none	64-59	JANM38510/15901BFA	none	50-25	JANM38510/20102AKA	none	32-32	JANM38510/20202CFA	none	35-29	JANM38510/23504AUA	none	28-51
JANM38510/05706BJB	none	64-60	JANM38510/15901BFB	none	50-26	JANM38510/20102AKB	none	32-33	JANM38510/20202CFB	none	35-30	JANM38510/23504AUB	none	28-52
JANM38510/05706BJC	none	64-61	JANM38510/15901BFC	none	50-27	JANM38510/20102AKC	none	32-34	JANM38510/20202CFC	none	35-31	JANM38510/23504AUC	none	28-53
JANM38510/05706BKA	none	64-62	JANM38510/15901CEA	none	50-28	JANM38510/20102AZA	none	32-35	JANM38510/23501AUA	none	27-102	JANM38510/23504AVA	none	28-54
JANM38510/05706BKB	none	64-63	JANM38510/15901CEB	none	50-29	JANM38510/20102AZB	none	32-36	JANM38510/23501AUB	none	27-103	JANM38510/23504AVB	none	28-55
JANM38510/05706BKC	none	64-64	JANM38510/15901CEC	none	50-30	JANM38510/20102AZC	none	32-37	JANM38510/23501AUC	none	27-104	JANM38510/23504AVC	none	29-1
JANM38510/05706CJA	none	64-65	JANM38510/15901CFA	none	50-31	JANM38510/20102BJA	none	32-38	JANM38510/23501AWA	none	27-105	JANM38510/23504ABUA	none	29-2
JANM38510/05706CJB	none	64-66	JANM38510/15901CFB	none	50-32	JANM38510/20102BJB	none	32-39	JANM38510/23501AWB	none	28-1	JANM38510/23504ABUB	none	29-3
JANM38510/05706CJC	none	64-67	JANM38510/15901CFC	none	50-33	JANM38510/20102BJC	none	32-40	JANM38510/23501AWC	none	28-2	JANM38510/23504ABUC	none	29-4
JANM38510/05706CKA	none	64-68	JANM38510/15902AEA	none	73-41	JANM38510/20102BKA	none	32-41	JANM38510/23501BUA	none	28-3	JANM38510/23504ABVA	none	29-5
JANM38510/05706CKB	none	64-69	JANM38510/15902AEB	none	73-42	JANM38510/20102BKB	none	32-42	JANM38510/23501BUB	none	28-4	JANM38510/23504ABVB	none	29-6
JANM38510/05706CKC	none	64-70	JANM38510/15902AEC	none	73-43	JANM38510/20102BKC	none	32-43	JANM38510/23501BUC	none	28-5	JANM38510/23504ABVC	none	29-7
JANM38510/07601AEA	none	58-73	JANM38510/15902AFA	none	73-44	JANM38510/20102BZA	none	32-44	JANM38510/23501BWA	none	28-6	JANM38510/23504ACUA	none	29-8
JANM38510/07601AEB	none	58-74	JANM38510/15902AFB	none	73-45	JANM38510/20102BZB	none	32-45	JANM38510/23501BWB	none	28-7	JANM38510/23504ACUB	none	29-9
JANM38510/07601AEC	none	58-75	JANM38510/15902AFC	none	73-46	JANM38510/20102BZC	none	32-46	JANM38510/23501BWC	TII	28-8	JANM38510/23504ACUC	none	29-10
JANM38510/07601AFA	none	58-76	JANM38510/15902BEA	none	73-47	JANM38510/20102CJA	none	32-47	JANM38510/23501CUA	none	28-9	JANM38510/23504ACVA	none	29-11
JANM38510/07601AFB	none	58-77	JANM38510/15902BEB	none	73-48	JANM38510/20102CJB	none	32-48	JANM38510/23501CUB	none	28-10	JANM38510/23504ACVB	none	29-12
JANM38510/07601AFC	none	58-78	JANM38510/15902BEC	none	73-49	JANM38510/20102CJC	none	32-49	JANM38510/23501CUC	none	28-11	JANM38510/23504ACVC	none	29-13
JANM38510/07601BEA	none	58-79	JANM38510/15902BFA	none	73-50	JANM38510/20102CKA	none	32-50	JANM38510/23501CWA	none	28-12	JANM38510/30106AEA	none	63-39
JANM38510/07601BEB	none	58-80	JANM38510/15902BFB	none	73-51	JANM38510/20102CKB	none	32-51	JANM38510/23501CWB	none	28-13	JANM38510/30106AEB	none	63-40
JANM38510/07601BEC	none	58-81	JANM38510/15902BFC	none	73-52	JANM38510/20102CKC	none	32-52	JANM38510/23501CWC	TII	28-14	JANM38510/30106AEC	none	63-41
JANM38510/07601BFA	none	58-82	JANM38510/15902CEA	none	73-53	JANM38510/20102CZA	none	32-53	JANM38510/23502AUA	none	28-15	JANM38510/30106AFA	none	63-42
JANM38510/07601BFB	none	58-83	JANM38510/15902CEB	none	73-54	JANM38510/20102CZB	none	32-54	JANM38510/23502AUB	none	28-16	JANM38510/30106AFB	none	63-43
JANM38510/07601BFC	none	58-84	JANM38510/15902CEC	none	73-55	JANM38510/20102CZC	none	32-55	JANM38510/23502AUC	none	28-17	JANM38510/30106AFC	none	63-44
JANM38510/07601CEA	none	58-85	JANM38510/15902CFA	none	73-56	JANM38510/20201AEA	none	34-100	JANM38510/23502AVA	none	28-18	JANM38510/30106BEA	none	63-45
JANM38510/07601CEB	none	58-86	JANM38510/15902CFB	none	73-57	JANM38510/20201AEB	none	34-101	JANM38510/23502AVB	none	28-19	JANM38510/30106BEB	none	63-46
JANM38510/07601CEC	none	58-87	JANM38510/15902CFC	none	73-58	JANM38510/20201AEC	none	34-102	JANM38510/23502AVC	none	28-20	JANM38510/30106BEC	none	63-47
JANM38510/07601CFA	none	58-88	JANM38510/20101AJA	none	32-2	JANM38510/20201AFA	none	34-103	JANM38510/23502BUA	none	28-21	JANM38510/30106BFA	none	63-48
JANM38510/07601CFB	none	58-89	JANM38510/20101AJB	none	32-3	JANM38510/20201AFB	none	34-104	JANM38510/23502BUB	none	28-22	JANM38510/30106BFB	none	63-49
JANM38510/07601CFC	none	58-90	JANM38510/20101AJC	none	32-4	JANM38510/20201AFC	none	35-1	JANM38510/23502BUC	none	28-23	JANM38510/30106BFC	none	63-50





# 1. TYPE No. CROSS INDEX

				IN TYPE NUMBER SEQUENCE			
TYPE No.	MFRS Pg&Line	TYPE No.	MFRS Pg&Line	TYPE No.	MFRS Pg&Line	TYPE No.	MFRS Pg&Line
MM4230QWJ	46-43	MM5203D#1	36-76	MM5240ABUJ	44-50	N74S172N	16-108
MM4230QXJ	45-67	MM5203D#2	38-19	MM5240ABUN	44-51	MULB	N82S126F
MM4230QYJ	45-17	MM5203Q#1	36-77	MM5240ABZJ	44-45	PHIN	PHIN
MM4231D#1	36-29	MM5203Q#2	38-20	MM5240ABZJ	44-46	MULB	N82S126F
MM4231D#2	37-57	MM5204D	39-59	MM5240ACAJ	44-47	PHIN	PHIN
MM4231J#1	36-30	MM5204Q	39-60	MM5240ACAN	44-48	MULB	N82S129B
MM4231J#2	37-58	MM5210D	39-78	MM5240D	44-32	PHIN	PHIN
MM4231RP2J	45-68	MM5210J	39-79	MM5240J	44-33	MULB	N82S129F
MM4232AEIJ	80-51	MM5210N	39-80	MM5240N	44-34	PHIN	PHIN
MM4232J#1	38-102	MM5211D	39-87	MM5241D	44-16	MULB	N82S129I
MM4232J#2	40-7	MM5211J	39-88	MM5241J	44-17	PHIN	PHIN
MM4233D	38-92	MM5211N	39-89	MM5241N	44-18	MULB	N82S130F
MM4233J	38-93	MM5212AD	41-44	MM5261D	23-40	PHIN	PHIN
MM4240ABUJ	44-49	MM5212AN	41-45	MM5261N	23-41	MULB	N82S131F
MM4240ABZJ	44-43	MM5213	36-22	MM5262D	26-58	PHIN	PHIN
MM4240ACAJ	44-44	MM5213D#1	36-26	MM5262N	26-59	MULB	N82S136F
MM4240D	44-30	MM5213D#2	37-54	MM5269D	22-56	PHIN	PHIN
MM4240J	44-31	MM5213J#1	36-27	MM5269N	22-57	MULB	N82S137F
MM4241D	44-13	MM5213J#2	37-55	MM5270D5	27-45	PHIN	PHIN
MM4241J	44-14	MM5213N#1	36-28	MM5270D	27-2	MULB	N82S184F
MM4241N	44-15	MM5213N#2	37-56	MM5271D	27-26	PHIN	PHIN
MM4250D	20-93	MM5214J	38-103	MM5280D5	27-3	MULB	N82S185F
MM4250D	23-42	MM5214N	38-104	MM5280D5	27-3	PHIN	PHIN
MM4261N	23-43	MM5215AD	41-42	MM5281D	27-27	MULB	N82S214I
MM4262D	26-60	MM5215AN	41-43	MM5606AN	62-67	PHIN	PHIN
MM4606AD	62-65	MM5220AEJ	45-43	MM5614AN	67-14	MULB	N82S215I
MM4606AF	62-66	MM5220AEN	45-44	MM5621AN	67-15	PHIN	PHIN
MM4614AD	67-10	MM5220APJ	45-100	MM5635AN	47-109	MULB	N82S226B
MM4614AF	67-11	MM5220APN	45-101	MM6055	43-48	PHIN	PHIN
MM4621AD	67-12	MM5220BLJ	46-60	MM6056	43-16	MULB	N82S226F
MM4621AF	67-13	MM5220BLN	46-61	MM6061	43-74	PHIN	PHIN
MM4635AD	47-107	MM5220BMJ	80-49	MM6062	43-32	MULB	N82S229B
MM4635AF	47-108	MM5220BMN	80-50	MM6071	43-63	PHIN	PHIN
MM5001AH	75-68	MM5220BNJ	80-2	MM6072	43-78	MULB	N82S229F
MM5006AD	76-70	MM5220BNN	80-3	MM6073	43-35	PHIN	PHIN
MM5006AH	76-71	MM5220D#1	33-13	MM6074	43-26	MULB	N82S230F
MM5007AAD	76-15	MM5220D#2	34-5	MM11011D	20-100	PHIN	PHIN
MM5007AAH	76-16	MM5220DFJ	80-30	MM11011N	20-101	MULB	N82S231F
MM5007D	76-72	MM5220DFN	80-31	MM1001	27-63	PHIN	PHIN
MM5007H	76-73	MM5220EKJ#1	45-106	MM1001-1	27-28	MULB	N82S280I
MM5007XXD	76-46	MM5220EKJ#2	45-2	MM1001-2	27-4	PHIN	PHIN
MM5007XXH	76-47	MM5220EKN#1	45-107	MM1003	23-34	MULB	N82S281I
MM5010AH	75-69	MM5220EKN#2	45-3	MP3802	38-89	PHIN	PHIN
MM5011A	75-70			MS109	46-53	MULB	N2010K
MM5011D	77-64			MS109	31-1	PHIN	PHIN
MM5012N	77-65	MM5220J#1	33-14	MS113	31-2	MULB	N2410I
MM5013D	78-86	MM5220J#2	34-6	MS115	31-7	PHIN	PHIN
MM5013H	78-87	MM5220LRJ#1	45-103	MS116	31-3	MULB	N2411I
MM5013N	78-88	MM5220LRJ#2	45-104	MS204	31-5	PHIN	PHIN
MM5015AD	75-87	MM5220LRN#1	45-104	MS208	31-4	MULB	N2420Y#1
MM5015D	78-14	MM5220LRN#2	45-41	MS212	31-4	PHIN	PHIN
MM5016H	78-15	MM5220N#1	33-15	MS618	74-73	MULB	N2420Y#2
MM5016N	78-16	MM5220N#2	34-7	MS625	74-79	PHIN	PHIN
MM5017D	78-43	MM5220NPJ	43-65	MSM540	75-21	MULB	N2421Y#1
MM5017H	78-44	MM5220NPJ	43-66	MSM541	68-81	PHIN	PHIN
MM5017N	78-45	MM5221J#1	33-21	MSM542	66-65	MULB	N2421Y#2
MM5018H	75-88	MM5221J#2	34-13	MSM543	66-65	PHIN	PHIN
MM5019D	77-66	MM5221N#1	33-22	MSM544	74-106	MULB	N2425Y#1
MM5019H	77-67	MM5221N#2	34-14	MSM575	38-77	PHIN	PHIN
MM5019XXD	77-57	MM5221RQJ#1	45-55	MSM575-01	44-40	MULB	N2425Y#2
MM5019XXH	77-58	MM5221RQJ#2	46-63	MSR4	49-4	PHIN	PHIN
MM5020D	76-30	MM5221RQN#1	45-56	MSR8	60-3	MULB	N2426Y#1
MM5020N	76-31			MTS1102	76-82	PHIN	PHIN
MM5021D	76-22	MM5221RQN#2	46-64	MTS2013	76-80	MULB	N2426Y#2
MM5021H	76-23			MTS2100	78-22	PHIN	PHIN
MM5021N	76-24			MTS2103	74-95	MULB	N2430Y#1
MM5023D	76-26	MM5221RRJ	45-5	MTS2105	75-80	PHIN	PHIN
MM5023N	76-27	MM5221RRN	45-6	MTS2107	77-2	MULB	N2430Y#2
MM5024AH	79-4	MM5229D	36-93	MTS2108	77-6	PHIN	PHIN
MM5025D	79-29	MM5229N	36-94	MTS2108	77-6	MULB	N2430Y#3
MM5025N	79-30	MM5230B01J	46-41	MUF5	62-78	PHIN	PHIN
MM5026D	79-31	MM5230B01N	46-42	MW4050D	27-64	MULB	N2431Y#1
MM5026N	79-32	MM5230D#1	36-19	MW4050DV1	27-29	PHIN	PHIN
MM5027F	79-36	MM5230D#2	37-48	MW4050DV2	27-5	MULB	N2431Y#2
MM5027N	79-37	MM5230FEJ#1	45-97	MW4060D	27-65	PHIN	PHIN
MM5040H	74-60	MM5230FEJ#2	45-81	MW4060DV1	27-30	MULB	N2435Y#1
MM5050D	74-91	MM5230FEN#1	45-98	MW4060DV2	27-6	PHIN	PHIN
MM5050AH	74-99	MM5230J#1	45-82	MW4104D	26-68	MULB	N2435Y#2
MM5050AD	74-100	MM5230J#2	36-20	MWS5001D	24-72	PHIN	PHIN
MM5051	74-92	MM5230J#3	37-49	MWS5001H	24-73	MULB	N2436Y#1
MM5051AH	74-101	MM5230JTJ#1	45-109	MWS5004D	21-90	PHIN	PHIN
MM5052H	76-18	MM5230JTJ#2	45-84	MWS5040H	21-91	MULB	N2436Y#2
MM5053H	76-88	MM5230JTN#1	45-110	MWS5501D	24-70	PHIN	PHIN
MM5054D	75-76	MM5230JTN#2	45-85	MWS5501H	24-71	MULB	N3101AB
MM5054N	75-77	MM5230KP2J	45-36	MWS5540D	21-88	PHIN	PHIN
MM5055D	77-20	MM5230KP2N	45-37	MWS5540H	21-89	MULB	N3101AF
		MM5230N#1	36-21	N8T10B	58-31	PHIN	PHIN
		MM5230N#2	37-50			MULB	N82S70A
		MM5230NNJ	43-69			PHIN	PHIN
		MM5230NNN	43-70			MULB	N82S70F
		MM5230NOJ	43-71			PHIN	PHIN
		MM5230NON	43-72			MULB	N82S71B
		MM5230QWJ	46-44			PHIN	PHIN
		MM5230QWN	46-45			MULB	N82S71F
		MM5230QXJ	45-69			PHIN	PHIN
		MM5230QYJ	45-70			MULB	N82S112
		MM5230QYN	45-19			PHIN	PHIN
		MM5231D#1	36-31			MULB	N82S114I
		MM5231D#2	37-59			PHIN	PHIN
		MM5231J#1	36-32			MULB	N82S115I
		MM5231J#2	37-60			PHIN	PHIN
		MM5231N#1	36-33			MULB	N82S116B
		MM5231N#2	37-61			PHIN	PHIN
		MM5231RP2J	45-71			MULB	N82S116F
		MM5231RP2N	45-72			PHIN	PHIN
		MM5232AEIJ	80-52			MULB	N82S117B
		MM5232AEIN	80-53			PHIN	PHIN
		MM5232J#1	38-105			MULB	N82S117F
		MM5232J#2	40-8			PHIN	PHIN
		MM5232N#1	38-106			MULB	N82S123B
		MM5232N#2	40-9			PHIN	PHIN
		MM5233D	38-94			MULB	N82S123F
		MM5233J	38-95			PHIN	PHIN
		MM5233N	38-96			MULB	N82S126B
						PHIN	PHIN







# 1. TYPE No. CROSS INDEX

IN TYPE NUMBER SEQUENCE

TYPE No.	MFRS	Pg&Line	TYPE No.	MFRS	Pg&Line	TYPE No.	MFRS	Pg&Line	TYPE No.	MFRS	Pg&Line
UA3540D8	♦SOD	43- 54									
UA3548#1	SOD	36- 40									
UA3548#2	SOD	37- 66									
UA3548#3	SOD	39- 97									
UA3548#4	SOD	41- 52									
UA3572D	♦SOD	36- 98									
UC6548#1	♦SOD	41- 54									
UC6548#2	♦SOD	39- 99									
UC6548#3	♦SOD	38- 24									
UC6548#4	♦SOD	36- 82									
UC6572	♦SOD	36- 95									
UC7523#1	♦SOD	36- 34									
UC7523#2	♦SOD	37- 62									
UC7526	SOD	33- 4									
UC7541-03	♦SOD	43- 21									
UC7541-79	♦SOD	44- 41									
UC7548#1	♦SOD	41- 55									
UC7548#2	♦SOD	39-100									
UC7548#3	♦SOD	38- 25									
UC7548#4	♦SOD	36- 83									
UC7572	♦SOD	36- 96									
UC7596S	♦SOD	40- 43									
UC65253K#1	♦SOD	33- 16									
UC65253K#2	♦SOD	36-102									
UC65253K#3	♦SOD	34- 8									
UC65253K#4	♦SOD	39- 81									
UC65254K#1	♦SOD	33- 17									
UC65254K#2	♦SOD	36-103									
UC65254K#3	♦SOD	34- 9									
UC65254K#4	♦SOD	39- 82									
UC65723K	♦SOD	36- 88									
UC75253K#1	♦SOD	33- 18									
UC75253K#2	♦SOD	36-104									
UC75253K#3	♦SOD	34- 10									
UC75253K#4	♦SOD	39- 83									
UC75254K#1	♦SOD	33- 19									
UC75254K#2	♦SOD	36-105									
UC75254K#3	♦SOD	34- 11									
UC75254K#4	♦SOD	39- 84									
UC75723K	♦SOD	36- 89									
ZN54L91E	♦FERB	72- 67									
ZN54L91J	♦FERB	72- 68									
ZN54L95E	♦FERB	48- 49									
ZN54L95J	♦FERB	48- 50									
ZN54L96E	♦FERB	60- 89									
ZN54L96J	♦FERB	60- 90									
ZN54L164E	♦FERB	70- 28									
ZN54L164J	♦FERB	70- 29									
ZN74L91E	♦FERB	72- 69									
ZN74L91J	♦FERB	72- 70									
ZN74L95E	♦FERB	48- 51									
ZN74L95J	♦FERB	48- 52									
ZN74L96E	♦FERB	60- 91									
ZN74L96J	♦FERB	60- 92									
ZN74L164E	♦FERB	70- 30									
ZN74L164J	♦FERB	70- 31									
ZN5491AE	♦FERB	72- 98									
ZN5491AJ	♦FERB	72- 99									
ZN5494E	♦FERB	59- 81									
ZN5494J	♦FERB	59- 82									
ZN5495AE	♦FERB	59- 90									
ZN5495AJ	♦FERB	56- 23									
ZN5496E	♦FERB	61- 54									
ZN5496J	♦FERB	61- 55									
ZN7491AE	♦FERB	72-100									
ZN7491AJ	♦FERB	72-101									
ZN7494E	♦FERB	59- 83									
ZN7494J	♦FERB	59- 84									
ZN7495AE	♦FERB	56- 24									
ZN7495AJ	♦FERB	56- 25									
ZN7496E	♦FERB	61- 56									
ZN7496J	♦FERB	61- 57									
ZN54164E	♦FERB	72- 20									
ZN54164J	♦FERB	72- 21									
ZN54165E	♦FERB	68- 37									
ZN54165J	♦FERB	68- 38									
ZN54166J	♦FERB	68- 63									
ZN54174E	FERB	63- 65									
ZN54174J	FERB	63- 66									
ZN54175E	FERB	56- 26									
ZN54175J	FERB	56- 27									
ZN54194E	♦FERB	56- 28									
ZN54194J	♦FERB	56- 29									
ZN74164E	♦FERB	72- 22									
ZN74164J	♦FERB	72- 23									
ZN74165E	♦FERB	68- 39									
ZN74165J	♦FERB	68- 40									
ZN74166E	♦FERB	68- 64									
ZN74166J	♦FERB	65- 60									
ZN74174E	FERB	63- 67									
ZN74174J	FERB	63- 68									
ZN74175E	FERB	56- 30									
ZN74175J	FERB	56- 31									
ZN74194E	♦FERB	56- 32									
ZN74194J	♦FERB	56- 33									

























## 2. READ-WRITE MEMORIES (RAMS)

IN ORDER OF (1)No.WORDS(2)No.BITS/WORD  
(3)MODE(4)STRUCT.(5)MAX ACC.TIME(6)TYPE No.

LINE No.	TYPE No.	ORGANIZATION		3	4	5	MAX ACCESS TIME (s)	MAX WRITE CYCLE TIME (s)	MAX OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MIN OUTPUT SINK CURRENT		MIN CLOCK FREQ. (Hz)	OPER. TEMP. RANGE CODE	DRAWINGS							
		1	2							M	D	O	S	NEG. (V)	POS. (V)			MAX '0' (V)	MIN '1' (V)	(A)	@ OUT (V)	+	-	LOGIC/BLOCK	OUTLINE
1	JANM38510/23501AWB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
2	JANM38510/23501AWC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
3	JANM38510/23501BUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	FL45						
4	JANM38510/23501BUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	FL45						
5	JANM38510/23501BUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	FL45						
6	JANM38510/23501BWA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
7	JANM38510/23501BWB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
8	JANM38510/23501BWC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
9	JANM38510/23501CUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	FL45						
10	JANM38510/23501CUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	FL45						
11	JANM38510/23501CUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	FL45						
12	JANM38510/23501CWA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
13	JANM38510/23501CWB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
14	JANM38510/23501CWC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	1.0kΔ	5	8	A202	ML254						
15	JANM38510/23502AUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
16	JANM38510/23502AUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
17	JANM38510/23502AUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
18	JANM38510/23502AVA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
19	JANM38510/23502AVB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
20	JANM38510/23502AVC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
21	JANM38510/23502BUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
22	JANM38510/23502BUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
23	JANM38510/23502BUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
24	JANM38510/23502BVA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
25	JANM38510/23502BVB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
26	JANM38510/23502BVC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
27	JANM38510/23502CUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
28	JANM38510/23502CUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
29	JANM38510/23502CUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	FL45						
30	JANM38510/23502CVA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
31	JANM38510/23502CVB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
32	JANM38510/23502CVC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	1.0kΔ	5	8	A204	ML255						
33	JANM38510/23503AUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
34	JANM38510/23503AUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
35	JANM38510/23503AUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
36	JANM38510/23503AWA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
37	JANM38510/23503AWB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
38	JANM38510/23503AWC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
39	JANM38510/23503BUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
40	JANM38510/23503BUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
41	JANM38510/23503BUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
42	JANM38510/23503BWA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
43	JANM38510/23503BWB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
44	JANM38510/23503BWC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
45	JANM38510/23503CUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
46	JANM38510/23503CUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
47	JANM38510/23503CUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	FL45						
48	JANM38510/23503CWA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
49	JANM38510/23503CWB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
50	JANM38510/23503CWC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	3.2m	.40	2.0kΔ	5	A	A202	ML254						
51	JANM38510/23504AUA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	2.0kΔ	5	A	A204	FL45						
52	JANM38510/23504AUB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	2.0kΔ	5	A	A204	FL45						
53	JANM38510/23504AUC	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	2.0kΔ	5	A	A204	FL45						
54	JANM38510/23504AVA	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	2.0kΔ	5	A	A204	ML255						
55	JANM38510/23504AVB	4096	1	D	MNX	300n	470n*	1.5	5.5	12	.60	2.2	5.0m	.40	2.0kΔ	5	A	A204	ML255						



## 2. READ-WRITE MEMORIES (RAMS)

IN ORDER OF (1)No.WORDS(2)No.BITS/WORD  
(3)MODE(4)STRUCT.(5)MAX ACC.TIME(6)TYPE No.

LINE No.	6 TYPE No.	ORGANIZATION		3 M O D E	4 STRUC TURE CODE	5 MAX ACCESS TIME (s)	MAX WRITE CYCLE TIME (s)	MAX OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MIN OUTPUT SINK CURRENT		MIN CLOCK FREQ. (Hz)	OPER. TEMP. RANGE CODE	DRAWINGS			
		1 No. WORDS	2 BITS PER WORD						NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)	(A)	@ OUT (V)			-	+	LOGIC/ BLOCK	OUTLINE
1	C2108-4	8192	1	D	MNX	300n	425n*	1.2	5.0	12	80	2.4	4.1m	.40	500 Δ	0	7	A295	ML140g	
2	D2108-4	8192	1	D	MNX	300n	425n*	1.2	5.0	12	80	2.4	4.1m	.40	500 Δ	0	7	A295	ML157c	
3	P2108-4	8192	1	D	MNX	300n	425n*	1.2	5.0	12	80	2.4	4.1m	.40	500 Δ	0	7	A295	ML127v	
4▼	MCM6616L3	16384	1	D	MN	200n	375n	600m	5.0	12	80	2.7	4.2m	.40	500 Δ	0	7	A299	ML145	
5▼	MCM6616P3	16384	1	D	MN	200n	375n	600m	5.0	12	80	2.7	4.2m	.40	500 Δ	0	7	A299	ML98b	
6▼	MCM6616L4	16384	1	D	MN	250n	400n	600m	5.0	12	80	2.7	4.2m	.40	500 Δ	0	7	A299	ML145	
7▼	MCM6616P4	16384	1	D	MN	250n	400n	600m	5.0	12	80	2.7	4.2m	.40	500 Δ	0	7	A299	ML98b	
8▼	MCM6616L5	16384	1	D	MN	300n	450n	600m	5.0	12	80	2.7	4.2m	.40	500 Δ	0	7	A299	ML145	
9▼	MCM6616P5	16384	1	D	MN	300n	450n	600m	5.0	12	80	2.7	4.2m	.40	500 Δ	0	7	A299	ML98b	
10▼	MK4116P-1	16384	1	D	MNG	130n	320n	462m	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A299	ML225	
11#	MB8116H	16384	1	D	MNG	150n	375n*	462m	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A282	ML140f	
12	MK4116P-2	16384	1	D	MNG	150n	375n*	462m†	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A299	ML225	
13	C2116-2	16384	1	D	MNG	200n	350n*	900m	5.0	12	80	2.4	4.1m	.40	500 Δ	0	7	A266	ML140g	
14▼#	HM4716-3	16384	1	D	MNG	200n	375n*	602m	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A99f	ML89h	
15▼	IM7116-3CDE	16384	1	D	MNG	200n	375n*	600m	5.0	12	80	2.4	3.2m	.40	500 Δ	0	7	A266	ML225	
16▼	IM7116A-3CDE	16384	1	D	MNG	200n	375n*	600m	5.0	12	80	2.4	3.2m	.40	500 Δ	0	7	A266	ML225	
17#	MB8116E	16384	1	D	MNG	200n	375n*	462m	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A282	ML140f	
18	MK4116P-3	16384	1	D	MNG	200n	375n*	462m†	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A299	ML225	
19	C2116	16384	1	D	MNG	250n	375n*	900m	5.0	12	80	2.4	4.1m	.40	500 Δ	0	7	A266	ML140g	
20	C2116-3	16384	1	D	MNG	250n	375n*	1.2	5.0	12	80	2.4	4.1m	.40	500 Δ	0	7	A266	ML140g	
21▼#	HM4716-4	16384	1	D	MNG	250n	375n*	602m	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A99f	ML89h	
22▼	IM7116-4CDE	16384	1	D	MNG	250n	385n*	600m	5.0	12	80	2.4	3.2m	.40	500 Δ	0	7	A266	ML225	
23▼	IM7116A-4CDE	16384	1	D	MNG	250n	385n*	600m	5.0	12	80	2.4	3.2m	.40	500 Δ	0	7	A266	ML225	
24	MK4116P-4	16384	1	D	MNG	250n	75n\$	1.0	5.0	12	80	2.4	10uΔ	0.0	500 Δ	0	7	A99f	ML	
25	C2116-4	16384	1	D	MNG	300n	425n*	900m	5.0	12	80	2.4	4.1m	.40	500 Δ	0	7	A266	ML140g	
26▼#	M58759S	16384	1	D	MNX	200n	375n	462m	5.0	12	80	2.4	4.2m	.40	500 Δ	0	7	A99f	ML	
27	uPD416D	16384	1	D	MNX	300n	500n*	720m†	5.0	12	60	2.4	2.0m	.40	500 Δ	0	7	A99f	ML	



### 3. READ ONLY MEMORIES (ROMS)

IN ORDER OF (1)No.WDS(2)No.BITS/WD(3)OP.MODE  
PRG.CODE(4)STRUCT.(5)MAX ACC.TIME(6)TYPE No.

LINE No.	6 TYPE No.	ORGANIZATION		3 OP MODE	4 STRUCTURE CODE	5 MAX ACCESS TIME (s)	MAX OPER. POWER (W)	RATED POWER SUP.		INPUT LOGIC LEVELS		MIN OUTPUT SINK CURRENT		OPER. TEMP. RANGE CODE	GENERAL DESCRIPTION	DRAWINGS		
		1 No. WORDS	2 BITS PER WORD					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)	(A)	@ OUT (V)			LOGIC/BLOCK	OUTLINE	
																		PROG. CODE
1	MCM5304L	64	8	SE	BTX	75n	600m	0.0	5.0	.45%	2.5	10m	4.7	5	C	B61	ML133	
2	JANM38510/20101AJA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
3	JANM38510/20101AJB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
4	JANM38510/20101AJC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
5	JANM38510/20101AKA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
6	JANM38510/20101AKB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
7	JANM38510/20101AKC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
8	JANM38510/20101AZA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
9	JANM38510/20101AZB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
10	JANM38510/20101AZC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
11	JANM38510/20101BJA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
12	JANM38510/20101BJB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
13	JANM38510/20101BJC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
14	JANM38510/20101BKA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
15	JANM38510/20101BKB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
16	JANM38510/20101BKC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
17	JANM38510/20101BZA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
18	JANM38510/20101BZB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
19	JANM38510/20101BZC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
20	JANM38510/20101CJA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
21	JANM38510/20101CJB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
22	JANM38510/20101CJC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
23	JANM38510/20101CKA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 ML126	
24	JANM38510/20101CKB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
25	JANM38510/20101CKC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
26	JANM38510/20101CZA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL29	
27	JANM38510/20101CZB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
28	JANM38510/20101CZC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
29	JANM38510/20102AJA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105 FL30	
30	JANM38510/20102AJB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
31	JANM38510/20102AJC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
32	JANM38510/20102AKA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
33	JANM38510/20102AKB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
34	JANM38510/20102AKC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
35	JANM38510/20102AZA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
36	JANM38510/20102AZB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
37	JANM38510/20102AZC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
38	JANM38510/20102BJA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
39	JANM38510/20102BJB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
40	JANM38510/20102BJC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
41	JANM38510/20102BKA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
42	JANM38510/20102BKB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
43	JANM38510/20102BKC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
44	JANM38510/20102BZA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
45	JANM38510/20102BZB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
46	JANM38510/20102BZC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
47	JANM38510/20102CJA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
48	JANM38510/20102CJB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
49	JANM38510/20102CJC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
50	JANM38510/20102CKA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a ML126	
51	JANM38510/20102CKB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
52	JANM38510/20102CKC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL29	
53	JANM38510/20102CZA	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
54	JANM38510/20102CZB	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
55	JANM38510/20102CZC	64	8	SE	BTX	140n	575m	0.0	5.0	.80	2.0	30m		5	C	PROM	B105a FL30	
56	HRM2048	64	32	SE	TAX	240n	400m	0.0	5.0	.80	2.0	30m		0	7	PROM Programmable	B105a B91	ML113



















### 3. READ ONLY MEMORIES (ROMS)

IN ORDER OF (1)No.WDS(2)No.BITS/WD(3)OP.MODE  
PRG.CODE(4)STRUCT.(5)MAX ACC.TIME(6)TYPE No.

LINE No.	6 TYPE No.	ORGANIZATION		3 OP. MODE		5 MAX ACCESS TIME (s)	MAX OPER. POWER (W)	RATED POWER SUP.		INPUT LOGIC LEVELS		MIN OUTPUT SINK CURRENT		OPER. TEMP. RANGE CODE	GENERAL DESCRIPTION	DRAWINGS				
		1 No. WORDS	2 BITS PER WORD	3 PROG. CODE	4 MODE			5 STRUCTURE CODE	NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)	(A)			@ (V)	-	+	LOGIC/BLOCK	OUTLINE
1	uPD2316AD	2048	8	SC	MNX	450n	490m	0.0	5.0	.80	2.0s	2.0m	.45	1	7	B164	ML205			
2	MCM6832L	2048	8	SC	MNX	500n	527m	5.0	12	.80	3.0s	1.6m	.40	0	7	B144	ML207b			
3	MCM6832P	2048	8	SC	MNX	500n	527m	5.0	12	.80	3.0s	1.6m	.40	0	7	B144	ML139			
4	MCM68317L	2048	8	SC	MNX	500n†		0.0	5.0							B210	ML207b			
5	MCM68317P	2048	8	SC	MNX	500n†		0.0	5.0							B210	ML139			
6	EA8316EDL	2048	8	SC	MNX	525n	700m	0.0	5.0	.80	2.2s	2.1m	.45	5	9	B258	ML184b			
7	EA8316ADC	2048	8	SC	MNX	600n	800m	0.0	5.0	.80	2.2s	1.6m	.40	0	7	B164	ML133e			
8	EA8316ADM	2048	8	SC	MNX	600n	800m	0.0	5.0	.80	2.2s	1.6m	.40	5	9	B164	ML133e			
9	S9996#1	2048	8	SC	MPI	1.5u\$	240m	12	5.0	.90	3.5			0	6	A146	ML34d			
10	S9996#2	2048	8	SC	MPI	1.5u\$	240m	12	5.0	.90	3.5			0	6	A146	ML155			
11	TMS4800JL#1	2048\$	8\$	SC	MPX	700n	450m†	12	5.0	.60	3.5	50u	.40	0	7	B94	ML207			
12	TMS4800NL#1	2048\$	8\$	SC	MPX	700n	450m†	12	5.0	.60	3.5	50u	.40	0	7	B94	ML72b			
13	EA4900#1	2048	8	SC	MPX	950n	1.2	12	5.0	5.6	3.5			0	6	B94	ML41			
14	EA4900#2	2048	8	SC	MPX	950n	1.2	12	5.0	5.6	3.5			0	6	B94	ML168			
15	EA4900C#1	2048	8	SC	MPX	950n	800m	12	5.0	.60	3.5	1.6m	.40	0	7	B94a	ML168			
16	EA4900C#2	2048	8	SC	MPX	950n	1.2	12	5.0	.60	3.5	1.6m	.40	0	7	B94a	ML187			
17	FDR151BZ	2048	8	SC	MPX	1.2u	525m	12	5.0	3.5	.60	1.6m	.40	0	7	B94	ML118b			
18	FDR151Z	2048	8	SC	MPX	1.2u	525m	12	5.0	3.5	.60	1.6m	.40	0	7	B94	ML41			
19	TMS2716JL	2048	8	SE	MNG	450n	700m	5.0	12	.65	2.4s	1.6m	.45	0	7	Erasable PROM	B269	ML207		
20	RO3-20480	2048	10	SC	MNI	500n	250m†	0.0	5.0					0	7	B231	ML216b			
21	MK28000P#2	4096	4	DC	MPI	600n	340m†	12	5.0	.80	-1.5	10u#Δ		0	7	B94	ML191			
22	EA4600C#3	4096	4	SC	MNA	550n	800m	0.0	5.0	.40%	2.4	1.6m	.40	0	7	B94a	ML168			
23	EA4600C#4	4096	4	SC	MNA	550n	1.2	0.0	5.0	.40%	2.4	1.6m	.40	0	7	B94a	ML187			
24	EA4600M#3	4096	4	SC	MNA	750n	800m	0.0	5.0	.40%	2.4	1.6m	.40	5	C	B94a	ML168			
25	EA4600M#4	4096	4	SC	MNA	750n	1.2	0.0	5.0	.40%	2.4	1.6m	.40	5	C	B94a	ML187			
26	EA4600C1#3	4096	4	SC	MNA	800n	800m	0.0	5.0	.40%	2.4	1.6m	.40	0	7	B94a	ML168			
27	EA4600C1#4	4096	4	SC	MNA	800n	1.2	0.0	5.0	.40%	2.4	1.6m	.40	0	7	B94a	ML187			
28	RO3-16384	4096	4	SC	MNI	1.5u\$	250m	0.0	5.0	.65	2.2s	1.6m	.45	0	7	B183				
29	S8996#1	4096	4	SC	MPI	1.5u\$	240m	12	5.0	.90	3.5			0	6	Random PR	A146	ML34d		
30	S8996#2	4096	4	SC	MPI	1.5u\$	240m	12	5.0	.90	3.5			0	6	Random PR	A146	ML155		
31	TMS4800JL#2	4096\$	4\$	SC	MPX	700n	450m†	12	5.0	.60	3.5	50u	.40	0	7	B94	ML207			
32	TMS4800NL#2	4096\$	4\$	SC	MPX	700n	450m†	12	5.0	.60	3.5	50u	.40	0	7	B94	ML72b			
33	EA4900L#1	4096	4	SC	MPX	1.3u	800m	12	5.0	.60	3.5	1.6m	.40	5	A	B94a	ML168			
34	EA4900L#2	4096	4	SC	MPX	1.3u	1.2	12	5.0	.60	3.5	1.6m	.40	5	A	B94a	ML187			
35	M58332-XXXP	4096	8	SC		600n		0.0	5.0	.80	2.2	2.0m	.45	0	7					
36	MCS2029	4096	8	S	MPX	800n	456m†	12	5.0	.60	-1.5					B160	ML191			
37	MK32000P-5	4096	8	SC	MNG	300n	200m†	0.0	5.0	.80	2.0s			0	7	B261	ML191			
38	HN46532-2	4096	8	SC	MNG	450n	440m	0.0	5.0	.80	2.0	1.6m	.40	0	7	B247	ML32c			
39	TMS4732JL	4096	8	SC	MNG	450n	580m†	0.0	5.0	.65	2.0s	2.0m	.40	0	7	B226	ML207			
40	TMS4732NL	4096	8	SC	MNG	450n	580m†	0.0	5.0	.65	2.0s	2.0m	.40	0	7	B226	ML72b			
41	HN46532-3	4096	8	SC	MNG	650n	440m	0.0	5.0	.80	2.2	1.6m	.40	0	7	B247	ML32c			
42	RO3-9332B	4096	8	SC	MNI	450n		0.0	5.0					0	7	B232	ML216b			
43	RO3-9332A	4096	8	SC	MNI	850n		0.0	5.0					0	7	B232	ML216b			
44	EA3200DC	4096	8	SC	MNX	350n	500m	5.0	12	.80	2.4s	2.1m	.45	0	7	B257	ML218a			
45	MK36000P-5	8192	8	SC	MNG	300n	200m†	0.0	5.0	.80	2.0s			0	7	B262	ML191			



# 4. CHARACTER GENERATORS

IN ORDER OF (1)USE CODE(2)No.CHARACTERS  
(3)No.BITS/CHAR(4)No.OUT(5)STRUCT(6)TYPE No.

LINE No.	TYPE No.	USE CODE	No. CHARACTERS	BITS PER CHAR.	No. OUT-PUTS	STRUC TURE CODE	MAX ACCESS TIME (s)	MAX OPER. POWER (W)	RATED POWER SUP.		INPUT LOGIC LEVELS		MIN OUTPUT SINK CURRENT @ OUT		OPER. TEMP. RANGE CODE	DRAWINGS			
									NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)	(A)	(V)		-	+	LOGIC/BLOCK	OUTLINE
1	MCM6578L	SAR	128	63	7	MNX	500n	800m	3.0	12	.80	4.0	1.6m	.40	0	7	C50	ML150a	
2	MCM6578P	SAR	128	63	7	MNX	500n	800m	3.0	12	.80	4.0	1.6m	.40	0	7	C50	ML39	
3	MCM6579L	SAR	128	63	7	MNX	500n	800m	3.0	12	.80	4.0	1.6m	.40	0	7	C50	ML150a	
4	MCM6579P	SAR	128	63	7	MNX	500n	800m	3.0	12	.80	4.0	1.6m	.40	0	7	C50	ML39	
5	2526I#1	SAS	64	81	9	MPG	700n	730m	12	5.0	.60	3.4	1.6m	.50	0	7	B112	ML174	
6	2526N#1	SAS	64	81	9	MPG	700n	730m	12	5.0	.60	3.4	1.6m	.50	0	7	B112	ML135	
7#	M200M1AA	SBC	16	64	8	MPX	4.0u	150m†	27	0.0	-2.0	-9.0	1.0uΔ	20	0	7	B57	ML59	
8#	M200M1XX	SBC	16	64	8	MPX	4.0u	150m†	27	0.0	-2.0	-9.0	1.0u	20	0	7	B57	ML59	
9#	uPD474D01	SCC	64	7	9	MNX	315n	704m♦	5.0	12	.80	3.0s	1.7m	.50	1	7	C63	ML234	
10#	uPD473D03	SCC	64	9	7	MNX	315n	704m♦	5.0	12	.80	3.0s	1.7m	.50	1	7	C62	ML234	
11	2516IXCMXXXX	SCC	64	48	8	MPG	500n	730m	12	5.0	.60	3.4s	1.6m	.40	0	7	C15b	ML88f	
12	2516NXCMXXXX	SCC	64	48	8	MPG	500n	730m	12	5.0	.60	3.4s	1.6m	.40	0	7	C15b	ML135	
13	MM4241D	SCC	64	48	8	MPX	900n∅	629m∅	12	5.0	1.0	3.0s	1.6m	.40	5	C	C42	ML200	
14	MM4241J	SCC	64	48	8	MPX	900n∅	629m∅	12	5.0	1.0	3.0s	1.6m	.40	5	C	C42	ML133a	
15	MM4241N	SCC	64	48	8	MPX	900n∅	629m∅	12	5.0	1.0	3.0s	1.6m	.40	5	C	C42	ML118	
16	MM5241D	SCC	64	48	8	MPX	900n∅	629m∅	12	5.0	1.0	3.0s	1.6m	.40	2	7	C42	ML200	
17	MM5241J	SCC	64	48	8	MPX	900n∅	629m∅	12	5.0	1.0	3.0s	1.6m	.40	2	7	C42	ML133a	
18	MM5241N	SCC	64	48	8	MPX	900n∅	629m∅	12	5.0	1.0	3.0s	1.6m	.40	2	7	C42	ML118	
19	NC6580L	SCC	128	63	9	MNA	400n	700m†	3.0	12	.80	3.0	1.6m	.40	0	7	C51	ML30g	
20	NC6580P	SCC	128	63	9	MNA	400n	700m†	3.0	12	.80	3.0	1.6m	.40	0	7	C51	ML30g	
21	MCM6580L	SCC	128	64	9	MNX	400n	800	3.0	12	.80	4.0	1.6m	.40	0	7	C51	ML150	
22	MCM6580P	SCC	128	64	9	MNX	400n	800	3.0	12	.80	4.0	1.6m	.40	0	7	C51	ML39	
23	3255-9-7K	SCN	16	35	7	MPG	400n	400m	12	5.0	.85	4.0	2.4m	.40	0	7	C17	ML57	
24	3256-9-7K	SCN	16	35	7	MPG	400n	400m	12	5.0	.85	4.0	2.4m	.40	0	7	C17	ML57	
25#	uPD473D01	SCR	64	9	7	MNX	315n	704m♦	5.0	12	.80	3.0s	1.7m	.50	1	7	C62	ML234	
26	2513IXCMXXXX#1	SCR	64	35	5	MPG	500n	730m	12	5.0	.60	3.4s	1.6m	.40	0	7	C15	ML88f	
27	2513NXCMXXXX#1	SCR	64	35	5	MPG	500n	730m	12	5.0	.60	3.4s	1.6m	.40	0	7	C15	ML135	
28	2513IXCMXXXX#2	SCR	64	40	5	MPG	500n	730m	12	5.0	.60	3.4s	1.6m	.40	0	7	C15	ML88f	
29	2513NXCMXXXX#2	SCR	64	40	5	MPG	500n	730m	12	5.0	.60	3.4s	1.6m	.40	0	7	C15	ML135	
30	MM4240D	SCR	64	40	5	MPX	600n∅	480m∅	12	12	10*	4.0#			5	C	C14a	ML200	
31	MM4240J	SCR	64	40	5	MPX	600n∅	480m∅	12	12	10*	4.0#			5	C	C14a	ML133a	
32	MM5240D	SCR	64	40	5	MPX	600n∅	480m∅	12	12	10*	4.0#			2	7	C14a	ML200	
33	MM5240J	SCR	64	40	5	MPX	600n∅	480m∅	12	12	10*	4.0#			2	7	C14a	ML133a	
34	MM5240N	SCR	64	40	5	MPX	600n∅	480m∅	12	12	10*	4.0#			2	7	C14a	ML183	
35	MK2300P	SCR	64	70	10	MPI	1.0u∅	40m	12	5.0	.60	3.5	2.0m	.40	0	7	C12	ML21	
36	NC6570AL	SCR	128	63	7	MNA	400n	600m†	5.0	12	.80	3.0	1.6m	.40	0	7	C50	ML30g	
37	NC6570AP	SCR	128	63	7	MNA	400n	600m†	5.0	12	.80	3.0	1.6m	.40	0	7	C50	ML30g	
38	NC6570L	SCR	128	63	7	MNA	400n	600m†	3.0	12	.80	3.0	1.6m	.40	0	7	C50	ML150c	
39	NC6570P	SCR	128	63	7	MNA	400n	600m†	3.0	12	.80	3.0	1.6m	.40	0	7	C50	ML150c	
40#	MSM575-01	SCC	5	7	7	MCX	1.5u†	2.5m†	0.0	5.0	.80	3.6	1.6m	.40	2	7	C61	ML118h	
41	UC7541-79	SECC	64	35	7	MPX	700n∅	200m†	12	12	-3.0	-11	2.0m	-4.0	2	8	C16	ML24	
42	EA4016	SECC	64	80	10	MPX	725n	450m	12	12	10	3.0	1.6m	.40	5	8	C41a	ML41	
43	MM4240ABZJ	SER	64	40	5	MPX	600n∅	504m∅	12	12	10*	4.0#			5	C	C14a	ML133a	
44	MM4240ACAJ	SER	64	40	5	MPX	600n∅	504m∅	12	12	10*	4.0#			5	C	C14a	ML133a	
45	MM5240ABZJ	SER	64	40	5	MPX	600n∅	504m∅	12	12	10*	4.0#			2	7	C14a	ML133a	
46	MM5240ABZJN	SER	64	40	5	MPX	600n∅	504m∅	12	12	10*	4.0#			2	7	C14a	ML183	
47	MM5240ACAJ	SER	64	40	5	MPX	600n∅	504m∅	12	12	10*	4.0#			2	7	C14a	ML133a	
48	MM5240ACAN	SER	64	40	5	MPX	600n∅	504m∅	12	12	10*	4.0#			2	7	C14a	ML183	
49	MM4240ABUJ	SHR	64	40	7	MPX	600n∅	504m∅	12	12	10*	4.0#			5	C	C14a	ML133a	
50	MM5240ABUJ	SHR	64	40	7	MPX	600n∅	504m∅	12	12	10*	4.0#			2	7	C14a	ML133a	
51	MM5240ABUN	SHR	64	40	7	MPX	600n∅	504m∅	12	12	10*	4.0#			2	7	C14a	ML183	
52#	T154D1A9	SNC	6	40	8	BTX	50n∅	400m♦	0.0	5.0	90	2.0	10m	45	0	7	B20	ML60	
53#	T154D1B8	SNC	6	40	8	BTX	50n∅	400m♦	0.0	5.0	90	2.0	10m	45	0	7	B20	ML60	
54	MCM6670P	SNR	128	35	5	MNG	500n	650m	0.0	5.0	80	2.0	1.6m	.40	0	7	C64	ML7	
55	HEPC380ZP-RT	SNS	12	8	8	BTX	45n	240m†	0.0	5.0	90	2.0	20m	5.25	0	7	C26	ML38	
56	MC4039P	SNS	12	8	8	BTX	45n♦	240m†	0.0	5.0	.45%		20m	.45	0	7	C26	ML40	



# 6. CODE CONVERTERS

IN ORDER OF (1)FROM CODE(2)TO CODE(3)No.WORDS  
(4)No.INPUT BITS(5)No.OUTPUT BITS(6)TYPE No.

LINE No.	TYPE No.	CONVERSION CODE		3 No. WORDS	No. CODE BITS		M O D E	STRUCTURE CODE	MAX ACCESS TIME (s)	MAX OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MIN OUTPUT SINK CURRENT		OPER. TEMP. RANGE CODE	DRAWINGS		
		1	2		4	5					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)	(A)	@ OUT (V)		LOGIC/BLOCK	OUTLINE	
		FRM	TO		IN	OUT														
1	DM54184AJ	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	5	C	B21	ML127f
2	DM54184AW	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	5	C	B21	FL39
3	DM54184J	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	5	C	B21	ML127f
4	DM54184W	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	5	C	B21	FL39
5	DM74184AJ	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	0	7	B21	ML127f
6	DM74184AN	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	0	7	B21	ML178
7	DM74184AW	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	0	7	B21	FL39
8	DM74184J	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	0	7	B21	ML127f
9	DM74184N	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	0	7	B21	ML178
10	DM74184W	4	5	40	6	6	S	BTX	50n\$	400m	0.0	5.0	.80	2.0	12m	.40	0	7	B21	FL39
11	FLH561-74184	4	5	40	6	6	S	BTC	40n\$	500m	0.0	5.0	.80	2.0	12m	.40	0	7	E20	MLZ
12	FLH565-84184	4	5	40	6	6	S	BTC	40n\$	500m	0.0	5.0	.80	2.0	12m	.40	2	8	E20	MLZ
13	SN54184J	4	5	40	6	6	S	BTX	40n\$	500m	0.0	5.0	.80	2.0Δ	12m	.40	5	C	E18	ML61a
14	SN54184W	4	5	40	6	6	S	BTX	40n\$	500m	0.0	5.0	.80	2.0Δ	12m	.40	5	C	E18	M0004AG
15	SN74184J	4	5	40	6	6	S	BTX	40n\$	500m	0.0	5.0	.80	2.0Δ	12m	.40	0	7	E18	ML61a
16	SN74184N	4	5	40	6	6	S	BTX	40n\$	500m	0.0	5.0	.80	2.0Δ	12m	.40	0	7	E18	ML48
17	5548	4	5	256	6	6	S	BTX	40n\$	930m	0.0	5.0	.80	2.0			0	7		
18	FLH571-74185A	5	4	64	6	6	S	BTC	40n\$	500m	0.0	5.0	.80	2.0	12m	.40	0	7	E20	MLZ
19	FLH575-84185A	5	4	64	6	6	S	BTC	40n\$	500m	0.0	5.0	.80	2.0	12m	.40	2	8	E20	MLZ
20	SN54185AJ	5	4	64	6	7	S	BTX	40n\$	500m	0.0	5.0	.80	2.0Δ	12m	.40	5	C	E18	ML61a
21	SN54185AW	5	4	64	6	7	S	BTX	40n\$	500m	0.0	5.0	.80	2.0Δ	12m	.40	5	C	E18	M0004AG
22	SN74185AJ	5	4	64	6	7	S	BTC	25n\$	546m	0.0	5.25	.80	2.0	12m	.40	0	7	E18	ML61a
23	SN74185AN	5	4	64	6	7	S	BTC	25n\$	546m	0.0	5.25	.80	2.0	12m	.40	0	7	E18	ML48
24	5549	5	4	256	6	6	S	BTX	40n\$	930m	0.0	5.0	.80	2.0			0	7		
25	MCM4067AL%	5	4	256	8	9	S	BTX	50n	650m	0.0	5.0	.45%	2.5	12m	.45	0	7	E12	ML78
26	MCM4067L%	5	4	256	8	9	S	BTX	50n	650m	0.0	5.0	.45%	2.5	12m	.45	0	7	E12	ML5
27	MCM4068AL%	5	4	256	8	9	S	BTX	50n	650m	0.0	5.0	.45%	2.5	12m	.45	0	7	E12	ML78
28	MCM4068L%	5	4	256	8	9	S	BTX	50n	650m	0.0	5.0	.45%	2.5	12m	.45	0	7	E12	ML5
29	NC6561L#3	6%	1	128	10	8	S	MNA	350n	600m	3.0	12	.80	3.0	1.6m	.40	0	7	B121	ML30g
30	NC6561P#3	6%	1	128	10	8	S	MNA	350n	600m	3.0	12	.80	3.0	1.6m	.40	0	7	B121	ML30g
31	MCM6591L#6	6%	1	128	11	8	S	MNG	800n	405m	3.0	12	.80	3.0	1.6m	.40	0	7	B144	ML150a
32	NC6591L#3	6%	1	128	11	8	S	MNA	800n	405m	3.0	12	.80	3.0	1.6m	.40	0	7	B144	ML30g
33	NC6591P#3	6%	1	128	11	8	S	MNA	800n	405m	3.0	12	.80	3.0	1.6m	.40	0	7	B144	ML30g
34	MCM6561L#3	6	1	256	8	8	S	MNA	350n	1.0	0.0	5.0	.40%	3.0	1.6m	.40	0	7	B121	ML150a
35	MCM6561P#3	6	1	256	8	8	S	MNA	350n	1.0	0.0	5.0	.40%	3.0	1.6m	.40	0	7	B121	ML39
36	MCM6562L#3	6	1	256	8	8	S	MNA	350n	1.0	0.0	5.0	.40%	3.0	1.6m	.40	0	7	B121	ML150a
37	MCM6562P#3	6	1	256	8	8	S	MNA	350n	1.0	0.0	5.0	.40%	3.0	1.6m	.40	0	7	B121	ML39
38	MCM4069AL%	6	1	256	12	8	S	BTX	40n	650m	0.0	5.0	.45%		12m	.45	0	7	E12a	ML98
39	MCM4070AL%	6	1	256	12	8	S	BTX	40n	650m	0.0	5.0	.45%		12m	.45	0	7	E12a	ML98
40	MM4230B01J	6	1	256	12	8	S	MPX	725n	960m	12	12	10*	4.0#			5	C	B26	ML133a
41	MM5230B01J	6	1	256	12	8	S	MPX	725n	960m	12	12	10*	4.0#			0	7	B26	ML133a
42	MM5230B01N	6	1	256	12	8	S	MPX	725n	960m	12	12	10*	4.0#			0	7	B26	ML183
43	MM4230QWJ	6	2	256	12	8	S	MPX	725n	960m	12	12	10*	4.0#			5	C	B26	ML133a
44	MM5230QWJ	6	2	256	12	8	S	MPX	725n	960m	12	12	10*	4.0#			0	7	B26	ML133a
45	MM5230QWN	6	2	256	12	8	S	MPX	725n	960m	12	12	10*	4.0#			0	7	B26	ML183
46	AY5-2376	8	1	264	88	9	D	MPN	170m		12	5.0	.80	3.5	1.6m	.40	0	7	E17	ML246
47	AY5-3600	8	1	360	90	10	D	MPN	204m		12	5.0	.80	3.5	1.6m	.40	0	7	E18	ML246
48	EA2007ADC#1	8	1	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML166
49	EA2007APC#1	8	1	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML167
50	EA2007ADC#2	8	2	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML166
51	EA2007APC#2	8	2	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML167
52	AY5-3600-PRO	8	5	360	90	9	D	MPN	204m		12	5.0	.80	3.5	1.6m	.40	0	7	E18	ML246
53	MP3802	8	9	360	90	10	S	MXX	204m		12	5.0	.80	3.5	1.6m	.40	0	7	E19	ML174a
54	EA2000CD	8	9	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML166
55	EA2000CP	8	9	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML167
56	EA2030DC	8	9	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML166
57	EA2030PC	8	9	396	99	10	D	MPX			12	5.0	.80	2.8	10uΔ		0	7	E16	ML167
58	2526/CM3400	10	1	64	9	9	S	MPG	700n	730m	12	5.0	.60	3.4	1.6m	.50	0	7	B112	ML135
59	MM4220BLJ	10	1	128	5	7	S	MPX	650n	300m	12	12	10*	4.0#			5	C	B26a	ML133a
60	MM5220BLJ	10	1	128	5	7	S	MPX	650n	300m	12	12	10*	4.0#			2	7	B26a	ML133a
61	MM5220BLN	10	1	128	5	7	S	MPX	650n	300m	12	12	10*	4.0#			2	7	B26a	ML183
62	MM4221RQJ#2	11%	1	128	6	8	S	MPX	950n	204m	12	5.0	3.0*	.80#			5	C	B26a	ML133a
63	MM5221RQJ#2	11%	1	128	6	8	S	MPX	950n	204m	12	5.0	3.0*	.80#			0	7	B26a	ML133a
64	MM5221RQN#2	11%	1	128	6	8	S	MPX	950n	204m	12	5.0	3.0*	.80#			0	7	B26a	ML183









# 7. SHIFT REGISTERS

IN ORDER OF (1) No. BITS/REG (2) No. REGISTERS  
(3) OP. CODE (4) MAX W/C FREQ (5) STRUCT (6) TYPE No

LINE No.	6 TYPE No.	ORGANIZATION		3 OPER. CODE	4 MAX WORST CASE FREQ. (Hz)	5 STRUCTURE CODE	MAX OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MAX PROP. DELAY (s)	MIN OUTPUT SINK CURRENT @ OUT (V)		MIN CLOCK FREQ. (Hz)	OPER. TEMP. RANGE CODE	DRAWINGS	
		1 BITS PER REGISTER	2 No. REGS					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)		MIN (A)	OUT (V)			LOGIC/BLOCK	OUTLINE
1	JANM38510/00901BBA	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21
2	JANM38510/00901BBB	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21
3	JANM38510/00901BBC	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21
4	JANM38510/00901BCA	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F155	ML143
5	JANM38510/00901BCB	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F155	ML143
6	JANM38510/00901BCC	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F155	ML143
7	JANM38510/00901BDA	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL22
8	JANM38510/00901BDB	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL22
9	JANM38510/00901BDC	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL22
10	JANM38510/00901CAA	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21a
11	JANM38510/00901CAB	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21a
12	JANM38510/00901CAC	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21a
13	JANM38510/00901CBA	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21
14	JANM38510/00901CBB	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21
15	JANM38510/00901CBC	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL21
16	JANM38510/00901CCA	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F155	ML143
17	JANM38510/00901CCB	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F155	ML143
18	JANM38510/00901CCC	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F155	ML143
19	JANM38510/00901CDA	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL22
20	JANM38510/00901CDB	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL22
21	JANM38510/00901CDC	4	1	PPS	16M	BTX	422m	0.0	5.0	.80	2.0	49n	16m	.40	5	C	F36	FL22
22	JANM38510/00901STD	4	1	PPS	16M*	BTX	315m	0.0	5.0	.80	2.0	49nΔ	16m	.40	5	C	F36	FL35
23▼	JANM38510/30606AAA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
24▼	JANM38510/30606AAB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
25▼	JANM38510/30606AAC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
26▼	JANM38510/30606ABA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
27▼	JANM38510/30606ABB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
28▼	JANM38510/30606ABC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
29▼	JANM38510/30606ACA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
30▼	JANM38510/30606ACB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
31▼	JANM38510/30606ACC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
32▼	JANM38510/30606ADA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL22
33▼	JANM38510/30606ADB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL22
34▼	JANM38510/30606ADC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL22
35▼	JANM38510/30606ABAA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
36▼	JANM38510/30606BAB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
37▼	JANM38510/30606BAC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
38▼	JANM38510/30606BBA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
39▼	JANM38510/30606BBB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
40▼	JANM38510/30606BBC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
41▼	JANM38510/30606BCA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
42▼	JANM38510/30606BCB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
43▼	JANM38510/30606BCC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
44▼	JANM38510/30606BDA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL22
45▼	JANM38510/30606BDB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL22
46▼	JANM38510/30606BDC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL22
47▼	JANM38510/30606CAA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
48▼	JANM38510/30606CAB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
49▼	JANM38510/30606CAC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21a
50▼	JANM38510/30606CBA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
51▼	JANM38510/30606CBB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
52▼	JANM38510/30606CBC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	FL21
53▼	JANM38510/30606CCA	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
54▼	JANM38510/30606CCB	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142
55▼	JANM38510/30606CCC	4	1	PPS	18M*	BTD	137m	0.0	5.5	.70	2.0	91n	4.0m	.40	5	C	F282	ML142



# 7. SHIFT REGISTERS

IN ORDER OF (1) No. BITS/REG (2) No. REGISTERS  
(3) OP. CODE (4) MAX. W/C FREQ. (5) STRUCT (6) TYPE No

LINE No.	6 TYPE No.	ORGANIZATION		3 OPER. CODE	4 MAX WORST CASE FREQ. (Hz)	5 STRUC. TURE CODE	MAX OPER. POWER DISS. (W)	RATED POWER SUP.		INPUT LOGIC LEVELS		MAX PROP. DELAY (s)	MIN OUTPUT SINK CURRENT @ OUT (V)	MIN CLOCK FREQ. (Hz)	OPER. TEMP. RANGE CODE	DRAWINGS	
		1 BITS PER REGISTER	2 No. REGS					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)					LOGIC/BLOCK	OUTLINE
1▼	JANM385 10/30603ADC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL22
2▼	JANM385 10/30603BAA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21a
3▼	JANM385 10/30603BAB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21a
4▼	JANM385 10/30603BAC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21a
5▼	JANM385 10/30603BBA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21
6▼	JANM385 10/30603BBB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21
7▼	JANM385 10/30603BBC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21
8▼	JANM385 10/30603BCA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	ML142
9▼	JANM385 10/30603BCB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	ML142
10▼	JANM385 10/30603BCC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	ML142
11▼	JANM385 10/30603BDA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL22
12▼	JANM385 10/30603BDB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL22
13▼	JANM385 10/30603BDC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL22
14▼	JANM385 10/30603CAA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21a
15▼	JANM385 10/30603CAB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21a
16▼	JANM385 10/30603CAC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21a
17▼	JANM385 10/30603CBA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21
18▼	JANM385 10/30603CBB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21
19▼	JANM385 10/30603CBC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL21
20▼	JANM385 10/30603CCA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	ML142
21▼	JANM385 10/30603CCB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	ML142
22▼	JANM385 10/30603CCC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	ML142
23▼	JANM385 10/30603CDA	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL22
24▼	JANM385 10/30603CDB	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL22
25▼	JANM385 10/30603CDC	4	1	PPS	20M*	BTD	116m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F155	FL22
26▼	JANM385 10/30607AEA	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
27▼	JANM385 10/30607AEB	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
28▼	JANM385 10/30607AEC	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
29▼	JANM385 10/30607AFA	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
30▼	JANM385 10/30607AFB	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
31▼	JANM385 10/30607AFC	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
32▼	JANM385 10/30607BEA	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
33▼	JANM385 10/30607BEB	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
34▼	JANM385 10/30607BEC	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
35▼	JANM385 10/30607BFA	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
36▼	JANM385 10/30607BFB	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
37▼	JANM385 10/30607BFC	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
38▼	JANM385 10/30607CEA	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
39▼	JANM385 10/30607CEB	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
40▼	JANM385 10/30607CEC	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	ML143
41▼	JANM385 10/30607CFA	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
42▼	JANM385 10/30607CFB	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
43▼	JANM385 10/30607CFC	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
44	SN54LS295AJ	4	1	PPS	20M*	BTD	159m	0.0	5.5	.70	2.0	56n	4.0m	.40	5 C	F283	FL31
45	SN54LS295AW	4	1	PPS	20M*	BTD	70mt	0.0	5.0	.70	2.0s	70m	4.0m	.40	5 C	F282	ML66b
46	SN74LS295AJ	4	1	PPS	20M*	BTD	70mt	0.0	5.0	.80	2.0s	70m	4.0m	.40	0 7	F282	MO004AA
47	SN74LS295AN	4	1	PPS	20M*	BTD	70mt	0.0	5.0	.80	2.0s	70m	4.0m	.40	0 7	F282	ML66b
48#	M53295P	4	1	PPS	20M*	BTX	82m	0.0	5.0	.80	2.0	35n	16m	.40	0 7	F155	ML86b
49#	MIC5495AJ	4	1	PPS	20M*	BTX	250mt	0.0	5.0	.80	2.0	35n	16m	.40	5 C	F70	TO116
50#	MIC6495AJ	4	1	PPS	20M*	BTX	250mt	0.0	5.0	.80	2.0	35n	16m	.40	4 8	F70	TO116
51#	MIC7495AJ	4	1	PPS	20M*	BTX	250mt	0.0	5.0	.80	2.0	35n	16m	.40	0 7	F70	TO116
52#	MIC7495AN	4	1	PPS	20M*	BTX	250mt	0.0	5.0	.80	2.0	35n	16m	.40	0 7	F70	ML9
53	SM61	4	1	PPS	20M*	BTX	40m%	0.0	5.0	.80	2.0	40n	250uΔ	5.5	6 k	F250	ML9
54	SM63	4	1	PPS	20M*	BTX	40m%	0.0	5.0	.80	2.0	35n	250uΔ	5.5	6 k	F250	ML9
55	SM71	4	1	PPS	20M*	BTX	40m%	0.0	5.0	.80	2.0	40n	250uΔ	5.5	6 k	F250a	ML9
56	SM73	4	1	PPS	20M*	BTX	40m%	0.0	5.0	.80	2.0	40n	250uΔ	5.5	6 k	F250a	ML9
57	SN7495N	4	1	PPS	20M*	BTX	250mt	0.0	5.0	.80	2.0	35n	16m	.40	0 7	F70	ML71
58	SN7495J	4	1	PPS	20M*	BTX	410m	0.0	5.0	.80	2.0	35n	16m	.40	0 7	F36	ML93
59	SN7495N	4	1	PPS	20M*	BTX	410m	0.0	5.0	.80	2.0	35n	16m	.40	0 7	F36	ML64a
60#	T150B1	4	1	PPS	20M	BTX	300m	0.0	5.0	.85	1.6	45n	9.6m	.45	0 7	F2	ML80
61#	T150D1	4	1	PPS	20M	BTX	300m	0.0	5.0	.85	1.6	45n	9.6m	.45	0 7	F2	ML94
62#	T150D2	4	1	PPS	20M	BTX	300m	0.0	5.0	.90	1.4	45n	9.6m	.40	5 C	F2	ML94
63	MC7270L	4	1	PPS	22M%	BTX	180mt	0.0	5.0	.40%	2.6	40n	12m	.40	0 7	F235	TO116
64	MC7270P	4	1	PPS	22M%	BTX	180mt	0.0	5.0	.40%	2.6	40n	12m	.40	0 7	F235	ML124
65	MC7271L	4	1	PPS	22M%	BTX	180mt	0.0	5.0	.40%	2.6	40n	12m	.40	0 7	F235a	ML60b
66	MC7271P	4	1	PPS	22M%	BTX	180mt	0.0	5.0	.40%	2.6	40n	12m	.40	0 7	F235a	ML5b
67	MC8270L	4	1	PPS	22M%	BTX	180mt	0.0	5.0	.40%	2.6	40n	12m	.40	5 C	F235	TO116





















7. SHIFT REGISTERS

IN ORDER OF(1)NO.BITS/REG(2)NO.REGISTERS (3)OP.CODE(4)MAX W/C FREQ(5)STRUCT(6)I/TYPE No

Table with columns: LINE No., TYPE No., ORGANIZATION (1: BITS PER REGISTER, 2: No. REGS), OPER. CODE, MAX WORST CASE FREQ., STRUCTURE CODE, MAX OPER. POWER DISS., RATED POWER SUP. SPAN (NEG. POS.), INPUT LOGIC LEVELS (MAX MIN), MAX PROP. DELAY, MIN OUTPUT SINK CURRENT, MIN CLOCK FREQ., OPER. TEMP. RANGE, DRAWINGS (LOGIC/BLOCK, OUTLINE).























# 7. SHIFT REGISTERS

IN ORDER OF(1)No.BITS/REG(2)No.REGISTERS  
(3)OP.CODE(4)MAX W/C FREQ(5)STRUCT(6)TYPE No

LINE No.	TYPE No.	ORGANIZATION		OPER. CODE	MAX WORST CASE FREQ. (Hz)	STRUCTURE CODE	MAX OPER. POWER DISS. (W)	RATED POWER SUP.		INPUT LOGIC LEVELS		MAX PROP. DELAY (s)	MIN OUTPUT SINK CURRENT @ OUT (A)	MIN CLOCK FREQ. (Hz)	OPER. TEMP. RANGE		DRAWINGS			
		BITS PER REGISTER	No. REGS					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)				-	+	LOGIC/BLOCK	OUTLINE		
1	AM3341DC	64	4	SSD		MPS		12	5.0	80	4.0	250nt	1.6m	.40	0	7	F294	ML127k		
2	AM3341DM	64	4	SSD		MPS		12	5.0	80	4.0	250nt	1.6m	.40	5	7	C	ML62c		
3	AM3341PC	64	4	SSD		MPS		12	5.0	80	4.0	250nt	1.6m	.40	0	7		ML89a		
4	MM4105H	64	4	SSD	1.4M	MPX	136m	12	5.0	80	3.5	200n	1.6m	.40	10k	5	C	F68c		
5	MMS105H	64	4	SSD	1.4M	MPX	136m	12	5.0	80	3.5	200n	1.6m	.40	10k	2	7	F68c	TO100	
6	TMS3417JC	64	4	SSD	5.0M	MPX	400m	12	5.0	80	3.0	160n	1.6m	.40	10k	2	8	F119	ML82	
7	TMS3417NC	64	4	SSD	5.0M	MPX	400m	12	5.0	80	3.0	160n	1.6m	.40	10k	2	8	F119	ML48a	
8#	GZF1106D	64	4	SSS	2.0M	MCX	100u	0.0	5.0	1.5	3.5	350n	1.6m	.40	4	8	F327	ML134a		
9#	GZF1106P	64	4	SSS	4.0M	MCX	500u	0.0	10.0	3.0	7.0	175n	1.6m	.40	4	8	F318	ML		
10	3326-4-5E	66	3	SSD	3.0M	MPX	180m	13	0.0	-3.0	-9.0	200n			5	8	F73	TO100		
11#	GWN105	68	1	SSD	500k	MPX				-10	-2.0		1.5m	23	0	7	F104	TO73		
12	DL1-2080	80	2	SSD	2.0M	MPT	200m	12	12		11#				0	7	F19	ML9		
13	MM4007AAD	80	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F332	ML199	
14	MM4007AAH	80	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F256	TO79	
15	MMS007AAD	80	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F332	ML179	
16	MM5007AAH	80	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F256	TO99	
17	MM4052H	80	2	SSS	1.6M	MPX	212m	12	5.0	80	3.0	300n	1.6m	.40	5	8	F71	TO100		
18	MMS052H	80	2	SSS	1.6M	MPX	212m	12	5.0	80	3.0	300n	1.6m	.40	0	7	F71	TO100		
19	MM4021D	80	3	SSD	2.5M	MPX	163m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F140b	ML	
20	MM4021H	80	3	SSD	2.5M	MPX	163m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F68a	TO100	
21	MM4021N	80	3	SSD	2.5M	MPX	163m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F140b	ML2e	
22	MM5021D	80	3	SSD	2.5M	MPX	163m	12	5.0	80	3.0	200n	1.6m	.40	10k	2	7	F140b	ML	
23	MM5021H	80	3	SSD	2.5M	MPX	163m	12	5.0	80	3.0	200n	1.6m	.40	10k	2	7	F68a	TO100	
24	MM5021N	80	3	SSD	2.5M	MPX	163m	12	5.0	80	3.0	200n	1.6m	.40	10k	2	7	F140b	ML2e	
25	MK1007P	80	4	SSD	2.5M	MPI	220m	12	5.0	80	3.5	200n	1.6m	.40	10k	0	7	F152	ML22	
26	MMS023D	80	4	SSD	2.5M	MPI	430m	12	5.0	80	3.5	200n	1.6m	.40	10k	0	7	F334	ML177	
27	MMS023N	80	4	SSD	2.5M	MPI	430m	12	5.0	80	3.5	200n	1.6m	.40	10k	0	7	F334	ML178	
28	MM4020D	80	4	SSD	2.5M	MPX	212m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F140a	ML	
29	MM4020N	80	4	SSD	2.5M	MPX	212m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F140a	ML2e	
30	MMS020D	80	4	SSD	2.5M	MPX	212m	12	5.0	80	3.0	200n	1.6m	.40	10k	2	7	F140a	ML	
31	MMS020N	80	4	SSD	2.5M	MPX	212m	12	5.0	80	3.0	200n	1.6m	.40	10k	2	7	F140a	ML2e	
32	TMS3120JC	80	4	SSD	2.5M	MPX	355m	12	5.0	80	3.4	400n	1.6m	.40	2	8	F119	ML206		
33	TMS3120NC	80	4	SSD	2.5M	MPX	355m	12	5.0	80	3.4	400n	1.6m	.40	2	8	F119	ML209		
34	TMS3409JC	80	4	SSD	5.0M	MPX	400m	12	5.0	80	3.0	160n	1.6m	.40	10k	2	8	F119	ML82	
35	TMS3409NC	80	4	SSD	5.0M	MPX	400m	12	5.0	80	3.0	160n	1.6m	.40	10k	2	8	F119	ML48a	
36	2532B	80	4	SSS	1.5M	MPG	640m	12	5.0	60	3.4	400n	1.6m	.50	0	7	F303	ML132		
37	ITT3347	80	4	SSS	2.0M	MPG	450m	12	5.0	80	4.0	200n	1.6m	.40	0	7	F119	ML		
38	2532-1B	80	4	SSS	2.5M	MPG		5.0	0.0						0	7	F400	ML85		
39#	M142B1	80	4	SSS	3.0M	MNG	250m	0.0	5.0	80	2.0	230n	1.6m	.55	0	7	F152	ML60		
40#	M142D1	80	4	SSS	3.0M	MNG	250m	0.0	5.0	80	2.0	230n	1.6m	.55	0	7	F152	ML158		
41	ITT3357	80	4	SSS	3.0M	MPG	450m	12	5.0	80	4.0	200n	1.6m	.40	0	7	F119	ML		
42	TMS3126LC	96	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	.40	2	8	F240	TO99		
43	TMS3126NC	96	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	.40	2	8	F240	ML208		
44	MM4007XXD	100	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F332	ML179	
45	MM4007XXH	100	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F256	TO99	
46	MM5007XXD	100	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F332	ML179	
47	MM5007XXH	100	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F256	TO99	
48	1406T	100	2	SSD		MPG	80m	5.0	5.0	80	2.5	100n	200u	.40	1.0M	#	5	C	TO99	
49	1407T	100	2	SSD		MPG	80m	5.0	5.0	80	2.5	100n	200u	.40	1.0M	#	5	C	TO99	
50	1506T	100	2	SSD		MPG	80m	5.0	5.0	80	2.5	100n	200u	.40	1.0M	#	0	7	TO99	
51	1507T	100	2	SSD		MPG	80m	5.0	5.0	80	2.5	100n	200u	.40	1.0M	#	0	7	TO99	
52#	HD3506	100	2	SSD		MPG	600m	5.0	5.0	80	3.5				10k	2	7	F205	ML90a	
53#	HD3507	100	2	SSD		MPG	600m	5.0	5.0	80	3.5				10k	2	7	F205	ML90a	
54	MM406H	100	2	SSD	1.0M	MPX	500m	5.0	5.0	2.0	-7.0		1.6m	.40	600	#	5	C	F54d	TO99
55	MM407H	100	2	SSD	1.0M	MPX	500m	5.0	5.0	2.0	-7.0		1.6m	.40	600	#	5	C	F54e	TO99
56	MMS06H	100	2	SSD	1.0M	MPX	500m	5.0	5.0	2.0	-7.0		1.6m	.40	600	#	2	7	F54d	TO99
57	MMS07H	100	2	SSD	1.0M	MPX	500m	5.0	5.0	2.0	-7.0		1.6m	.40	600	#	2	7	F54e	TO99
58	AM1406HM	100	2	SSD	2.0M	MPG	500m	5.0	5.0	80	2.5	100n			80	5	C	F256	TO99	
59	AM1407HM	100	2	SSD	2.0M	MPG	500m	5.0	5.0	80	2.5	100n	200u	.40	80	5	C	F256	TO99	
60	AM1506HC	100	2	SSD	2.0M	MPG	500m	5.0	5.0	80	2.5	100n			80	0	7	F256	TO99	
61	AM1507HC	100	2	SSD	2.0M	MPG	500m	5.0	5.0	80	2.5	100n	200u	.40	80	0	7	F256	TO99	
62	M1406	100	2	SSD	2.0M	MPG	110m	5.0	5.0	80	3.5	100n	1.6m	.40	6.0k	5	C	F133b	CY4a	
63	M1407	100	2	SSD	2.0M	MPG	110m	5.0	5.0	80	3.5	100n	1.6m	.40	6.0k	5	C	F133b	CY4a	
64	M1506	100	2	SSD	2.0M	MPG	110m	5.0	5.0	80	3.5	100n	1.6m	.40	6.0k	0	7	F133b	CY4a	
65	M1507	100	2	SSD	2.0M	MPG	110m	5.0	5.0	80	3.5	100n	1.6m	.40	6.0k	0	7	F133b	CY4a	
66	MM4006AD	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F332	ML179	
67	MM4006AH	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F256	TO99	
68	MM4007D	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F332	ML179	
69	MM4007H	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	5	C	F256	TO99	
70	MM5006AD	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F332	ML179	
71	MM5006AH	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F256	TO99	
72	MM5007D	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F332	ML179	
73	MM5007H	100	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	.40	10k	0	7	F256	TO99	
74	2506T	100	2	SSD	3.0M	MPG	535m	5.0	5.0	1.0	3.2	150n			600	#	0	7	F127	CY4c
75	2506V	100	2	SSD	3.0M	MPG	455m	5.0	5.0	1.0	3.2	150n			600	#	0	7	F127a	ML87
76	2507T	100	2	SSD	3.0M	MPG	535m	5.0	5.0	1.0	3.2	150n			600	#	0	7	F127	CY4c
77	2507V	100	2	SSD	3.0M	MPG	455m	5.0	5.0	1.0	3.2									

# 7. SHIFT REGISTERS

IN ORDER OF (1)No.BITS/REG(2)No.REGISTERS  
(3)OP.CODE(4)MAX W/C FREQ(5)STRUCT(6)TYPE No

LINE No.	TYPE No.	ORGANIZATION	1		3	4	5	MAX	RATED		INPUT LOGIC		MAX	MIN		MIN	OPER.	DRAWINGS	
			BIT	No.					OPER.	POWER	NEG.	POS.		MAX	MIN			OUTPUT	CLOCK
			REGS	CODE	OP	STRUC	DISS.	SPAN	'0'	'1'	DELAY	SINK	CLOCK	RANGE	CODE	LOGIC/	OUTLINE		
							(W)	(V)	(V)	(V)	(s)	CURRENT	FREQ.						
1	MM5060AAN	128	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML116b	
2	MTS2107	128	2	SSS	1.5M	MPN	250m	12	5.0	70	3.5	250n	1.6m	40	0	7	F95h	TO99	
3	AM2809HM	128	2	SSS	2.0M	MPG	528m	12	5.0	1.1	3.3	350n	1.6m	40	5	5	F289	TO99	
4	AM2810DC	128	2	SSS	2.0M*	MPG	245m	12	5.0	1.0	4.0	250n	1.6m	40	0	7	F290a	ML127k	
5	AM2810DM	128	2	SSS	2.0M*	MPG	355m	12	5.0	1.0	4.0	250n	1.6m	40	5	5	F290a	ML62c	
6	MTS2108	128	2	SSS	2.0M	MPN	340m	12	5.0	70	3.5	240n	1.6m	40	5	5	F95h	TO99	
7	SL5-C2100-16	128	2	SSS	2.0M	MPN	350m	12	5.0	80	3.5	200n	1.6m	40	0	7	F25	TO78	
8	SL5-C2128-12	128	2	SSS	2.0M	MPN	500m	12	5.0	80	3.5	300n	1.6m	40	0	7	F25d	ML250	
9	SL5-C2128-16	128	2	SSS	2.0M	MPN	500m	12	5.0	80	3.5	300n	1.6m	40	0	7	F25	TO78	
10	TMS3114JC	128	2	SSS	2.0M	MPT	360m	12	5.0	60	3.5	350n	1.6m	50	2	8	F115	ML82	
11	TMS3114NC	128	2	SSS	2.0M	MPT	360m	12	5.0	60	3.5	350n	1.6m	50	2	8	F115	ML48a	
12	AM2809HC	128	2	SSS	2.5M	MPG	456m	12	5.0	1.1	3.3	300n	1.6m	40	0	7	F289	TO99	
13	AM2809PC	128	2	SSS	2.5M	MPG	456m	12	5.0	1.1	3.3	300n	1.6m	40	0	7	F289	ML163	
14	AM2814DC	128	2	SSS	2.5M*	MPG	255m	13	5.0	60	3.5	250n	1.6m	40	2	8	F291	ML62c	
15	AM2814DM	128	2	SSS	2.5M*	MPG	364m	12.6	5.0	60	3.5	250n	1.6m	40	5	5	F291	ML62c	
16	AM2814PC	128	2	SSS	2.5M*	MPG	255m	13	5.0	60	3.5	250n	1.6m	40	2	8	F291	ML89a	
17	TMS3128LC	128	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	TO99	
18	TMS3128NC	128	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	ML208	
19	MM4055D	128	4	SSS	1.0M	MPG	163m	12	5.0	80	4.0	700n	1.6m	40	5	5	F340	ML177	
20	MM5055D	128	4	SSS	1.0M	MPG	163m	12	5.0	80	4.0	700n	1.6m	40	0	7	F340	ML177	
21	MM5055N	128	4	SSS	1.0M	MPG	163m	12	5.0	80	4.0	700n	1.6m	40	0	7	F340	ML178	
22	AM2855DC	128	4	SSS	2.5M	MPG	280m	12	5.0	80	4.0	280n	1.6m	40	0	7	F292	ML127k	
23	AM2855DM	128	4	SSS	2.5M	MPG	280m	12	5.0	80	4.0	280n	1.6m	40	5	5	F292	ML62c	
24	AM2855PC	128	4	SSS	2.5M	MPG	280m	12	5.0	80	4.0	280n	1.6m	40	0	7	F292	ML89a	
25	SL9-4128-28#1	128	4	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147	ML64	
26	SL9-4128-69#1	128	4	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147	ML9	
27	SL9-4128-28#2	128	4	SSS	4.0M	MPG	380m	12	5.0	80	3.5	260n	1.6m	50	0	7	F147	ML64	
28	SL9-4128-69#2	128	4	SSS	4.0M	MPG	380m	12	5.0	80	3.5	260n	1.6m	50	0	7	F147	ML9	
29	2522V	132	2	SSS	1.5M	MPG	535m	12	5.0	60	3.4	350n	1.6m	50	0	7	F130	ML87	
30	MM5060ABD	132	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML202	
31	MM5060ABN	132	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML116b	
32	TMS3129LC	132	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	TO99	
33	TMS3129NC	132	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	ML208	
34	MM5060ACD	133	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML202	
35	MM5060ACN	133	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML116b	
36	TMS3113JC	133	2	SSS	2.0M	MPT	360m	12	5.0	60	3.5	350n	1.6m	50	2	8	F115	ML82	
37	TMS3113NC	133	2	SSS	2.0M	MPT	360m	12	5.0	60	3.5	350n	1.6m	50	2	8	F115	ML48a	
38	TMS3130LC	133	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	TO99	
39	TMS3130NC	133	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	ML208	
40	MM5060ADD	144	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML202	
41	MM5060ADN	144	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML116b	
42	MM5060XDD	144	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML202	
43	MM5060XDN	144	2	SSS	1.5M	MPG	442m	12	5.0	80	3.5	350n	1.6m	40	0	7	F130	ML116b	
44	TMS3132LC	144	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	TO99	
45	TMS3132NC	144	2	SSS	2.5M	MPT	510m	12	5.0	1.1	3.2	250n	1.6m	40	2	8	F240	ML208	
46	CD4062AH	200	1	SSD	2.0M	MCX	15m	0.0	10	0.5%	9.95	800n	3.2m	50	150	5	C	F262	CH10
47	CD4062AK	200	1	SSD	2.0M	MCX	15m	0.0	10	0.5%	9.95	800n	3.2m	50	150	5	C	F262	MO002AG
48	CD4062AT	200	1	SSD	2.0M	MCX	15m	0.0	10	0.5%	9.95	800n	3.2m	50	150	5	C	F262	MO006AG
49	2511A	200	2	SSS	1.5M	MPG	535m	5.0	5.0	60	3.4	300n	1.6m	50	0	7	F128a	ML86	
50	2511K	200	2	SSS	1.5M	MPG	535m	5.0	5.0	60	3.4	300n	1.6m	50	0	7	F128a	CY7	
51	2529V	240	2	SSS	1.5M	MPG	535m	5.0	5.0	60	3.4	300n	1.6m	50	0	7	F128a	ML163	
52	2528V	250	2	SSS	1.5M	MPG	535m	5.0	5.0	60	3.4	300n	1.6m	50	0	7	F128a	ML163	
53#	HD3510	256	1	SSD	1.5M	MPG	600m	5.0	5.0	80	3.5	300n	1.6m	50	10k	2	7	F134e	ML90a
54	3383-9-5F	256	1	SSD	2.0M	MPG	155m	12	5.0	85	4.0	150n	1.6m	40	10k	0	7	F76	TO100
55	MM4019XDD	256	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F332	ML179
56	MM4019XXH	256	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F256	TO99
57	MM5019XDD	256	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F332	ML179
58	MM5019XXH	256	1	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F256	TO99
59#	M125T1	256	2	SSD	1.0M	MPX	160m	27	0.0	-2.0	-1.0	350n	10u	-1.0	10k	0	7	F49	TO100
60	MM4012D	256	2	SSD	2.5M	MPX	272m	12	5.0	80	3.0	250n	1.6m	40	10k	5	C	F195	ML7
61	MM4012N	256	2	SSD	2.5M	MPX	272m	12	5.0	80	3.0	250n	1.6m	40	10k	5	C	F195	ML2e
62	MM4019D	256	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F332	ML179
63	MM4019H	256	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F256	TO99
64	MM5012D	256	2	SSD	2.5M	MPX	272m	12	5.0	80	3.0	250n	1.6m	40	10k	2	7	F195	ML7
65	MM5012N	256	2	SSD	2.5M	MPX	272m	12	5.0	80	3.0	250n	1.6m	40	10k	2	7	F195	ML2e
66	MM5019D	256	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F332	ML179
67	MM5019H	256	2	SSD	2.5M	MPX	204m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F256	TO99
68	2527V	256	2	SSS	1.5M	MPG	535m	12	5.0	60	3.4	450n	1.6m	50	0	7	F130	ML163	
69	AM2856HC	256	2	SSS	2.5M	MPG	535m	12	5.0	80	4.0	280n	1.6m	40	0	7	F292a	TO100	
70	AM2856HM	256	2	SSS	2.5M	MPG	535m	12	5.0	80	4.0	280n	1.6m	40	5	5	F292a	TO100	
71	SL9-2256-23#1	256	2	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147b	TO100	
72	SL9-2256-28#1	256	2	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147d	ML64	
73	SL9-2256-69#1	256	2	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147d	ML9	
74	SL9-2256-23#2	256	2	SSS	4.0M	MPG	380m	12	5.0	80	3.5	260n	1.6m	50	0	7	F147b	TO100	
75	SL9-2256-28#2	256	2	SSS	4.0M	MPG	380m	12	5.0	80	3.5	260n	1.6m	50	0	7			

# 7. SHIFT REGISTERS

IN ORDER OF (1) No. BITS (REG/2) No. REGISTERS  
(3) OP. CODE (4) MAX W/C FREQ (5) STRUCT (6) TYPE No

LINE No.	TYPE No.	ORGANIZATION		OP. CODE	WORST CASE FREQ. (Hz)	STRUCTURE CODE	MAX OPER. POWER DISS. (W)	RATED POWER SUP.		INPUT LOGIC LEVELS		MAX PROP. DELAY (s)	MIN OUTPUT SINK CURRENT		MIN. CLOCKS FREQ. (Hz)	OPER. TEMP. RANGE CODE	DRAWINGS			
		BITS PER REGISTER	No. REGS					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)		@ OUT	LOGIC/ BLOCK			OUTLINE			
																		1	2	1
1	C2416	256	64	SSC	2.0M	MNG	1.0	5.0	5.0	80%	3.5	40nΔ	3.0m	45	0	7	F325	ML8d		
2	P2416	256	64	SSC	2.0M	MNG	1.0	5.0	5.0	80%	3.5	40nΔ	3.0m	45	0	7	F325	ML3		
3	MM4104H	360s	1	SSD	2.5M	MPX	170m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F68b	TO100	
4	MM5104H	360s	1	SSD	2.0M	MPX	170m	12	5.0	80	3.0	200n	1.6m	40	10k	2	7	F68b	TO100	
5	3330-9.5F	480	1	SSD	2.0M	MPG	250m	0.0	5.0	85	4.0	150n	1.6m	40	10k	0	7	F74	TO100	
6	3331-9.5F	500	1	SSD	2.0M	MPG	250m	0.0	5.0	85	4.0	150n	1.6m	40	10k	0	7	F74	TO100	
7#	HD3505	512	1	SSD	2.0M	MPG	600m	5.0	5.0	80	3.5	150n	1.6m	40	10k	2	7	F204	ML88a	
8	3329-9.5F	512	1	SSD	2.0M	MPG	250m	5.0	5.0	80	4.0	150n	1.6m	40	10k	0	7	F74	TO100	
9	M1405A	512	1	SSD	2.0M	MPG	400m	5.0	5.0	80	3.5	250n	1.6m	50	10k	0	7	F30	CY7a	
10	T405K	512	1	SSD	2.0M	MPX	535m	5.0	5.0	80	3.0	30u*	1.6m	40	500	0	7	F401	CY7c	
11	2505K	512	1	SSD	2.5M	MPG	535m	5.0	5.0	80	3.0	100m	1.6m	40	500	0	7	F401	CY7c	
12	MM4016D	512s	1	SSD	2.5M	MPX	170m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F223	ML177	
13	MM4016H	512s	1	SSD	2.5M	MPX	170m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F65	TO100	
14	MM5016D	512s	1	SSD	2.5M	MPX	170m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F223	ML177	
15	MM5016H	512s	1	SSD	2.5M	MPX	170m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F65	TO100	
16	MM5016N	512s	1	SSD	2.5M	MPX	170m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F134b	ML116b	
17	2524V	512	1	SSD	3.0M	MPG	535m	5.0	5.0	80	3.0	100m	1.6m	50	500	0	7	F131	ML87	
18	AM2805HM	512	1	SSD	3.0M	MPG	60m*	5.0	5.0	80	3.0	5.0n*	1.0uΔ	40	400	0	7	F288	TO100	
19	1405A	512	1	SSD	4.0M	MPG	60m*	5.0	5.0	80	3.0	5.0n*	1.0uΔ	40	400	0	7	F288	TO100	
20	AM2805HC	512	1	SSD	4.0M	MPG	60m*	5.0	5.0	80	3.0	5.0n*	1.0uΔ	40	400	0	7	F288	TO100	
21	AM2807PC	512	1	SSD	4.0M	MPG	60m*	5.0	5.0	80	3.0	5.0n*	1.0uΔ	40	400	0	7	F288b	ML163	
22	MT52100	512	1	SSD	5.0M	MPN	300m	5.0	5.0	1.0	3.5	80n	1.6m	40	10k	2	8	F120k	TO100	
23	MM4057D	512	1	SSS	1.0M	MPG	163m	12	5.0	80	4.0	700n	1.6m	40	0	7	F340b	ML202		
24	MM5057D	512	1	SSS	1.0M	MPG	163m	12	5.0	80	4.0	700n	1.6m	40	0	7	F340b	ML202		
25	MM5057N	512	1	SSS	1.0M	MPG	163m	12	5.0	80	4.0	700n	1.6m	40	0	7	F340b	ML116b		
26	AM2857DC	512	1	SSS	2.5M	MPG	200m	12	5.0	80	4.0	280n	1.6m	40	0	7	F292b	ML164		
27	AM2857DM	512	1	SSS	2.5M	MPG	200m	12	5.0	80	4.0	280n	1.6m	40	5	C	F292b	ML164		
28	AM2857PC	512	1	SSS	2.5M	MPG	200m	12	5.0	80	4.0	280n	1.6m	40	0	7	F292b	ML163		
29	SL9-1512-23#1	512Δ	1	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147a	TO100		
30	SL9-1512-28#1	512Δ	1	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147c	ML64		
31	SL9-1512-69#1	512Δ	1	SSS	2.5M	MPG	200m	5.0	5.0	80	3.5	340n	1.6m	50	0	7	F147c	ML9		
32	SL9-1512-23#2	512Δ	1	SSS	4.0M	MPG	380m	12	5.0	80	3.5	260n	1.6m	50	0	7	F147a	TO100		
33	SL9-1512-28#2	512Δ	1	SSS	4.0M	MPG	380m	12	5.0	80	3.5	260n	1.6m	50	0	7	F147c	ML64		
34	SL9-1512-69#2	512Δ	1	SSS	4.0M	MPG	380m	12	5.0	80	3.5	260n	1.6m	50	0	7	F147c	ML9		
35#	HD3503	512	2	SSD	2.5M	MPG	600m	5.0	5.0	80	3.5	340n	1.6m	50	10k	2	7	F29a	ML90a	
36	DL9-2512-23#1	512Δ	2	SSD	2.5M	MPG	250m	5.0	5.0	80	3.5	340n	1.6m	50	10k	0	7	F147b	TO100	
37	DL9-2512-28#1	512Δ	2	SSD	2.5M	MPG	250m	5.0	5.0	80	3.5	340n	1.6m	50	10k	0	7	F147d	ML64	
38	DL9-2512-69#1	512Δ	2	SSD	2.5M	MPG	250m	5.0	5.0	80	3.5	340n	1.6m	50	10k	0	7	F147d	ML9	
39	1403ATA	512	2	SSD	2.5M	MPX	238m	5.0	5.0	80	3.0	10u*	1.6m	40	0	7	F133	CY4c		
40	1403AV	512	2	SSD	2.5M	MPX	238m	5.0	5.0	80	3.0	10u*	1.6m	40	0	7	F134	ML163		
41	MM4017D	512s	2	SSD	2.5M	MPX	238m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F225	ML177	
42	MM4017H	512s	2	SSD	2.5M	MPX	238m	12	5.0	80	3.0	200n	1.6m	40	10k	5	C	F333	TO100	
43	MM5017D	512s	2	SSD	2.5M	MPX	238m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F225	ML177	
44	MM5017H	512s	2	SSD	2.5M	MPX	238m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F333	TO100	
45	MM5017N	512s	2	SSD	2.5M	MPX	238m	12	5.0	80	3.0	200n	1.6m	40	10k	0	7	F225	ML178	
46	AM1403A59F#2	512	2	SSD	3.0M	MPG	200m*	9.0	5.0	-1.0	-2.0	110n	1.6m	50	15M#	0	7	F255a	FL33a	
47	AM1403A#2	512	2	SSD	3.0M	MPG	600m	9.0	5.0	80	3.0	110n	1.6m	50	15M#	0	7	F255a	T099	
48	AM1403A5H#2	512	2	SSD	3.0M	MPG	600m	9.0	5.0	80	3.0	110n	1.6m	50	10k	5	C	F255a	T099	
49	AM1403APC#2	512	2	SSD	3.0M	MPG	600m	9.0	5.0	80	3.0	110n	1.6m	50	100	0	7	F255a	ML89a	
50	DL9-1403A15#2	512	2	SSD	3.0M	MPG	600m	9.0	5.0	80	3.0	110n	1.6m	50	10k	0	7	F120a	T099	
51	DL9-1403A26#2	512	2	SSD	3.0M	MPG	600m	9.0	5.0	80	3.0	110n	1.6m	50	10k	0	7	F120c	ML	
52	DL9-1403A55#2	512	2	SSD	3.0M	MPG	600m	9.0	5.0	80	3.0	110n	1.6m	50	10k	0	7	F120c	ML65	
53	DL9-2512-23#2	512Δ	2	SSD	4.0M	MPG	440m	12	5.0	80	3.5	260n	1.6m	50	10k	0	7	F147b	TO100	
54	DL9-2512-28#2	512Δ	2	SSD	4.0M	MPG	440m	12	5.0	80	3.5	260n	1.6m	50	10k	0	7	F147d	ML64	
55	DL9-2512-69#2	512Δ	2	SSD	4.0M	MPG	440m	12	5.0	80	3.5	260n	1.6m	50	10k	0	7	F147d	ML9	
56	1403A	512	2	SSD	5.0M	MPG	500m	5.0	5.0	80	2.4	100n	1.6m	50	5	C	F29a	CY7d		
57	AM1403A#1	512	2	SSD	5.0M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	2.5M#	0	7	F255a	T099	
58	AM1403A5H#1	512	2	SSD	5.0M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	100	5	C	F255a	T099	
59	AM1403APC#1	512	2	SSD	5.0M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	100	0	7	F255a	ML89a	
60	DL9-1403A15#1	512	2	SSD	5.0M	MPG	525m	5.0	5.0	80	3.0	90n	1.6m	50	10k	0	7	F120a	T099	
61	DL9-1403A26#1	512	2	SSD	5.0M	MPG	525m	5.0	5.0	80	3.0	90n	1.6m	50	10k	0	7	F120c	ML	
62	DL9-1403A55#1	512	2	SSD	5.0M	MPG	525m	5.0	5.0	80	3.0	90n	1.6m	50	10k	0	7	F120c	ML65	
63#	M136T1	512	2	SSD	5.0M*	MPG	500m	5.0	5.0	80	3.5	60n†	1.6m	50	100	†	0	7	F29a	T099
64	M1403A	512	2	SSD	5.0M	MPG	500m	5.0	5.0	80	3.5	90n	1.6m	50	10k	0	7	F133	CY4f	
65	MM1403AH	512	2	SSD	5.0M	MPG	600m	5.0	5.0	80	3.3	90n	1.6m	50	10k	0	7	F133	T099	
66	MM1403AN	512	2	SSD	5.0M	MPG	600m	5.0	5.0	80	3.3	90n	1.6m	50	10k	0	7	F134c	ML116b	
67	2503TA	512	2	SSD	8.0M	MPG	535m	5.0	5.0	60	3.4	90n	1.6m	50	500	0	7	F29a	CY4d	
68	2503V	512	2	SSD	8.0M	MPG	535m	5.0	5.0	60	3.4	90n	1.6m	50	500	0	7	F29a	ML87	
69	AM1403A51F	512	2	SSD	10M	MPG	250m	5.0	5.0	-1.0	-2.0	90n	1.6m	50	2.5M#	5	C	F255a	FL33a	
70	AM1403A51T	512	2	SSD	10M	MPG	250m	5.0	5.0	-1.0	-2.0	90n	1.6m	50	2.5M#	5	C	F255a	T099	
71	AM1403A59F#1	512	2	SSD	10M	MPG	250m	5.0	5.0	-1.0	-2.0	90n	1.6m	50	2.5M#	0	7	F255a	FL33a	
72	AM2803HC	512	2	SSD	10M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	100	0	7	F255a	T099	
73	AM2803HM	512	2	SSD	10M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	100	5	C	F255a	T099	
74	AM2803PC	512	2	SSD	10M															

# 7. SHIFT REGISTERS

IN ORDER OF (1)No.BITS/REG(2)No.REGISTERS  
(3)OP.CODE(4)MAX W/C FREQ(5)STRUCT(6)TYPE No

LINE No.	TYPE No.	ORGANIZATION		3 OPER. CODE	4 MAX WORST CASE FREQ. (Hz)	5 STRUCTURE CODE	MAX OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MAX PROP. DELAY (s)	MIN OUTPUT SINK CURRENT @ OUT (A)	MIN CLOCK FREQ. (Hz)	OPER. TEMP. RANGE CODE	DRAWINGS			
		1 BITS PER REGISTER	2 No. REGS					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)					LOGIC/BLOCK	OUTLINE		
																		-	+
1	MT1404A	1024	1	SSD	5.0M	MPG	500m	5.0	5.0	80	3.5	90n	1.6m	50	10k	0	7	F133a	CY4f
2	MM1404AH	1024	1	SSD	5.0M	MPG	600m	5.0	5.0	80	3.3	90n	1.6m	50	10k	0	7	F133a	TO99
3	MM1404AN	1024	1	SSD	5.0M	MPG	600m	5.0	5.0	80	3.3	90n	1.6m	50	10k	0	7	F134d	ML116b
4	MM5024AH	1024	1	SSD	5.0M	MPG	600m	5.0	5.0	80	3.3	90n	1.6m	50	10k	0	7	F133c	TO99
5	2504TA	1024	1	SSD	8.0M	MPG	535m	5.0	5.0	60	3.4	90n	1.6m	50	500	0	7	F29b	CY4d
6	2504V	1024	1	SSD	8.0M	MPG	535m	5.0	5.0	60	3.4	90n	1.6m	50	500	0	7	F29b	ML87
7	AM1404A51F	1024	1	SSD	10M	MPG	250m	5.0	5.0	-10	-2.0	90n	1.6m	50	2.5M#	5	C	F255	FL33a
8	AM1404A51T	1024	1	SSD	10M	MPG	250m	5.0	5.0	-10	-2.0	90n	1.6m	50	2.5M#	5	C	F255	TO99
9	AM1404A59F#1	1024	1	SSD	10M	MPG	250m	5.0	5.0	-10	-2.0	90n	1.6m	50	2.5M#	0	7	F255	FL33a
10	AM2804HC	1024	1	SSD	10M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	100	0	7	F255	TO99
11	AM2804HM	1024	1	SSD	10M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	100	5	C	F255	TO99
12	AM2804PC	1024	1	SSD	10M	MPG	600m	5.0	5.0	80	3.0	90n	1.6m	50	100	0	7	F255	ML163
13	AM2533DC	1024	1	SSS	1.5M	MPG	150m	12	5.0	80	2.0	300n	1.6m	40	0	7	F287	ML164	
14	AM2533V	1024	1	SSS	1.5M	MPG	150m	12	5.0	80	3.2	300n	1.6m	40	0	7	F287	ML163	
15	MM5058N	1024	1	SSS	1.5M	MPG	456m	12	5.0	80	3.5	300n	1.6m	40	0	7	F238	ML116b	
16	2533V	1024	1	SSS	1.5M	MPX	535m	12	5.0	60	3.4	300n	1.6m	50	0	7	F238	ML87a	
17	AM2833DC	1024	1	SSS	2.0M	MPG	175m	12	5.0	80	2.0	300n	1.6m	40	0	7	F287	ML164	
18	AM2833DM	1024	1	SSS	2.0M	MPG	210m	12	5.0	80	2.0	300n	1.6m	40	5	C	F287	ML164	
19	AM2833PC	1024	1	SSS	2.0M	MPG	175m	12	5.0	80	2.0	300n	1.6m	40	0	7	F287	ML163	
20	AM3355DC	1024	1	SSS	4.0M	MPG	175m	12	5.0	40%	4.0	215m	1.6m	40	0	7	F287	ML164	
21	AM3355PC	1024	1	SSS	4.0M	MPG	175m	12	5.0	40%	4.0	215m	1.6m	40	0	7	F287	ML163	
22	C2401	1024	2	SSD	1.0k	MNG	1.0	0.0	5.0	65	2.2	500n	5.0m	45	0	7	F106	ML10c	
23	P2401	1024	2	SSD	1.0k	MNG	1.0	0.0	5.0	65	2.2	80n	6.3m	45	25k	0	7	F106	ML89a
24	MM4025D	1024	2	SSD	1.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	60k	5	C	F335	ML177
25	MM4026D	1024	2	SSD	1.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	60k	5	C	F336	ML177
26	AM9401DC	1024	2	SSD	2.0M	MNG	315m	0.0	5.0	65	2.2	320n	1.6m	45	25k	0	7	F106	ML127k
27	AM9401DM	1024	2	SSD	2.0M	MNG	315m	0.0	5.0	65	2.2	320n	1.6m	45	25k	5	C	F106	ML62c
28	AM9401PC	1024	2	SSD	2.0M	MNG	315m	0.0	5.0	65	2.2	320n	1.6m	45	25k	0	7	F106	ML89a
29	MM5025D	1024	2	SSD	3.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	10k	0	7	F335	ML177
30	MM5025N	1024	2	SSD	3.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	10k	0	7	F338	ML203
31	MM5026D	1024	2	SSD	3.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	10k	0	7	F336	ML177
32	MM5026N	1024	2	SSD	3.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	10k	0	7	F336	ML178
33	CCD450DC	1024	9	SSC	1.0M	MNG	250m	2.5	12	80	2.2	180n	2.0m	40	100k	0	5	F348	ML134c
34	CCD450ADC	1024	9	SSC	2.0M	MNG	250m	2.5	12	80	2.2	140n	2.0m	40	100k	0	5	F348	ML134c
35	MM4027F	2048	1	SSD	1.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	60k	5	C	F337	FL36
36	MM5027F	2048	1	SSD	3.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	10k	0	7	F337	FL36
37	MM5027N	2048	1	SSD	3.0M	MPG	409m	12	5.0	80	3.3	80n	1.6m	40	10k	0	7	F339	ML116b
38	TMS3064JL	4096	16	SSC	5.0M	MNG	260m	5.0	12	40%	2.7	15n	3.2m	40	400k	0	7	F473	ML206

# 20. SPECIAL MEMORY DEVICES

IN ORDER OF: (1) FUNCT CODE (2) NO. WORDS  
(3) BITS/WD (4) OP MODE (5) STRUCT & (6) TYPE NO.

LINE No.	6] TYPE No.	1] FUNCT-ION CODE	2] ORGANIZATION		4] OP MODE	5] STRUCTURE CODE	MAX. ACCES TIME (S)	MAX. OPER. PWR. DISS. (W)	RATED PWR. SUPPLY SPAN		INPUT LOGIC LEVELS		MIN. SINK CURRENT (A)	OUTPUT CURRENT (V)	OUTPUT TEMP. RNG. °C	GENERAL DESCRIPTION	DRAWINGS						
			No. WORDS	3] BITS per WORD					NEG. (V)	POS. (V)	MAX. '0' (V)	MIN. '1' (V)					LOGIC	SINK	CURRENT	OUTPUT VOLT	RNG.	LOGIC/BLOCK	OUT-LINE
1	MM4220BNJ	ATN	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					Z19	ML133a					
2	MM5220BNJ	ATN	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					Z19	ML133a					
3	MM5220BNN	ATN	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					Z19	ML183					
4	S8771B	ATN	512	10	SS	MPI	450n	1.0	12	5.0	60	4.0				B109a	ML13b						
5	N8220B	CAM	4	2	SW	BTX	65n	590m	0.0	5.0	80	2.0		30m	40		Z21	ML132					
6	C3104	CAM	4	4	SW	BTD	30n	625m	0.0	5.0	85	2.0		15m	45		Z13	ML34c					
7	SCM5533D	CAM	8	8	SW	MCX	250n	6.0m*	0.0	10	05%	9.95	360u%	9.5		5	C	Z12	ML195				
8	SCM5533H	CAM	8	8	SW	MCX	250n	6.0m*	0.0	10	05%	9.95	360u%	9.5		5	C	Z12	CHØ				
9	EA4080	COS	512	10	SC	MPX	725n	205m	12	12	10	3.0				5	8	B38	ML41				
10	82S1001	PLA	48	8	SE	BTX	50n	600mt	0.0	5.0	80	2.0		9.6m	45	0	7	16 VAR	Z24	ML218			
11	82S1011	PLA	48	8	SE	BTX	50n	600mt	0.0	5.0	80	2.0		9.6m	45	0	7	16 VAR	Z24	ML218			
12	IM5200CJG	PLA	48	8	SE	BDX	100n\$	675m	0.0	5.0	80	2.0		50uA#	5.5		0	7		Z4	ML188		
13	93458DC	PLA	48	8	SE	BTD	25nΔ†		0.0	5.0	80	2.0		16m	45	0	7	16 VAR	Z28	ML192a			
14	93458DM	PLA	48	8	SE	BTD	25nΔ†		0.0	5.0	80	2.0		16m	45	5	C	16 VAR	Z28	ML192a			
15	93459DC	PLA	48	8	SE	BTD	25nΔ†		0.0	5.0	80	2.0		16m	45	0	7	16 VAR	Z28	ML192a			
16	93459DM	PLA	48	8	SE	BTD	25nΔ†		0.0	5.0	80	2.0		16m	45	5	C	16 VAR	Z28	ML192a			
17	SN54S330J	PLA	50	6	SE	BTD	35n†\$	550mtØ	0.0	5.0	80	2.0		20m	50	5	C	12 VAR	Z25	ML213			
18	SN54S331J	PLA	50	6	SE	BTD	35n†\$	610mtØ	0.0	5.0	80	2.0		20m	50	5	C	12 VAR	Z25	ML213			
19	SN74S330J	PLA	50	6	SE	BTD	35n†\$	550mtØ	0.0	5.0	80	2.0		20m	50	0	7	12 VAR	Z25	ML213			
20	SN74S330J	PLA	50	6	SE	BTD	35n†\$	550mtØ	0.0	5.0	80	2.0		20m	50	0	7	12 VAR	Z25	ML213			
21	SN74S331J	PLA	50	6	SE	BTD	35n†\$	610mtØ	0.0	5.0	80	2.0		20m	50	0	7	12 VAR	Z25	ML213			
22	SN74S331N	PLA	50	6	SE	BTD	35n†\$	610mtØ	0.0	5.0	80	2.0		20m	50	0	7	12 VAR	Z25	ML213			
23	DM7575J	PLA	96	8	SC	BTX	100n†	550mt	0.0	5.0	80	2.0		12m	40	5	C	14 VAR	Z14	ML133a			
24	DM7576J	PLA	96	8	SC	BTX	100n†	550mt	0.0	5.0	80	2.0		12m	40	5	C	14 VAR	Z14	ML133a			
25	DM8575J	PLA	96	8	SC	BTX	100n†	550mt	0.0	5.0	80	2.0		12m	40	0	7	14 VAR	Z14	ML133a			
26	DM8575N	PLA	96	8	SC	BTX	100n	550mt	0.0	5.0	80	2.0		12m	40	5	C	14 VAR	Z14	ML183			
27	DM8576J	PLA	96	8	SC	BTX	100n†	550mt	0.0	5.0	80	2.0		12m	40	0	7	14 VAR	Z14	ML133a			
28	DM8576N	PLA	96	8	SC	BTX	100n	550mt	0.0	5.0	80	2.0		12m	40	5	C	14 VAR	Z14	ML183			
29	MM4220DFJ	QBF	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					Z19	ML133a					
30	MM5220DFJ	QBF	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					Z19	ML133a					
31	MM5220DFN	QBF	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					Z19	ML183					
32	S8890	RYG	64	1	SS	MPI			12	0.0	-10	-7.0					10	RHY	Z22	ML14c			
33	M253B1XX	RYG	384	8	SC	MPG		120m	12	5.0	90	3.5					0	7	12 RHY	Z17a	ML31c		
34	M253D1XX	RYG	384	8	SC	MPG		120m	12	5.0	90	3.5					0	7	12 RHY	Z17a	ML173		
35	M253B1AA	RYG	384	8	SS	MPG		120m	12	5.0	90	3.5					0	7	12 RHY	Z17	ML31c		
36	M253D1AA	RYG	384	8	SS	MPG		120m	12	5.0	90	3.5					0	7	12 RHY	Z17	ML173		
37	M250D1	RYG	432	8	SC	MPN		180mt	0.0	18	8.0	1.7		100uΔ	18	0	7	12 RHY	Z15	ML173			
38	M254B1XX	RYG	432	8	SC	MPN		180m	0.0	18	8.0	1.7					0	7	8 RHY	Z18	ML31c		
39	M252B1XX	RYG	480	8	SC	MPG		120m	12	5.0	90	3.5					0	7	15 RHY	Z16a	ML60		
40	M252D1XX	RYG	480	8	SC	MPG		120m	12	5.0	90	3.5					0	7	15 RHY	Z16a	ML158		
41	M252B1AA	RYG	480	8	SS	MPG		120m	12	5.0	90	3.5					0	7	15 RHY	Z16	ML60		
42	M252D1AA	RYG	480	8	SS	MPG		120m	12	5.0	90	3.5					0	7	15 RHY	Z16	ML158		
43	MCM6550L	RYG	7168	1	SC	MNM	1.0m*	500m	0.0	5.0	60	3.0		2.0m	40	0	7		Z5	ML189			
44	MCM6550P	RYG	7168	1	SC	MNM	1.0m*	500m	0.0	5.0	60	3.0		2.0m	40	0	7		Z5	ML190			
45	S8771#1	SCN	512\$	10	SS	MPI	450nt	1.0	12	5.0	60	4.0					0	6		B109	ML13b		
46	S8771A	SCN	512	10	SS	MPI	450n	1.0	12	5.0	60	4.0					0	6		B109a	ML13b		
47	S8771#2	SCN	1024\$	5	SS	MPI	450nt	1.0	12	5.0	60	4.0					0	6		B109	ML13b		
48	MM4220BMJ	SIN	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					5	C	Z19	ML133a			
49	MM5220BMJ	SIN	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					0	7		Z19	ML133a		
50	MM5220BMN	SIN	128	8	SS	MPX	650nØ	300mØ	12	12	10*	4.0#					0	7		Z19	ML183		
51	MM4232AEJ	SIN	512	8	SS	MPX	1.0uØ	629mØ	12	5.0	1.0	3.0		1.6m	40	5	C	Z20	ML133a				
52	MM5232AEJ	SIN	512	8	SS	MPX	1.0uØ	629mØ	12	5.0	1.0	3.0		1.6m	40	0	7		Z20	ML133a			
53	MM5232AEN	SIN	512	8	SS	MPX	1.0uØ	629mØ	12	5.0	1.0	3.0		1.6m	40	0	7		Z20	ML183			
54	EA4079	SIN	512	10	SC	MPX	725n	205m	12	12	10	3.0					5	8	B38	ML41			
55	EA3801	SIN	1024	12	DC	MPX	2.5u	350m	12	12	-2.0	-1.0					0	7		B93	MLØ		
56	NC7035	SYS	16	18	SE	MPX	1.8m	360m	18	0.0	-7.0	-1.0		500u	-1.0	2	8	Non-VOL	Z27	MLØ			
57	NC7033	SYS	21	16	SE	MPN	10u	420m	30	0.0	-4.6	-8.0		500u	-1.0	0	7	Non-VOL	Z26	MLØ			

# 21. TYPES WITH U.S. MILITARY SPECIFICATIONS

IN TYPE NUMBER  
SEQUENCE

TYPE No.	MFRS	MIL-M-38510/	TYPE No.	MFRS	MIL-M-38510/	TYPE No.	MFRS	MIL-M-38510/	TYPE No.	MFRS	MIL-M-38510/	TYPE No.	MFRS	MIL-M-38510/
M38510/00901AAA	9C none	AMEND 3	M38510/00902AEA	9C none	AMEND 3	M38510/00903BCB	9C SIC	AMEND 3	M38510/00905AEC	9C none	AMEND 3	M38510/02801AAB	28B none	AMEND 2
M38510/00901AAB	9C none	AMEND 3	M38510/00902AEB	9C none	AMEND 3	M38510/00903BCC	9C none	AMEND 3	M38510/00905AFA	9C none	AMEND 3	M38510/02801AAC	9C none	AMEND 2
M38510/00901AAC	9C none	AMEND 3	M38510/00902AEC	9C none	AMEND 3	M38510/00903BDA	9C none	AMEND 3	M38510/00905AFB	9C none	AMEND 3	M38510/02801ABA	9C none	AMEND 2
M38510/00901ABA	9C none	AMEND 3	M38510/00902AFA	9C none	AMEND 3	M38510/00903BDB	9C none	AMEND 3	M38510/00905AFC	9C none	AMEND 3	M38510/02801ABB	9C none	AMEND 2
M38510/00901ABB	9C none	AMEND 3	M38510/00902AFB	9C none	AMEND 3	M38510/00903BDC	9C none	AMEND 3	M38510/00905BEA	9C MOTA	AMEND 3	M38510/02801ABC	9C none	AMEND 2
M38510/00901ABC	9C none	AMEND 3	M38510/00902AFC	9C none	AMEND 3	M38510/00903CAA	9C none	AMEND 3	M38510/00905BEB	9C MOTA	AMEND 3	M38510/02801ACB	9C none	AMEND 2
M38510/00901ACA	9C none	AMEND 3	M38510/00902BEA	9C MOTA SIC	AMEND 3	M38510/00903CAB	9C none	AMEND 3	M38510/00905BEC	9C none	AMEND 3	M38510/02801ACA	9C none	AMEND 2
M38510/00901ACB	9C none	AMEND 3	M38510/00902BEB	9C MOTA SIC	AMEND 3	M38510/00903CAC	9C none	AMEND 3	M38510/00905BFA	9C MOTA	AMEND 3	M38510/02801ACB	9C none	AMEND 2
M38510/00901ACC	9C none	AMEND 3	M38510/00902BEC	9C none	AMEND 3	M38510/00903CBA	9C none	AMEND 3	M38510/00905BFB	9C MOTA	AMEND 3	M38510/02801ACC	9C none	AMEND 2
M38510/00901ADA	9C none	AMEND 3	M38510/00902BFA	9C MOTA SIC	AMEND 3	M38510/00903CBB	9C none	AMEND 3	M38510/00905BFC	9C none	AMEND 3	M38510/02801ADC	9C none	AMEND 2
M38510/00901ADB	9C none	AMEND 3	M38510/00902BFB	9C MOTA SIC	AMEND 3	M38510/00903CBC	9C none	AMEND 3	M38510/00905CEA	9C MOTA	AMEND 3	M38510/02801ADA	9C none	AMEND 2
M38510/00901ADC	9C none	AMEND 3	M38510/00902BFC	9C none	AMEND 3	M38510/00903CCA	9C SIC	AMEND 3	M38510/00905CEB	9C MOTA	AMEND 3	M38510/02801ADB	9C none	AMEND 2
M38510/00901BAA	9C MOTA	AMEND 3	M38510/00902CEA	9C MOTA SIC	AMEND 3	M38510/00903CCB	9C SIC	AMEND 3	M38510/00905CEC	9C none	AMEND 3	M38510/02801ADC	9C none	AMEND 2
M38510/00901BAB	9C MOTA	AMEND 3	M38510/00902CEB	9C MOTA SIC	AMEND 3	M38510/00903CCC	9C none	AMEND 3	M38510/00905CFA	9C MOTA	AMEND 3	M38510/02801BAA	9C none	AMEND 2
M38510/00901BAC	9C MOTA	AMEND 3	M38510/00902CEC	9C none	AMEND 3	M38510/00903CDA	9C none	AMEND 3	M38510/00905CFB	9C MOTA	AMEND 3	M38510/02801BAB	9C none	AMEND 2
M38510/00901BBA	9C none	AMEND 3	M38510/00902CFA	9C MOTA SIC	AMEND 3	M38510/00903CDB	9C none	AMEND 3	M38510/00905CFC	9C none	AMEND 3	M38510/02801BAC	9C none	AMEND 2
M38510/00901BBB	9C none	AMEND 3	M38510/00902CFB	9C MOTA SIC	AMEND 3	M38510/00903CDC	9C none	AMEND 3	M38510/00906AEA	9C none	AMEND 3	M38510/02801BBA	9C none	AMEND 2
M38510/00901BBC	9C none	AMEND 3	M38510/00902CFC	9C none	AMEND 3	M38510/00904AEA	9C none	AMEND 3	M38510/00906AEB	9C none	AMEND 3	M38510/02801BBB	9C none	AMEND 2
M38510/00901BCA	9C MOTA SIC	AMEND 3	M38510/00903AAA	9C none	AMEND 3	M38510/00904AEB	9C none	AMEND 3	M38510/00906AEC	9C none	AMEND 3	M38510/02801BBC	9C none	AMEND 2
M38510/00901BCB	9C MOTA SIC	AMEND 3	M38510/00903AAB	9C none	AMEND 3	M38510/00904AEC	9C none	AMEND 3	M38510/00906AFA	9C none	AMEND 3	M38510/02801BCA	9C NSC	AMEND 2
M38510/00901BCC	9C none	AMEND 3	M38510/00903AAC	9C none	AMEND 3	M38510/00904AFA	9C none	AMEND 3	M38510/00906AFB	9C none	AMEND 3	M38510/02801BCB	9C NSC	AMEND 2
M38510/00901BDA	9C none	AMEND 3	M38510/00903ABA	9C none	AMEND 3	M38510/00904AFB	9C none	AMEND 3	M38510/00906AFC	9C none	AMEND 3	M38510/02801BCC	9C NSC	AMEND 2
M38510/00901BDB	9C none	AMEND 3	M38510/00903ABB	9C none	AMEND 3	M38510/00904AFC	9C none	AMEND 3	M38510/00906BEA	9C MOTA	AMEND 3	M38510/02801BDA	9C NSC	AMEND 2
M38510/00901BDC	9C none	AMEND 3	M38510/00903ABC	9C none	AMEND 3	M38510/00904BEA	9C MOTA	AMEND 3	M38510/00906BEB	9C MOTA	AMEND 3	M38510/02801BDB	9C NSC	AMEND 2
M38510/00901CAA	9C MOTA	AMEND 3	M38510/00903ACA	9C none	AMEND 3	M38510/00904BEB	9C MOTA	AMEND 3	M38510/00906BEC	9C none	AMEND 3	M38510/02801BDC	9C NSC	AMEND 2
M38510/00901CAB	9C MOTA	AMEND 3	M38510/00903ACB	9C none	AMEND 3	M38510/00904BEC	9C none	AMEND 3	M38510/00906BFA	9C MOTA	AMEND 3	M38510/02801CAA	9C none	AMEND 2
M38510/00901CAC	9C MOTA	AMEND 3	M38510/00903ACC	9C none	AMEND 3	M38510/00904BFA	9C MOTA	AMEND 3	M38510/00906BFB	9C MOTA	AMEND 3	M38510/02801CAB	9C none	AMEND 2
M38510/00901CBA	9C none	AMEND 3	M38510/00903ADA	9C none	AMEND 3	M38510/00904BFB	9C MOTA	AMEND 3	M38510/00906BFC	9C none	AMEND 3	M38510/02801CAC	9C none	AMEND 2
M38510/00901CBB	9C none	AMEND 3	M38510/00903ADB	9C none	AMEND 3	M38510/00904BFC	9C none	AMEND 3	M38510/00906CEA	9C MOTA	AMEND 3	M38510/02801CAB	9C none	AMEND 2
M38510/00901CBC	9C none	AMEND 3	M38510/00903ADC	9C none	AMEND 3	M38510/00904CEA	9C MOTA	AMEND 3	M38510/00906CEB	9C MOTA	AMEND 3	M38510/02801CBA	9C none	AMEND 2
M38510/00901CCA	9C MOTA SIC	AMEND 3	M38510/00903BAA	9C none	AMEND 3	M38510/00904CEB	9C MOTA	AMEND 3	M38510/00906CEC	9C none	AMEND 3	M38510/02801CBB	9C none	AMEND 2
M38510/00901CCB	9C MOTA SIC	AMEND 3	M38510/00903BAB	9C none	AMEND 3	M38510/00904CEC	9C none	AMEND 3	M38510/00906CFA	9C MOTA	AMEND 3	M38510/02801CBB	9C none	AMEND 2
M38510/00901CCC	9C none	AMEND 3	M38510/00903BAC	9C none	AMEND 3	M38510/00904CFA	9C MOTA	AMEND 3	M38510/00906CFB	9C MOTA	AMEND 3	M38510/02801CCB	9C none	AMEND 2
M38510/00901CDA	9C none	AMEND 3	M38510/00903BBA	9C none	AMEND 3	M38510/00904CFB	9C MOTA	AMEND 3	M38510/00906CFC	9C MOTA	AMEND 3	M38510/02801CCB	9C none	AMEND 2
M38510/00901CDB	9C none	AMEND 3	M38510/00903BBB	9C none	AMEND 3	M38510/00904CFC	9C none	AMEND 3	M38510/00906AEA	9C none	AMEND 3	M38510/02801CCB	9C none	AMEND 2
M38510/00901CDC	9C none	AMEND 3	M38510/00903BBC	9C none	AMEND 3	M38510/00905AEA	9C none	AMEND 3	M38510/02801AAA	28B AMEND 2	AMEND 3	M38510/02801CAC	28B none	AMEND 2
M38510/00901STD	9C none	AMEND 3	M38510/00903BCA	9C SIC	AMEND 3	M38510/00905AEB	9C none	AMEND 3						USAF







# 21. TYPES WITH U.S. MILITARY SPECIFICATIONS

IN TYPE NUMBER  
SEQUENCE

TYPE No.	MFRS	MILM-38510/
M38510/30606AAB	none	306 USAF
M38510/30606AAC	none	306 USAF
M38510/30606ABA	none	306 USAF
M38510/30606ABB	none	306 USAF
M38510/30606ABC	none	306 USAF
M38510/30606ACA	none	306 USAF
M38510/30606ACB	none	306 USAF
M38510/30606ACC	none	306 USAF
M38510/30606ADA	none	306 USAF
M38510/30606ADB	none	306 USAF
M38510/30606ADC	none	306 USAF
M38510/30606BAA	none	306 USAF
M38510/30606BAB	none	306 USAF
M38510/30606BAC	none	306 USAF
M38510/30606BBA	none	306 USAF
M38510/30606BBB	none	306 USAF
M38510/30606BBC	none	306 USAF
M38510/30606BCA	none	306 USAF
M38510/30606BCB	none	306 USAF
M38510/30606BCC	none	306 USAF
M38510/30606BDA	none	306 USAF
M38510/30606BDB	none	306 USAF
M38510/30606BDC	none	306 USAF
M38510/30606CAA	none	306 USAF
M38510/30606CAB	none	306 USAF
M38510/30606CAC	none	306 USAF
M38510/30606CBA	none	306 USAF
M38510/30606CBB	none	306 USAF
M38510/30606CBC	none	306 USAF
M38510/30606CCA	none	306 USAF
M38510/30606CCB	none	306 USAF
M38510/30606CCC	none	306 USAF
M38510/30606CDA	none	306 USAF
M38510/30606CDB	none	306 USAF
M38510/30606CDC	none	306 USAF
M38510/30607AEA	none	306 USAF
M38510/30607AEB	none	306 USAF
M38510/30607AEC	none	306 USAF
M38510/30607AFA	none	306 USAF
M38510/30607AFB	none	306 USAF
M38510/30607AFC	none	306 USAF
M38510/30607BEA	none	306 USAF
M38510/30607BEB	none	306 USAF
M38510/30607BEC	none	306 USAF
M38510/30607BFA	none	306 USAF
M38510/30607BFB	none	306 USAF
M38510/30607BFC	none	306 USAF
M38510/30607CEA	none	306 USAF
M38510/30607CEB	none	306 USAF
M38510/30607CEC	none	306 USAF
M38510/30607CFA	none	306 USAF
M38510/30607CFB	none	306 USAF
M38510/30607CFC	none	306 USAF

## MILITARY DOCUMENTS

Department of Defense Index of Specifications and Standards dated 1 July 1976, Supplement dated November 1976.

Device Manufacturers Qualifications on Test Reference Letter.

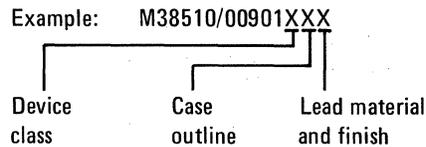
MIL-M-0038510B (USAF) Military Specification, General Specification for Microcircuits, dated 1 October 1973, Supplement 1B, dated 3 May 1976, used in lieu of MIL-M-38510C, Military Specification, dated 1 March 1976, Supplement 1B, dated 10 December 1976.

QPL-38510-28 Qualified Products List (Part I) of Products Qualified Under Military Specification MIL-M-38510, dated 1 April 1977. Qualified Products List (Part II) of Products Qualified Under Military Specification MIL-M-38510, dated 1 April 1977, Amendment, dated 4 April 1977. products. Therefore, manufacturers listed on QPL-38510 shall "JAN" mark and ship the specific part numbered devices for which they are listed, providing all required quality conformance inspections have been successfully completed. They have not been subjected to all the tests required for qualification under the latest effective issue of MIL-M-38510; however, the manufacturers have performed sufficient similar tests to indicate that the products have the potential of complying with the MIL-M-38510 requirements.

MIL-STD-833A Military Standard; Test Methods and Procedures for Microelectronics, dated 15 November 1974, Notice 2, dated 1 March 1976.

MIL-STD-1562 Military Standard; List of Standard Microcircuits, dated 5 November 1974.

NOTE: The 3-letter suffix at the end of the type number represents device class (degree of quality assurance testing), case outline and lead material finish as shown below:



21A. COMMERCIAL-TO-MILITARY TYPE NUMBER CROSS-REFERENCE

COMMERCIAL TYPE No.	MILITARY TYPE No.
4006A	M38510/20101----
4014A	M38510/20201----
4015A	M38510/20202----
4021A	M38510/20101----
4031A	M38510/20102----
4034A	M38510/05701----
4035A	M38510/05702----
54L91	M38510/05703----
54L95	M38510/05704----
54L164	M38510/05705----
54LS95	M38510/05706----
54LS96	M38510/05707----
54LS164	M38510/02806----
54LS194	M38510/02801----
54LS195	M38510/02802----
54LS295	M38510/30603----
54LS395	M38510/30604----
5495	M38510/30605----
5496	M38510/30601----
54164	M38510/30602----
54165	M38510/30605----
54194	M38510/30607----
54195	M38510/00901----
76L70	M38510/00902----
93L00	M38510/00903----
93L28	M38510/00904----
HYPROM 512	M38510/00905----
IM5603A	M38510/00906----
IM5623	M38510/02805----
MCM5303	M38510/02804----
MCM5304	M38510/02803----

# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

## DRAWING PREFIX ASSIGNMENTS

### LOGIC/BLOCK DRAWINGS

- A: RAMs
- B: ROMs
- C: Character Generators
- E: Code Converters
- F: Shift Registers
- Z: Miscellaneous

### OUTLINE DRAWINGS

- CH: Chip
- CY: TO-5 type (non-JEDEC)
- FL: Flat package (non-JEDEC)
- ML: Molded or encapsulated package not included in other categories
- MO: Standard JEDEC outline
- PL: Printed circuit board
- TO: Standard JEDEC outline
- : Package style only shown; no dimensions.

## NOTES

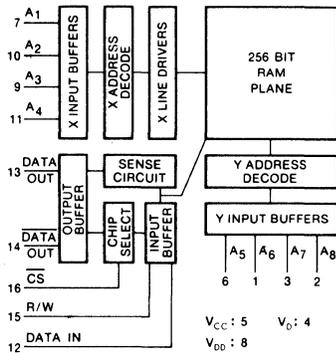
These outline drawings are intended as a guide for the user. They should not be used for construction purposes without first checking with the appropriate manufacturer.

These drawings are referenced in the Technical Sections of this D.A.T.A.BOOK in accordance with information supplied by the manufacturers.

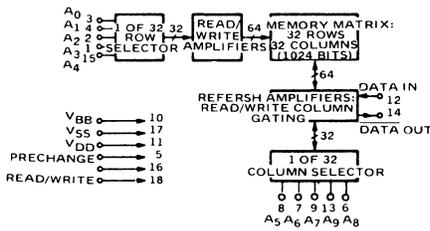
The DO and TO drawings have been reproduced from JEDEC Registration Data Files with the permission of the National Electrical Manufacturer's Association - Electronic Industries Association. JEDEC designations are assigned only to outlines submitted by the JC-11 Committee on Mechanical Standardization. The procedure of assigning and announcing the JEDEC designation constitutes registration.

All drawings have circular symmetry unless otherwise indicated.

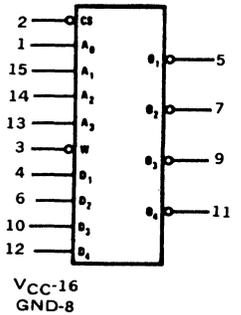
**A1**



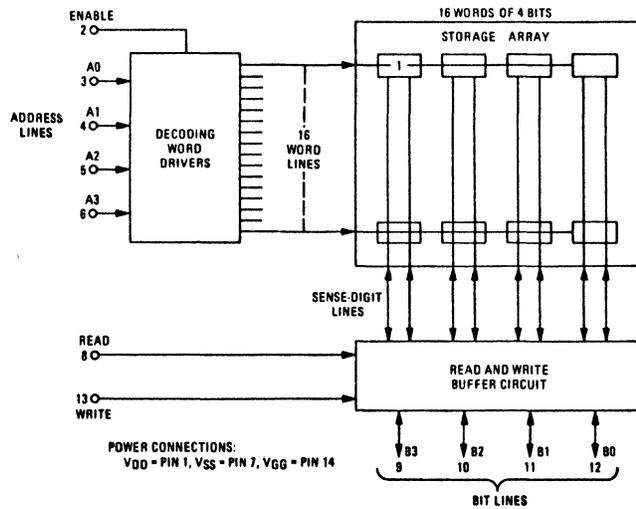
**A2**



**A4**



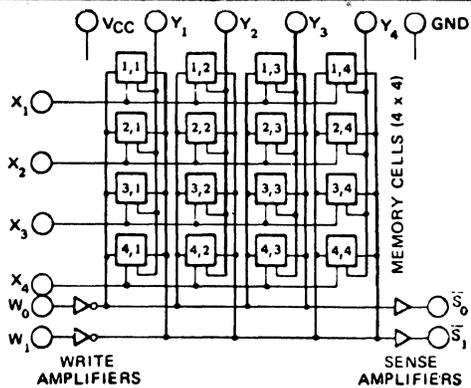
**A8**



# 22. LOGIC/BLOCK DRAWINGS

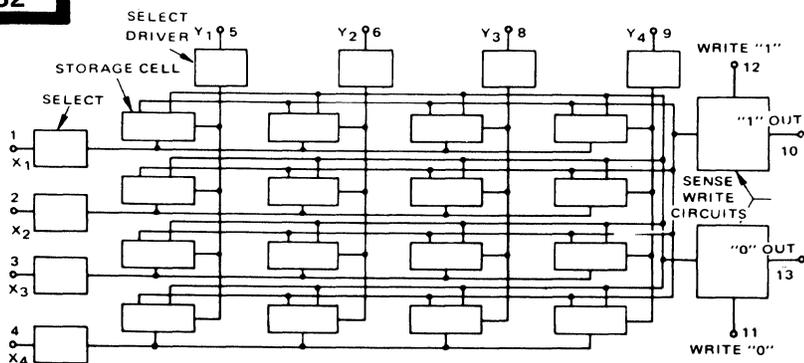
IN DRAWING NUMBER  
SEQUENCE

A27

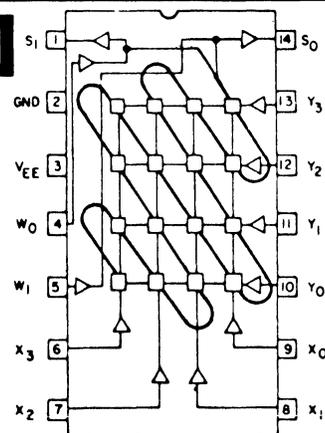


	X1	X2	X3	X4	Y1	Y2	Y3	Y4	S1	S0	W1	W0	VCC	GND
A27	3	2	1	14	5	6	7	8	12	11	13	9	4	10
A27a	1	2	3	4	13	12	11	10	6	8	5	9	14	7

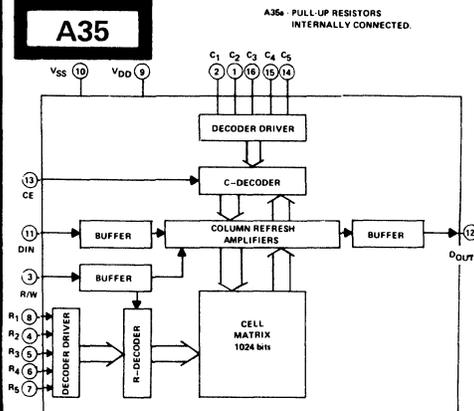
A32



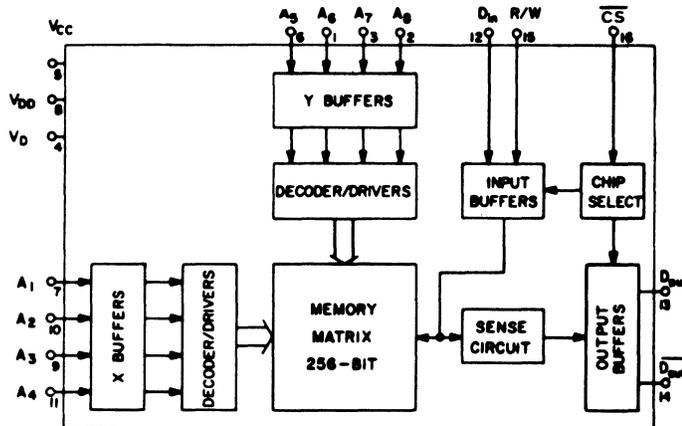
A33



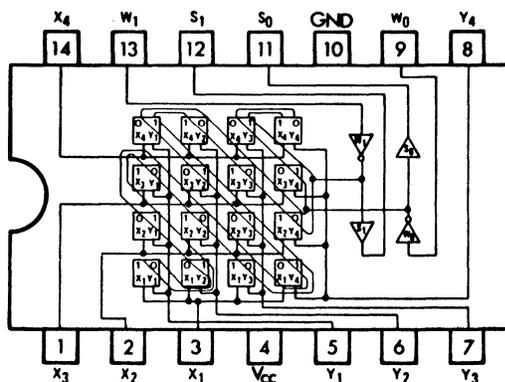
A35



A36

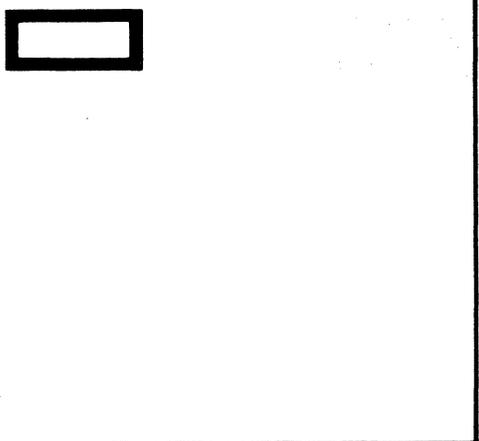
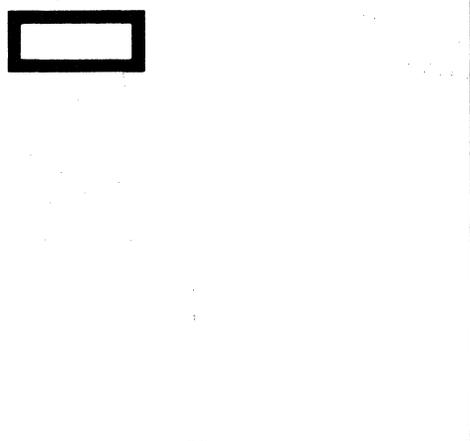
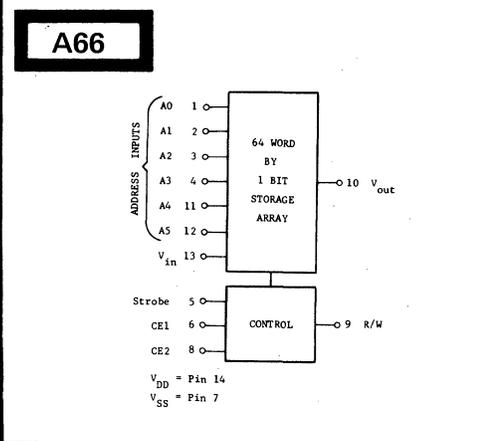
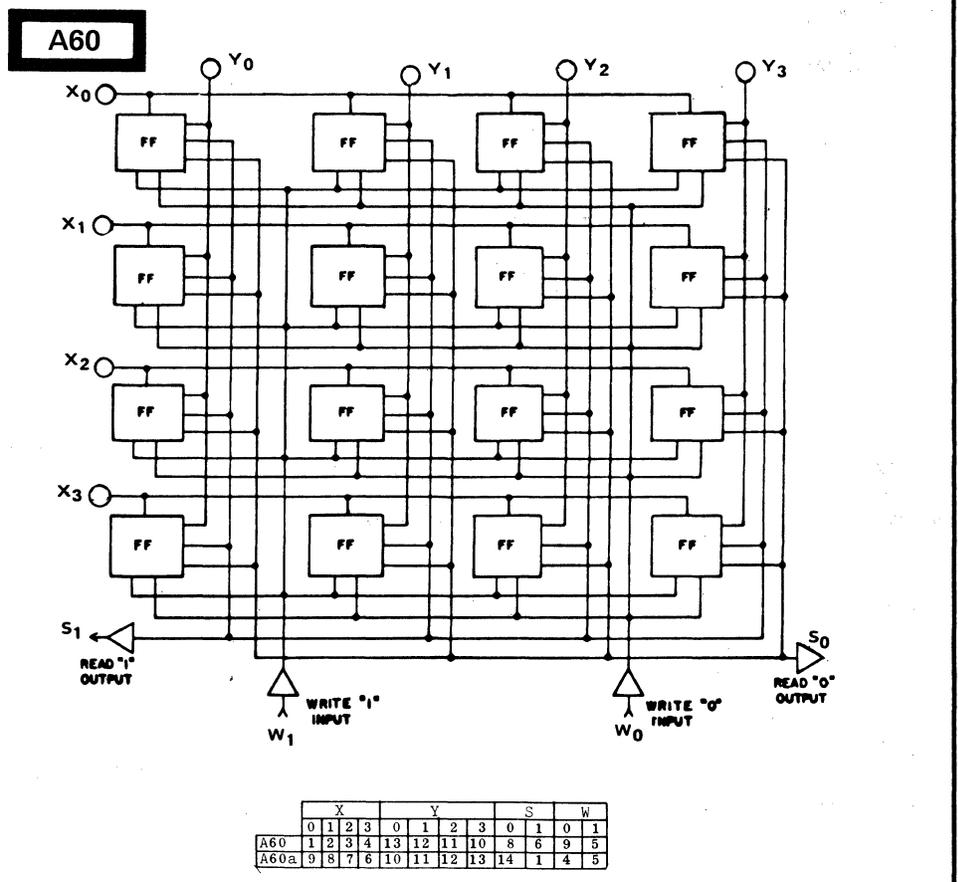
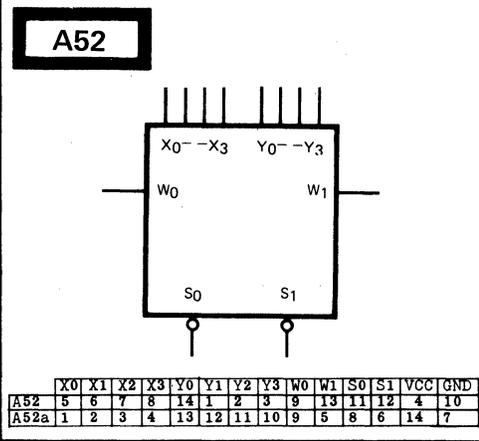
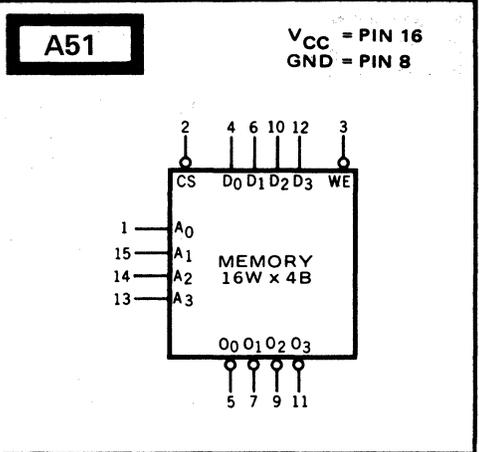
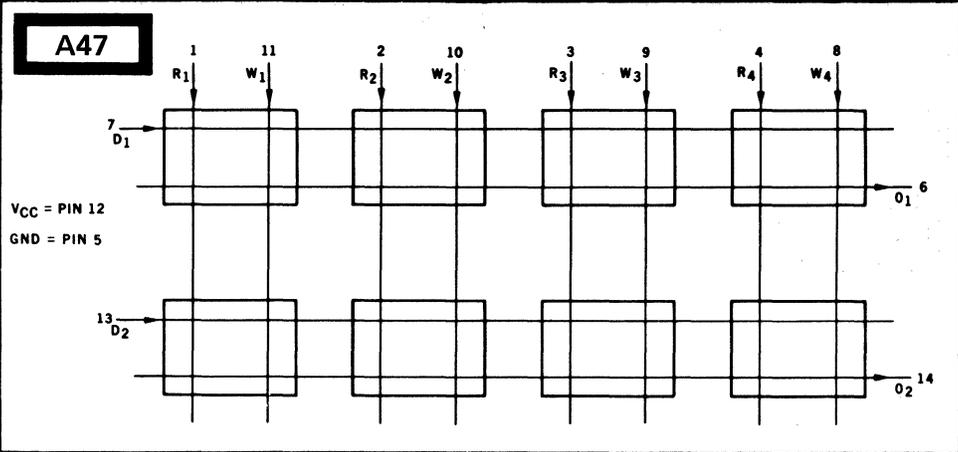


A45



# 22. LOGIC/BLOCK DRAWINGS

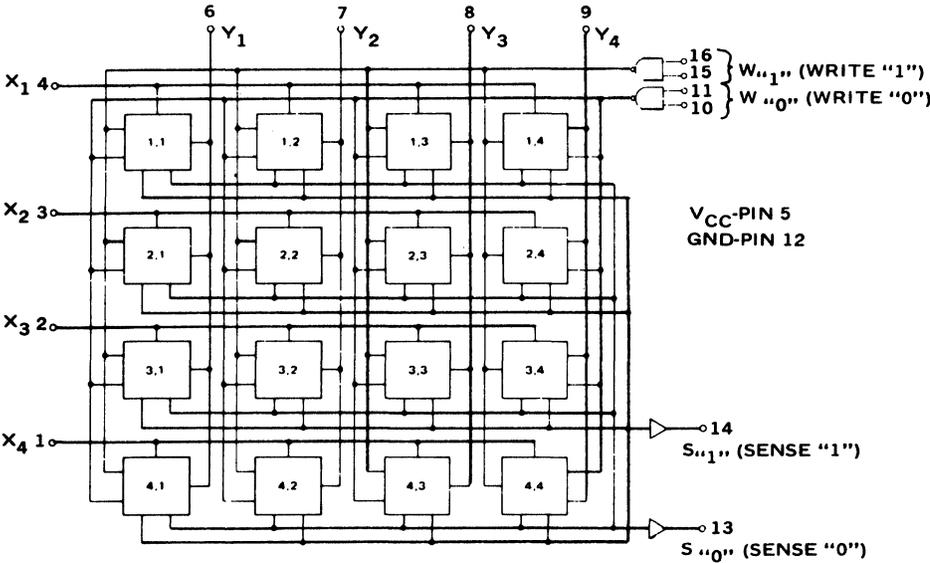
IN DRAWING NUMBER SEQUENCE



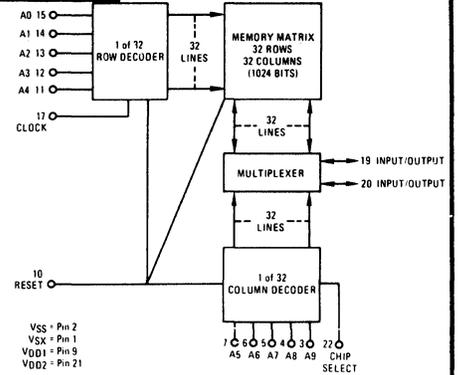
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

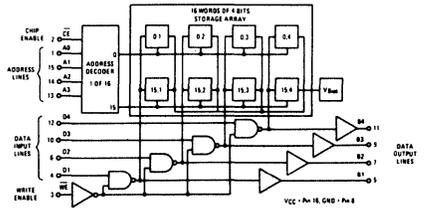
**A67**



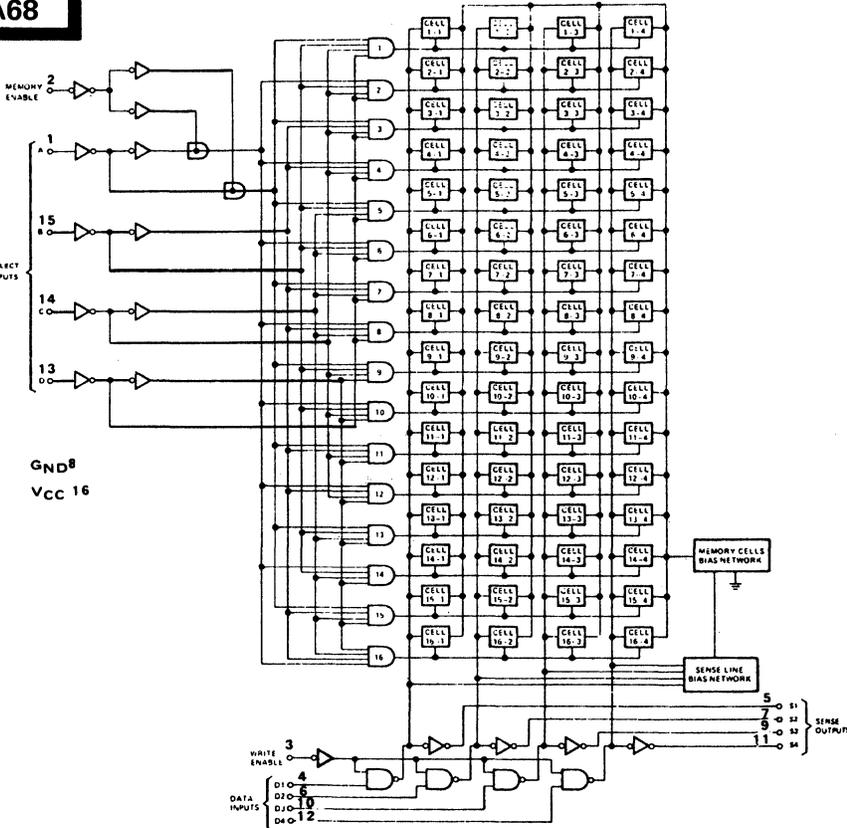
**A69**



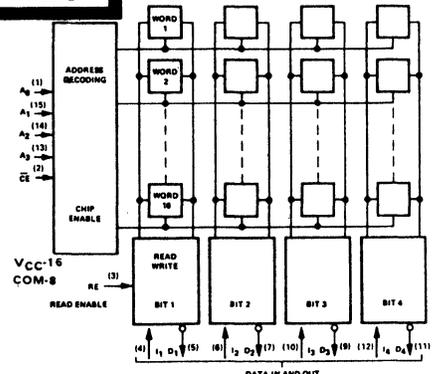
**A70**



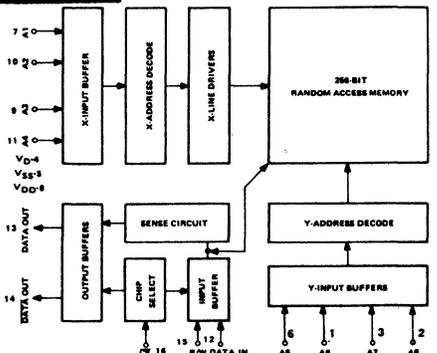
**A68**



**A73**

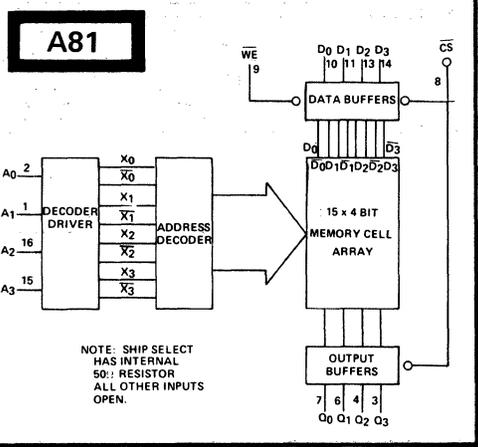
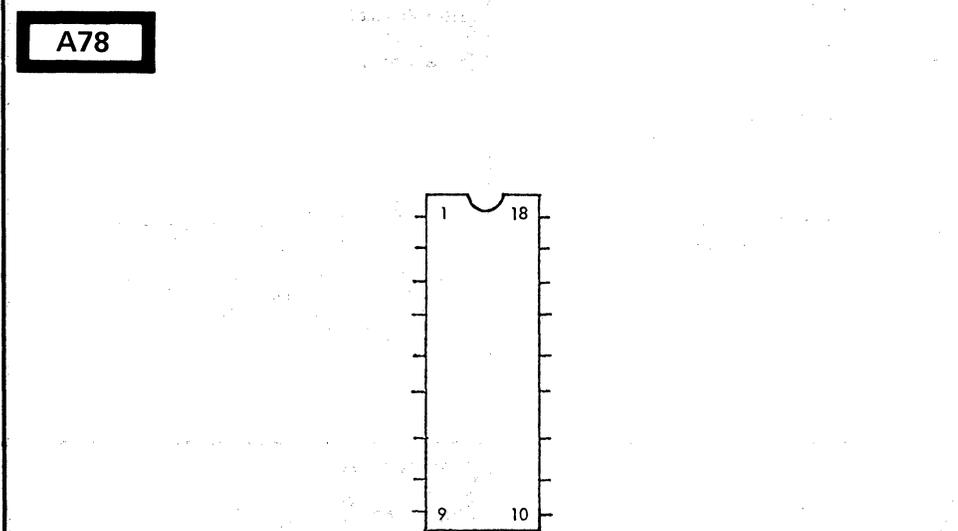
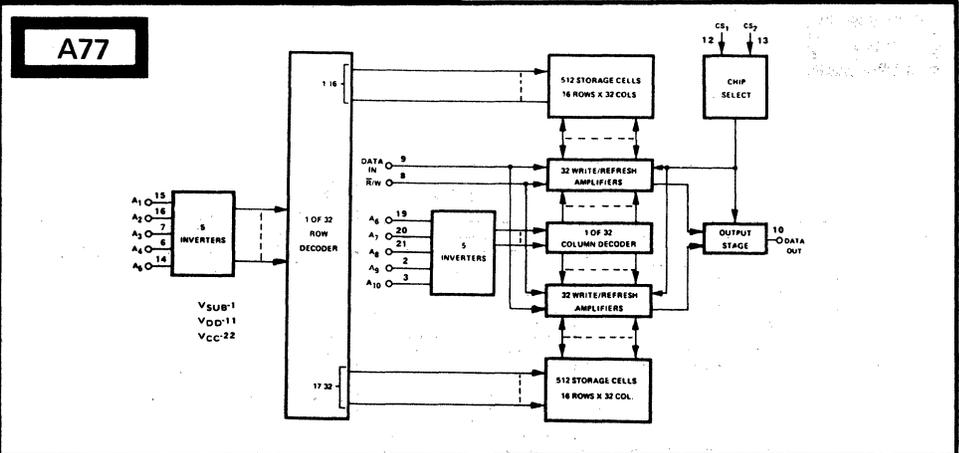
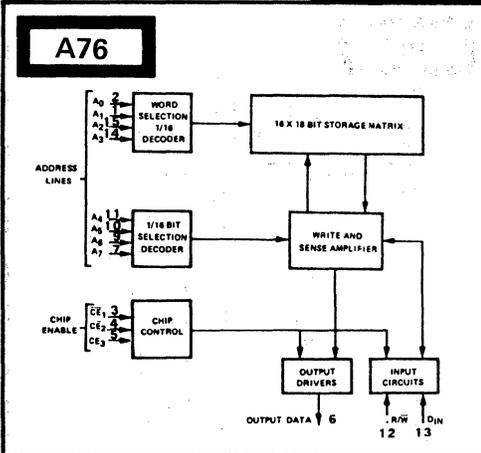


**A74**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

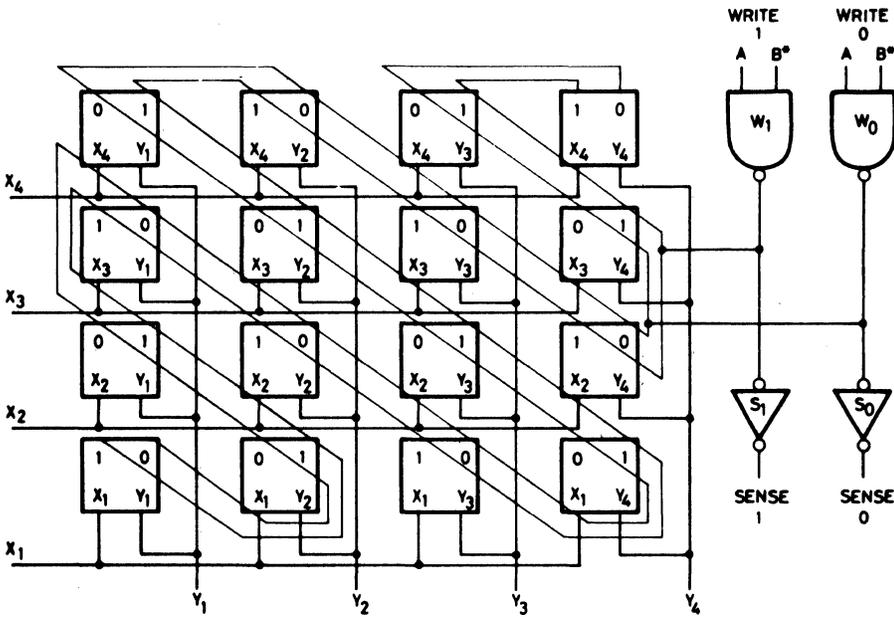


		PIN NUMBERS																	
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
A76	CE2	CE3	A0	A1	A2	A3	A4	DO	GND	A5	A6	A7	A8	A9	WE	D1	CE1	VCC	
A76a	A0	A4	RW	A1	A2	A3	A5	A6	NC	VBB	VDD	VSS	PRE	DI/	CE	A9	A8	A7	
													CHANGE	OUT					
A78b	A3	A2	A1	A0	A5	A6	A7	VSS	QD	CE2	DY/O1	DY/O2	DY/O3	DY/O4	CE1	R/W	A4	VCC	
A78c	A4	A3	A2	A1	A0	BUS7	BUS6	BUS5	VSS	BUS4	BUS3	BUS2	BUS1	BUS0	CS	MRD	MWR	VDD	
A78d	VBB	I/O	A0	A1	A2	R/W	CE	A3	A4	VDD	A5	A6	A7	A8	A9	A10	A11	VSS	
A78e	A4	A3	A2	A1	A0	DY	D5	D6	VSS	D4	D3	D2	D1	D0	CS	RD	WR	VDD	
A78f	VSX	A9	A8	A7	A6	A5	VREF	RESET	A4	A3	A2	A1	A0	C1/CS1	I/O	I/O	VDD	VSS	

# 22. LOGIC/BLOCK DRAWINGS

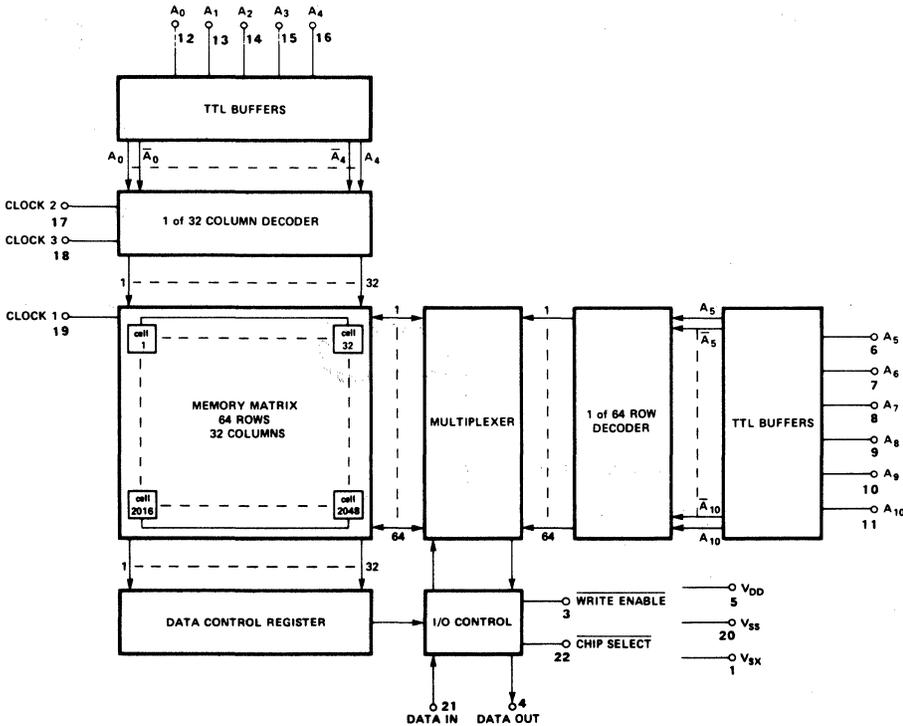
IN DRAWING NUMBER SEQUENCE

**A84**

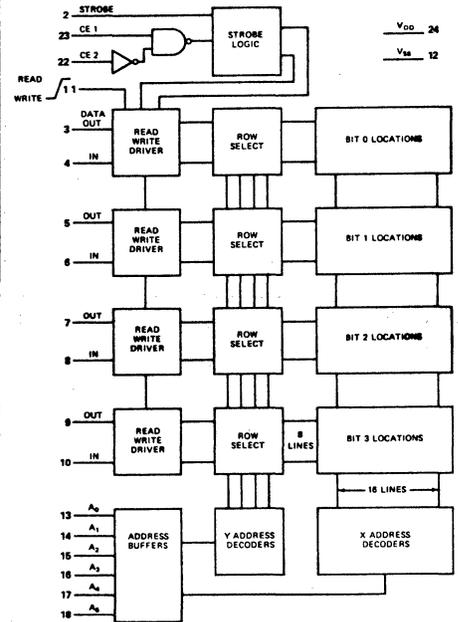


\* NOTE: GATED INPUTS (AS SHOWN) ARE AVAILABLE ON A84 ONLY. A84a HAS ONE W<sub>0</sub> AND ONE W<sub>1</sub> INPUT.

**A87**



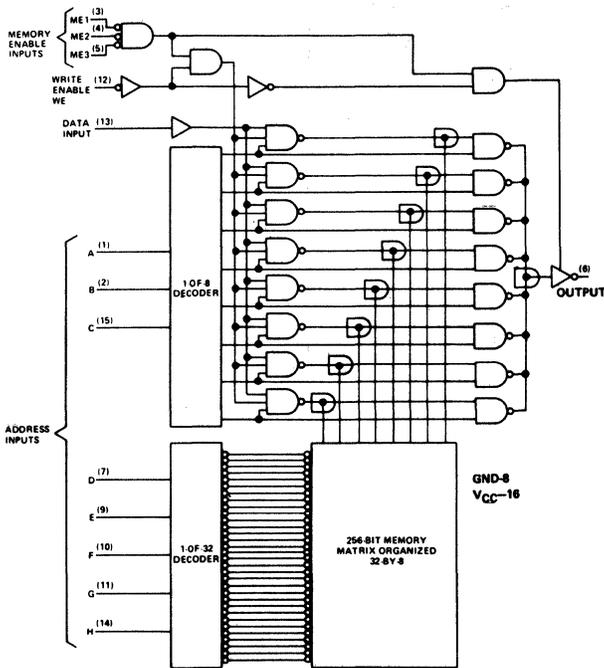
**A90**



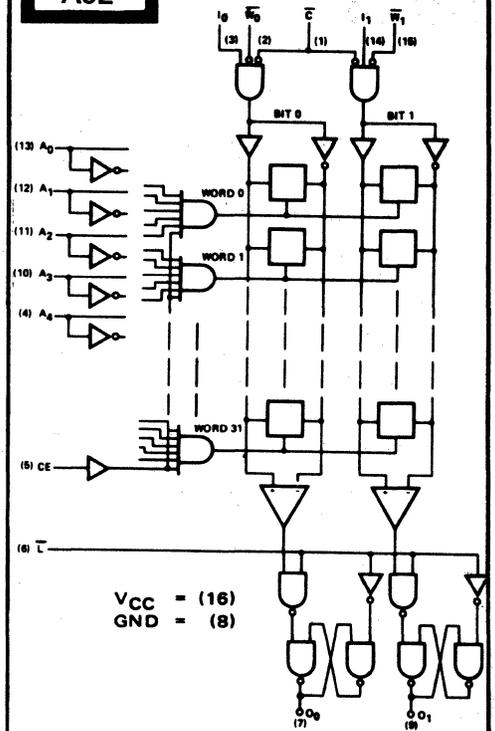
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

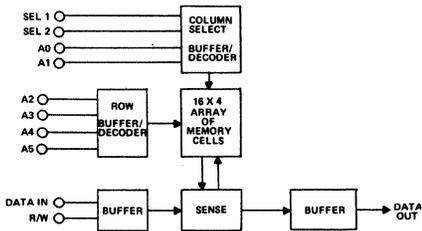
**A91**



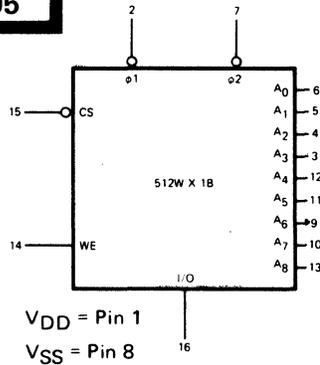
**A92**



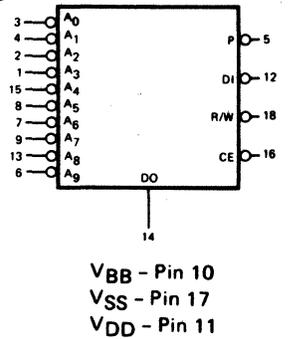
**A94**



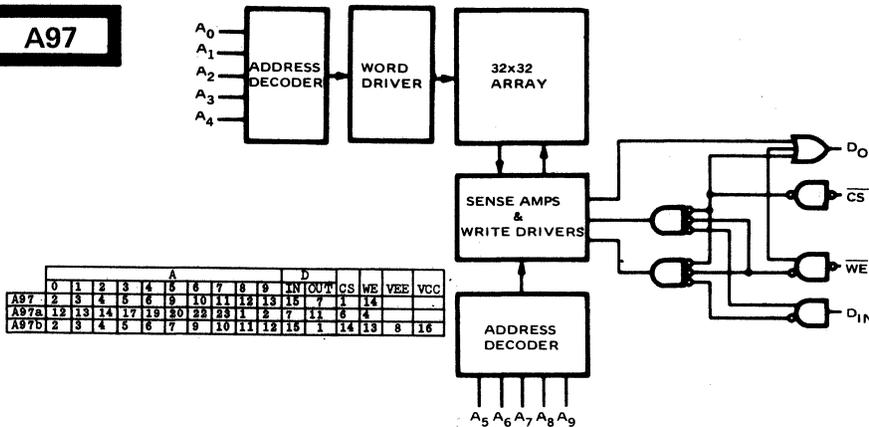
**A95**



**A96**



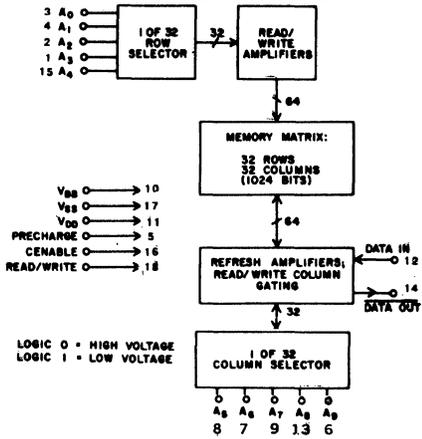
**A97**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A98**

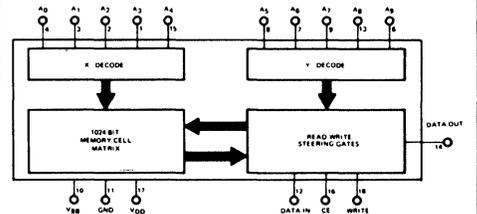


**A99**

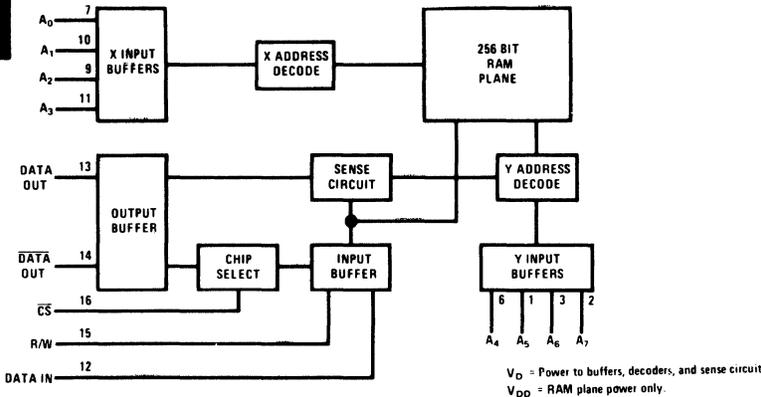


		PIN NUMBERS															
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
A99	A <sub>6</sub>	A <sub>8</sub>	A <sub>7</sub>	VGG	VSS	A <sub>5</sub>	A <sub>V</sub>	VCC	A <sub>3</sub>	A <sub>2</sub>	A <sub>4</sub>	DI	DO	VDD	R/W	CS	
A99a	A	WE	DI	S1	D2	S2	GND	S3	D3	S4	D4	D	C	B	VCC		
A99b	A1	A0	CS1	CS2	CS3	DO	A4	GND	A5	A6	A7	WE	DI	A3	A2	VCC	
A99c	CS	A0	A1	A2	A3	A4	DOUT	VSS	A5	A6	A7	A8	A9	R/W	DIN	VDD	
A99d	A3	A2	A1	A0	A5	A6	A7	VSS	DI/01	DI/02	DI/03	DI/04	CE	WE	A4	VCC	
A99e	VBB	DIN	W	RAS	A0	A2	A1	VDD	VCC	A5	A4	A3	CS	DOUT	CAS	VSS	
A99f	VBB	DIN	W	RAS	A0	A2	A1	VDD	VCC	A5	A4	A3	A6	DOUT	CAS	VSS	
A99g	A6	A5	R/W	A1	A2	A3	A4	A0	GND	VCC	DIN	DOUT	CE	A9	A8	A7	
A99h	CS	A0	A1	A2	A3	A4	DOUT	GND	A5	A6	A7	A8	A9	WE	DIN	VCC	

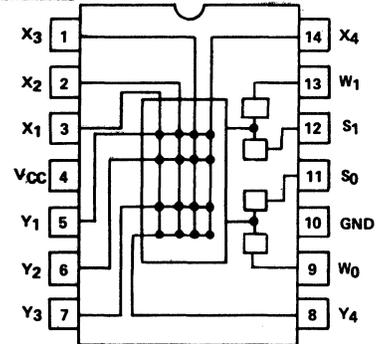
**A101**



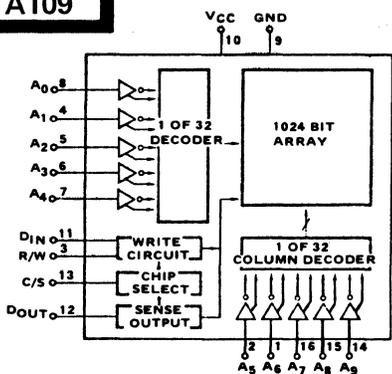
**A102**



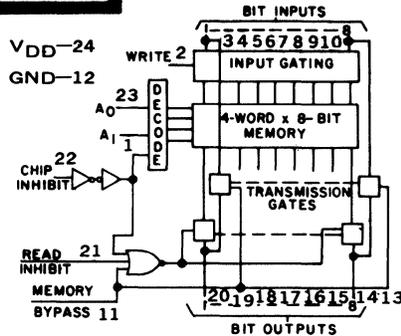
**A107**



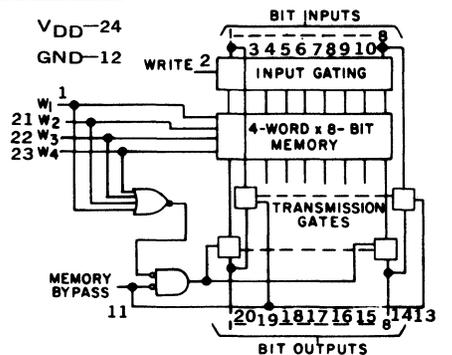
**A109**



**A110**



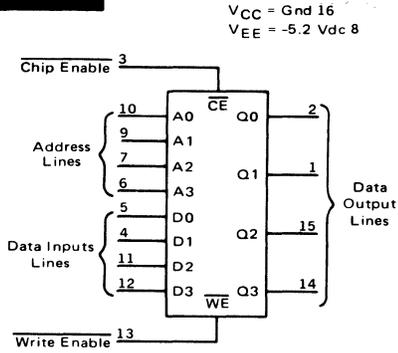
**A111**



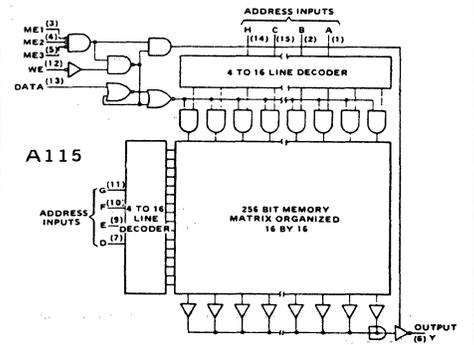
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A114**



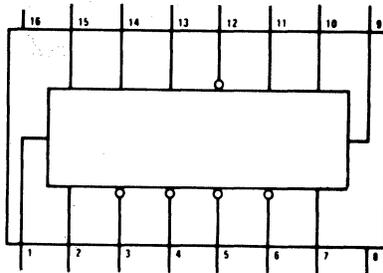
**A115**



A115a SAME AS ABOVE EXCEPT  
OUTPUT IS AS SHOWN BELOW.

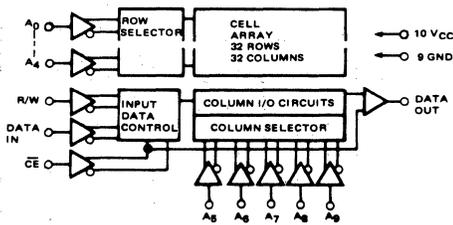


**A117**

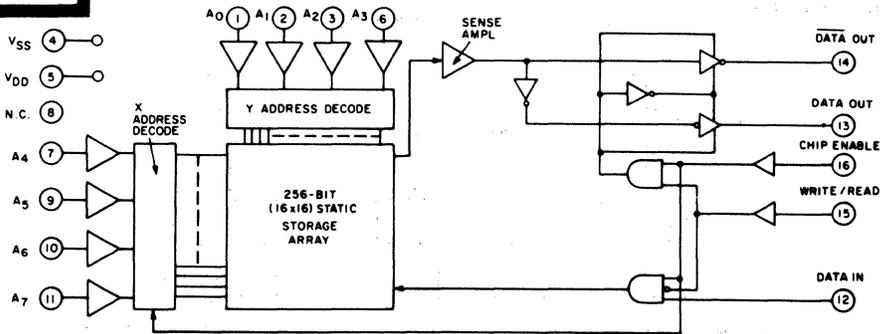


PIN NUMBERS																
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
A117	A	B	CE1	CE2	CE3	DOUT	D	GND	E	F	G	WE	DM	H	C	VCC
A117a	D3	D4	R*	Ra	Q4	Q3	GND	Q2	Q1	READ	WRITE	B	A	D1	VCC	

**A118**



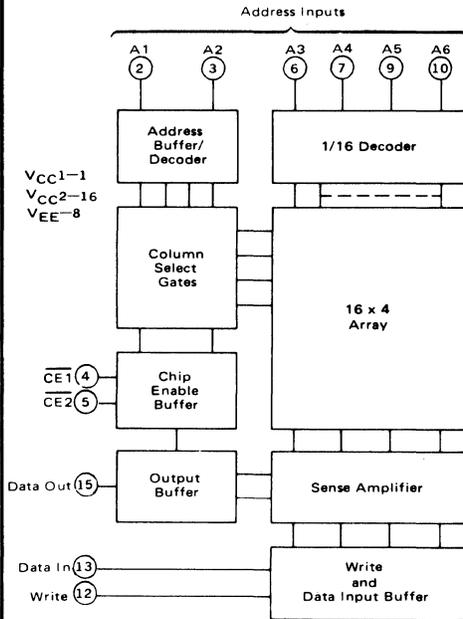
**A121**



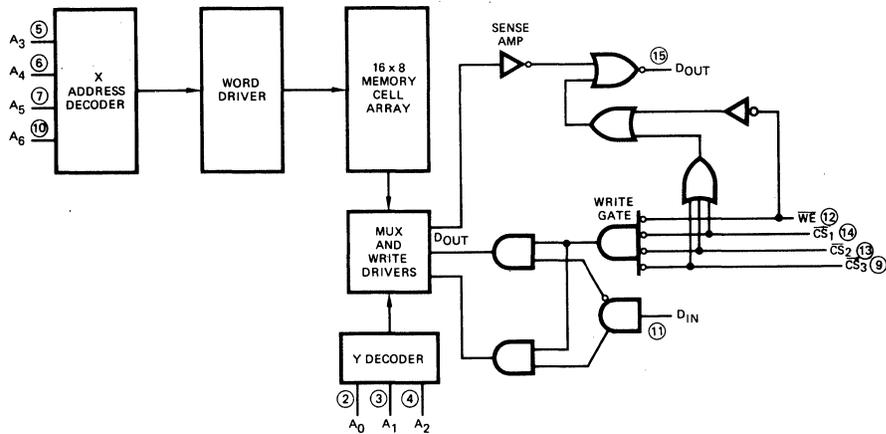
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A126**



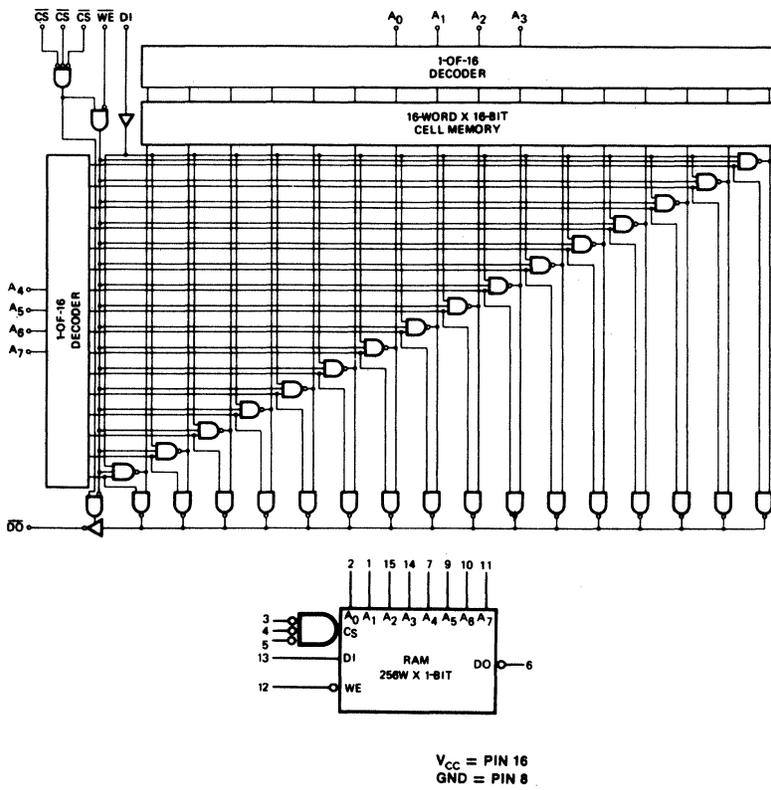
**A128**



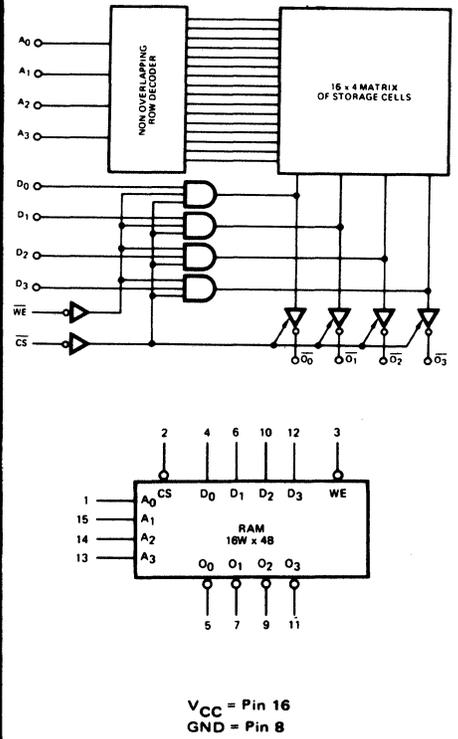
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

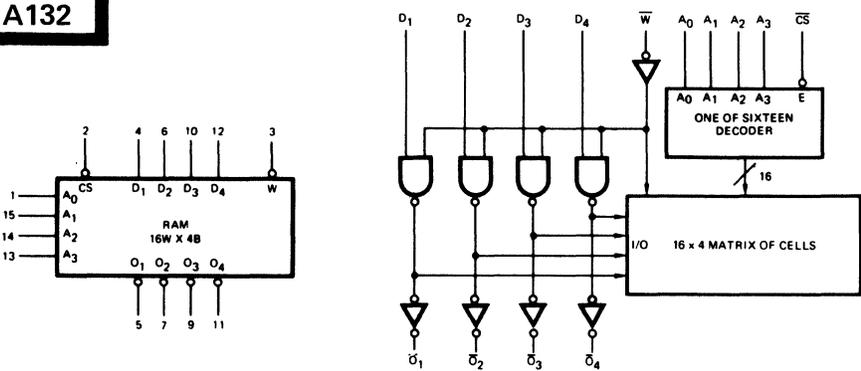
**A130**



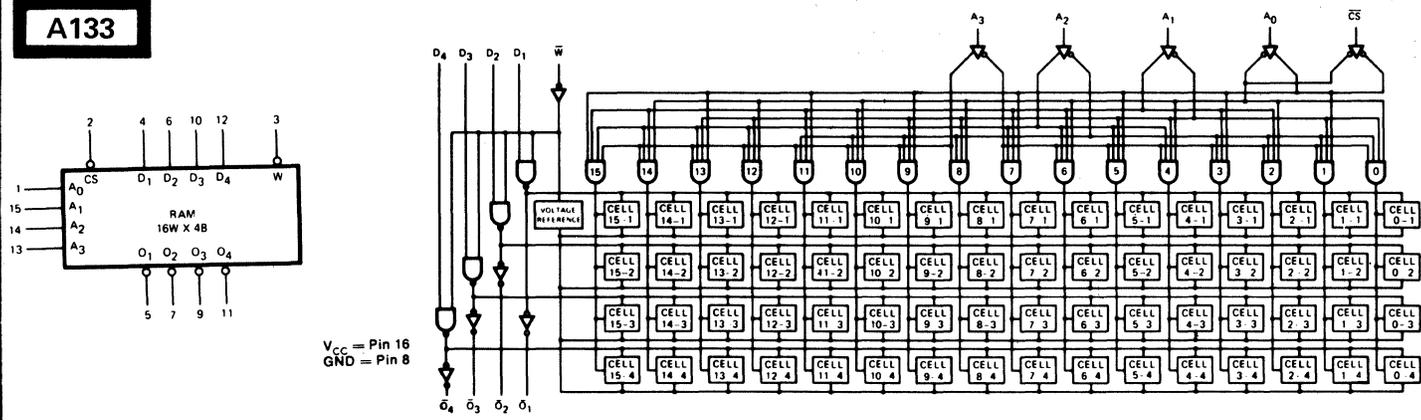
**A131**



**A132**



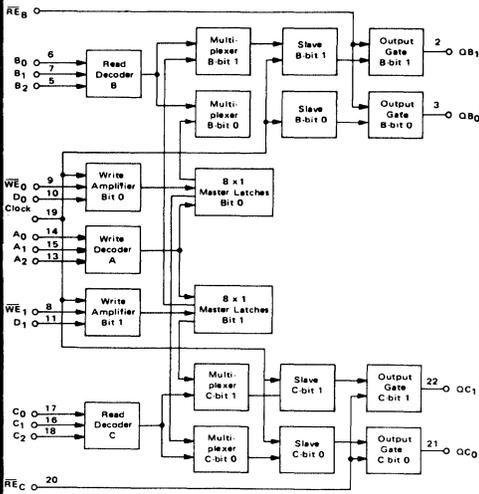
**A133**



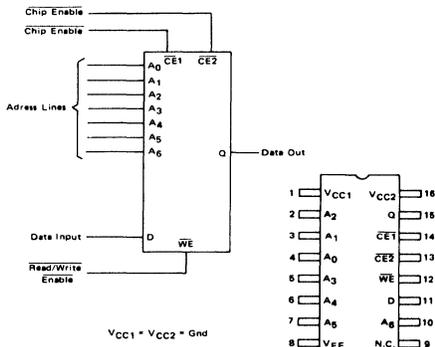
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

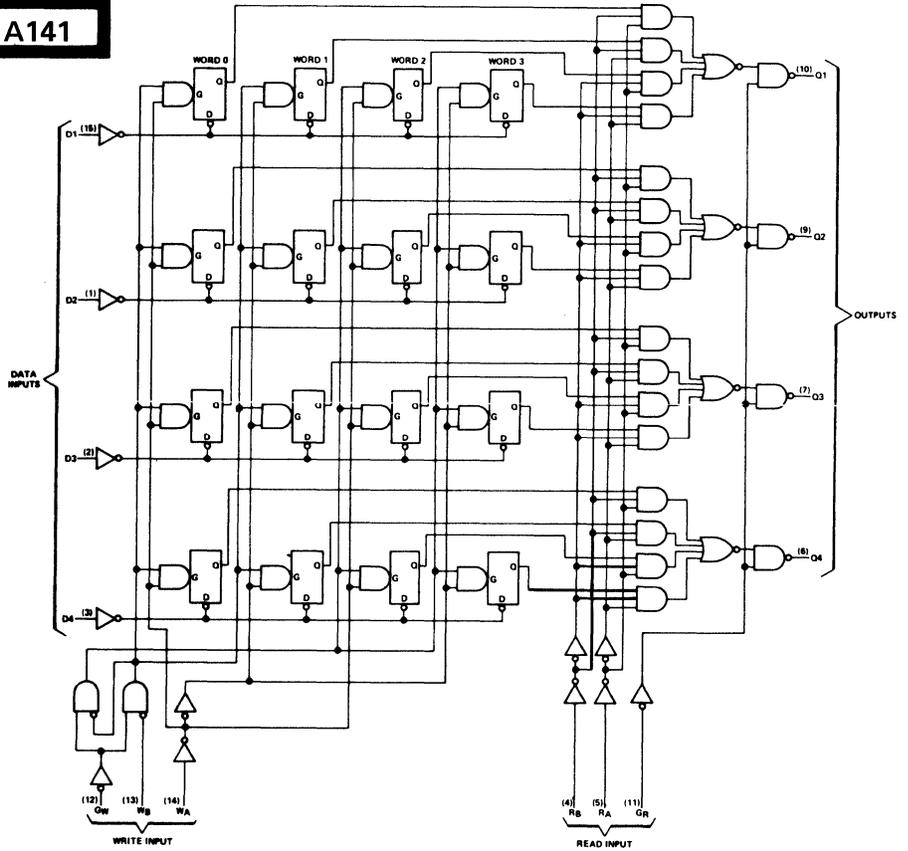
**A134**



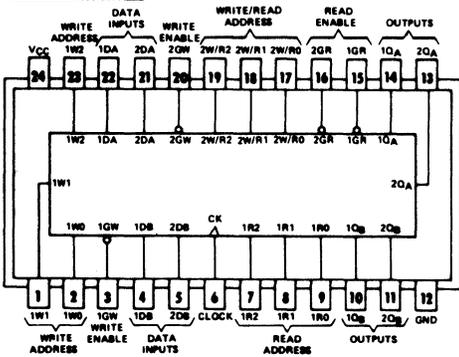
**A136**



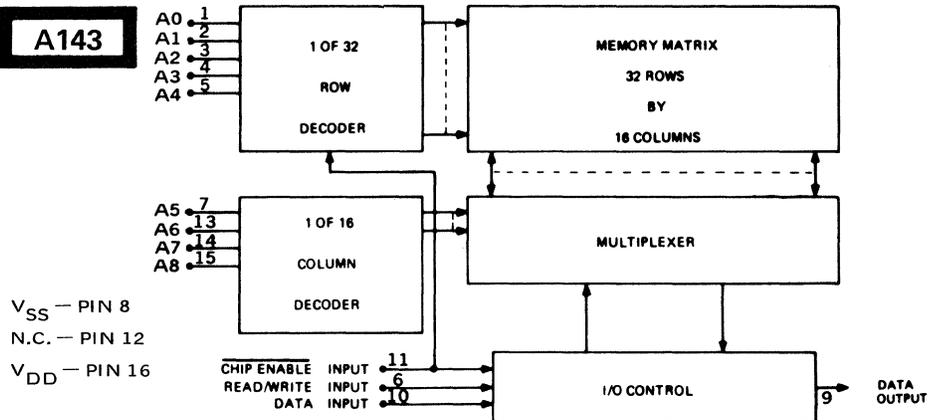
**A141**



**A142**



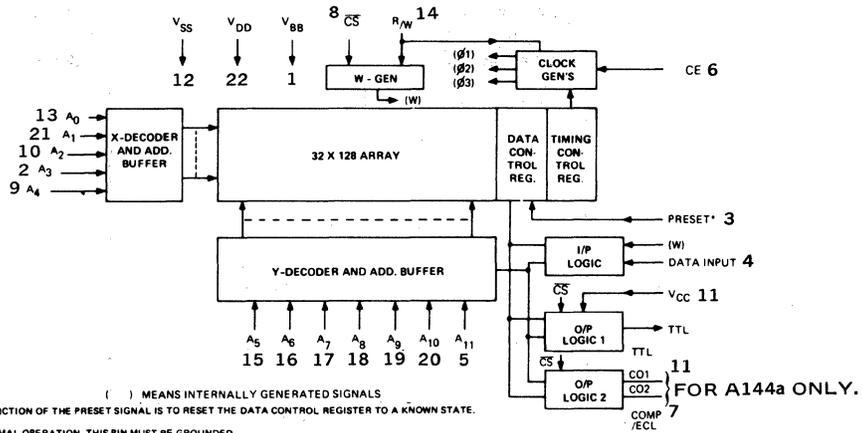
**A143**



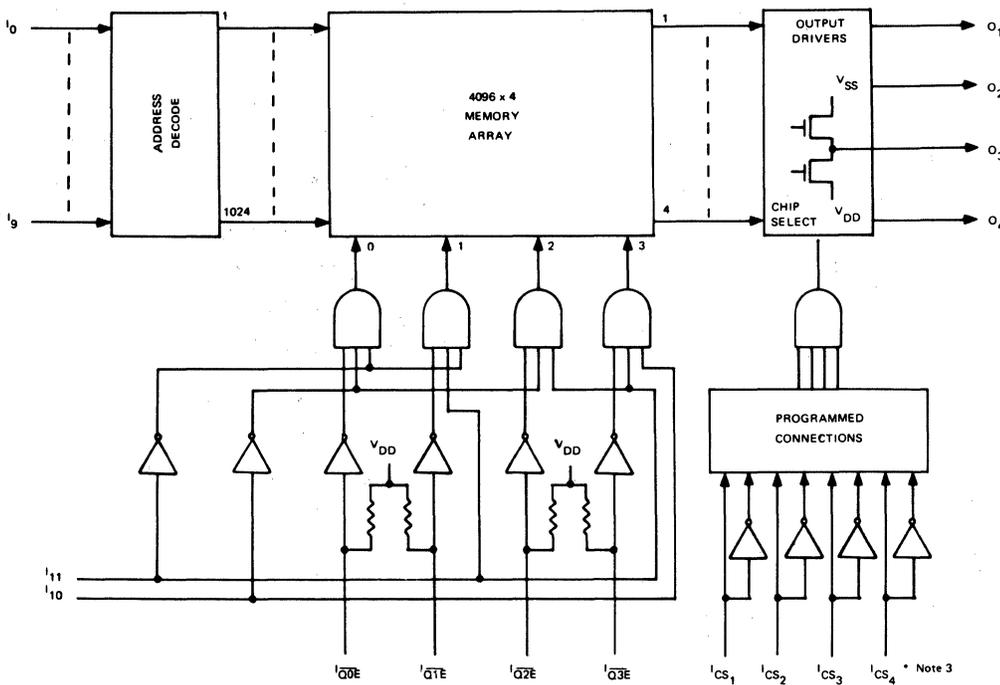
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A144**



**A146**



**A146**

Pin/Function	Pin/Function
1 V <sub>SS</sub> (+5V)	24 V <sub>DD</sub> (GRD)
2 NC	23 I <sub>CS1</sub>
3 $\phi$ 1 READ	22 I <sub>CS2</sub>
4 I <sub>0</sub>	21 I <sub>CS3</sub>
5 I <sub>1</sub>	20 I <sub>11</sub>
6 I <sub>2</sub>	19 O <sub>1</sub>
7 I <sub>3</sub>	18 O <sub>2</sub>
8 I <sub>4</sub>	17 O <sub>3</sub>
9 I <sub>5</sub>	16 O <sub>4</sub>
10 I <sub>6</sub>	15 V <sub>GG</sub> (-12V)
11 I <sub>7</sub>	14 I <sub>10</sub>
12 I <sub>8</sub>	13 I <sub>9</sub>

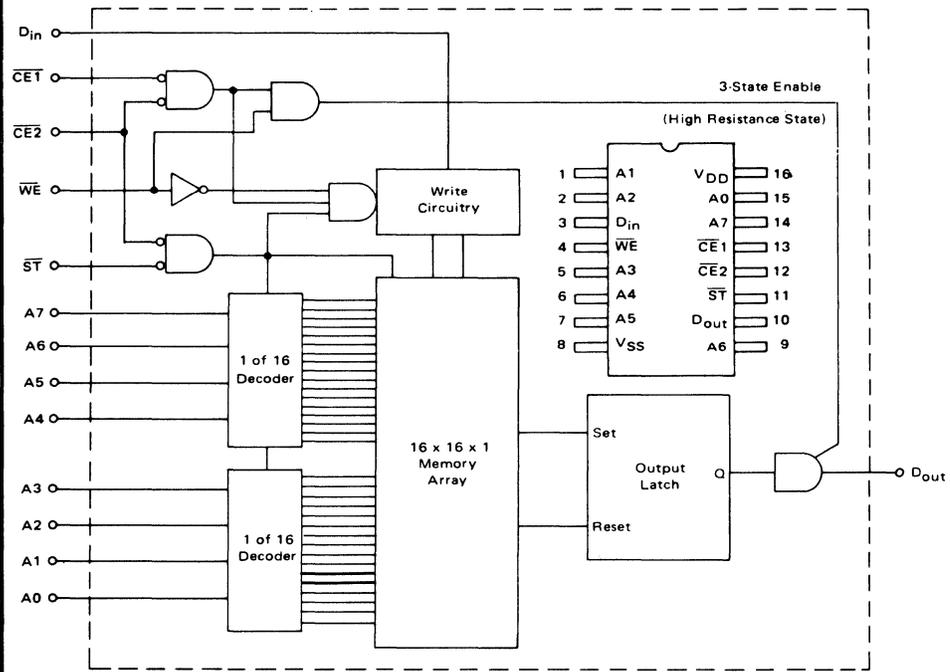
**A146a**

Pin/Function	Pin/Function
1 V <sub>SS</sub>	28 V <sub>DD</sub>
2 $\phi$ 1 READ	27 I <sub>CS1</sub>
3 NC	26 I <sub>CS2</sub>
4 I <sub>0</sub>	25 I <sub>CS3</sub>
5 I <sub>1</sub>	24 I <sub>11</sub>
6 I <sub>2</sub>	23 O <sub>1</sub>
7 I <sub>3</sub>	22 O <sub>2</sub>
8 I <sub>4</sub>	21 O <sub>3</sub>
9 I <sub>5</sub>	20 O <sub>4</sub>
10 I <sub>6</sub>	19 V <sub>GG</sub>
11 I <sub>7</sub>	18 I <sub>10</sub>
12 I <sub>8</sub>	17 I <sub>9</sub>
13 I <sub>QOE</sub>	16 I <sub>Q3E</sub>
14 I <sub>QTE</sub>	15 I <sub>Q2E</sub>

# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A147**



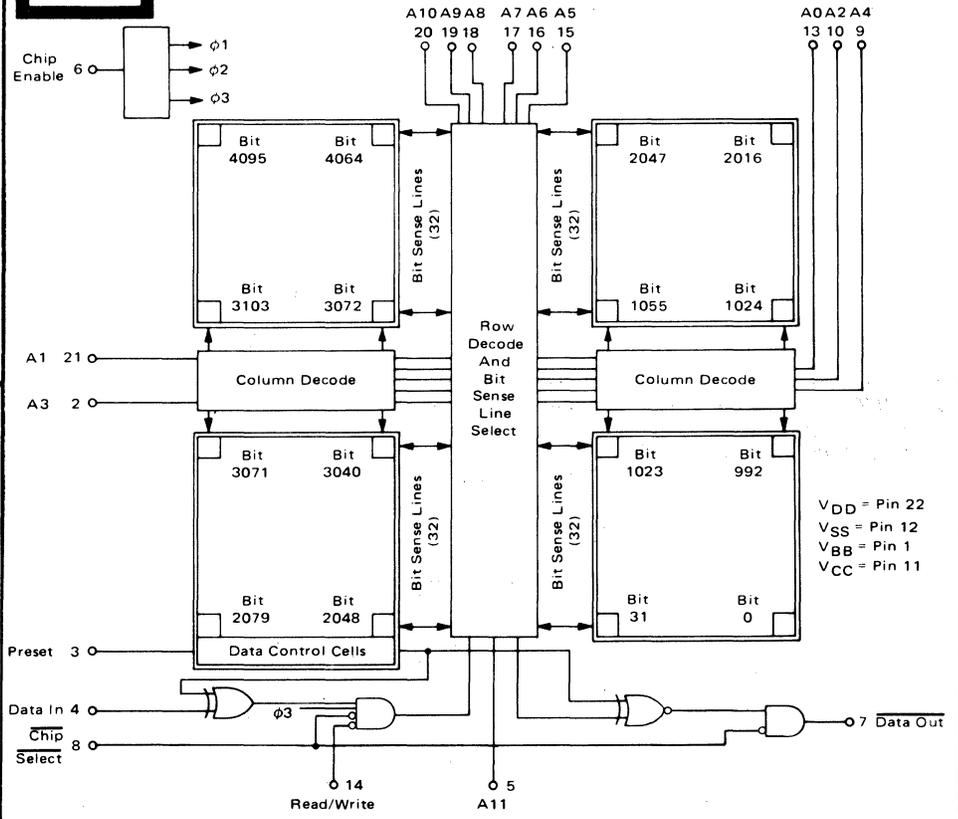
[ ]

[ ]

[ ]

[ ]

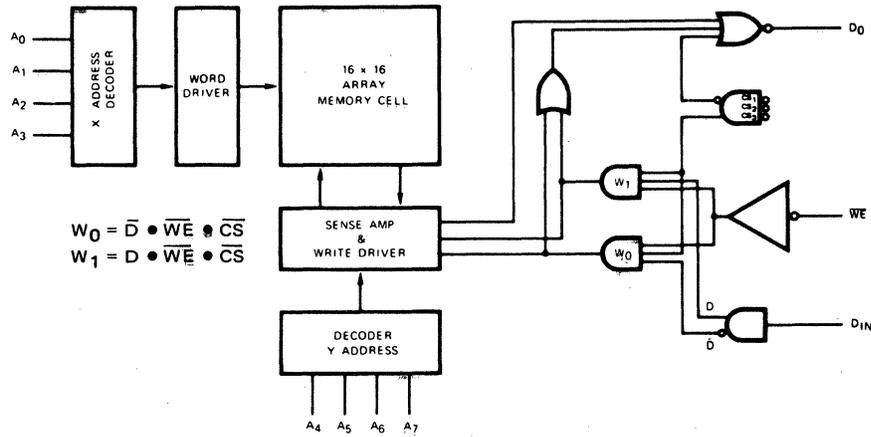
**A148**



# 22. LOGIC/BLOCK DRAWINGS

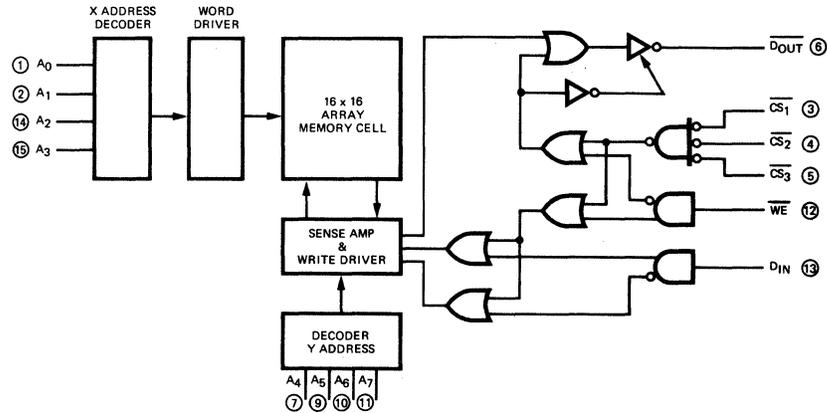
IN DRAWING NUMBER SEQUENCE

**A150**

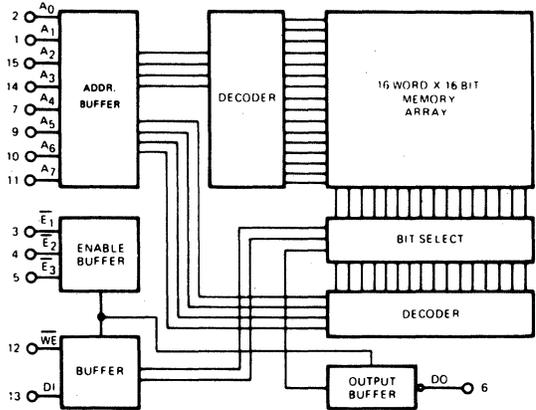


	A							CS			WE	DIN	REMARKS		
	0	1	2	3	4	5	6	7	1	2				3	
A150	1	2	3	4	9	10	11	12	15	5	6	7	14	18	
A150a	1	2	14	15	7	9	10	11	6	3	4	5	12	13	NON-INVERTED DO
A150b	1	2	3	4	12	13	14	15	11	5	6	7	10	9	
A150c	1	2	7	9	10	11	14	15	6	3	4	5	12	13	

**A153**



**A154**



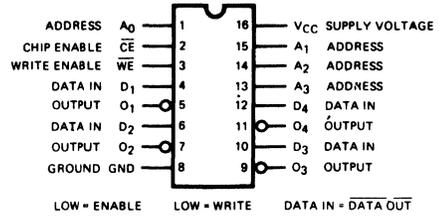
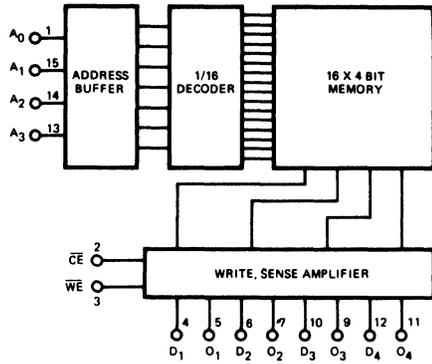
ADDRESS	A1	1	16	VCC	SUPPLY VOLTAGE
ADDRESS	A0	2	15	A2	ADDRESS
ENABLE	E1	3	14	A3	ADDRESS
ENABLE	E2	4	13	DI	DATA IN
ENABLE	E3	5	12	WE	WRITE ENABLE
DATA OUT	DO	6	11	A7	ADDRESS
ADDRESS	A4	7	10	A6	ADDRESS
GROUND	GND	8	9	A5	ADDRESS

LOW = ENABLE    LOW = WRITE    DATA IN = DATA OUT

# 22. LOGIC/BLOCK DRAWINGS

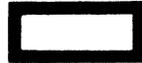
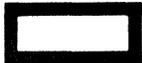
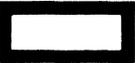
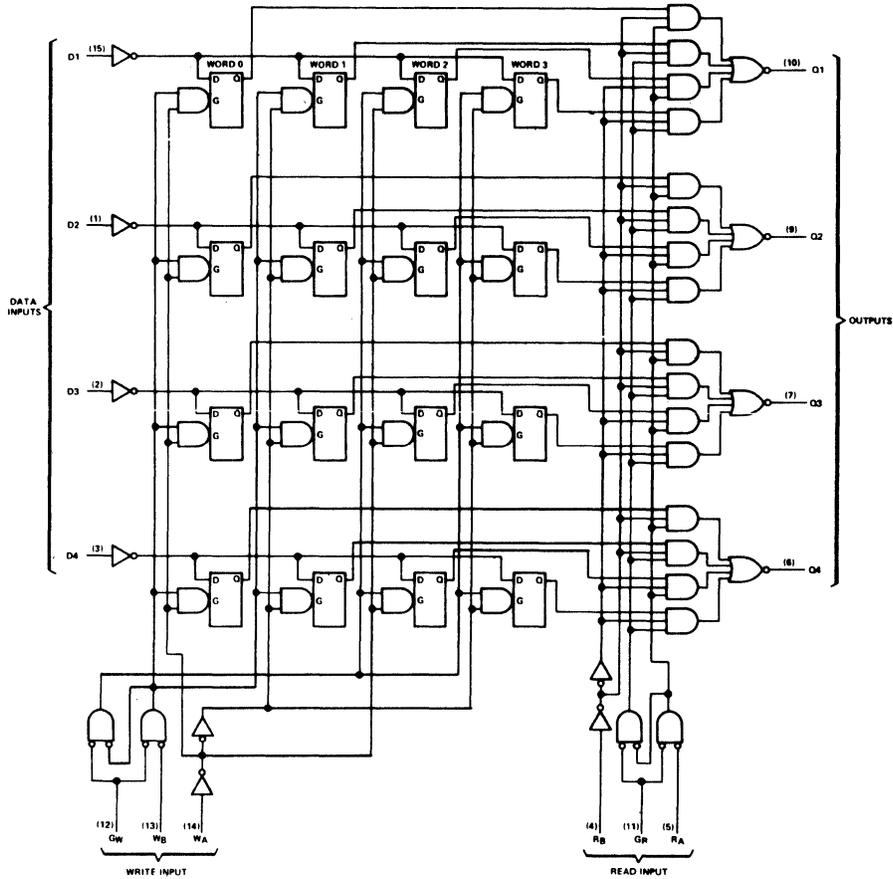
IN DRAWING NUMBER  
SEQUENCE

**A155**



V<sub>CC</sub>/V<sub>DD</sub>-PIN 16  
GND/V<sub>SS</sub>-PIN 8

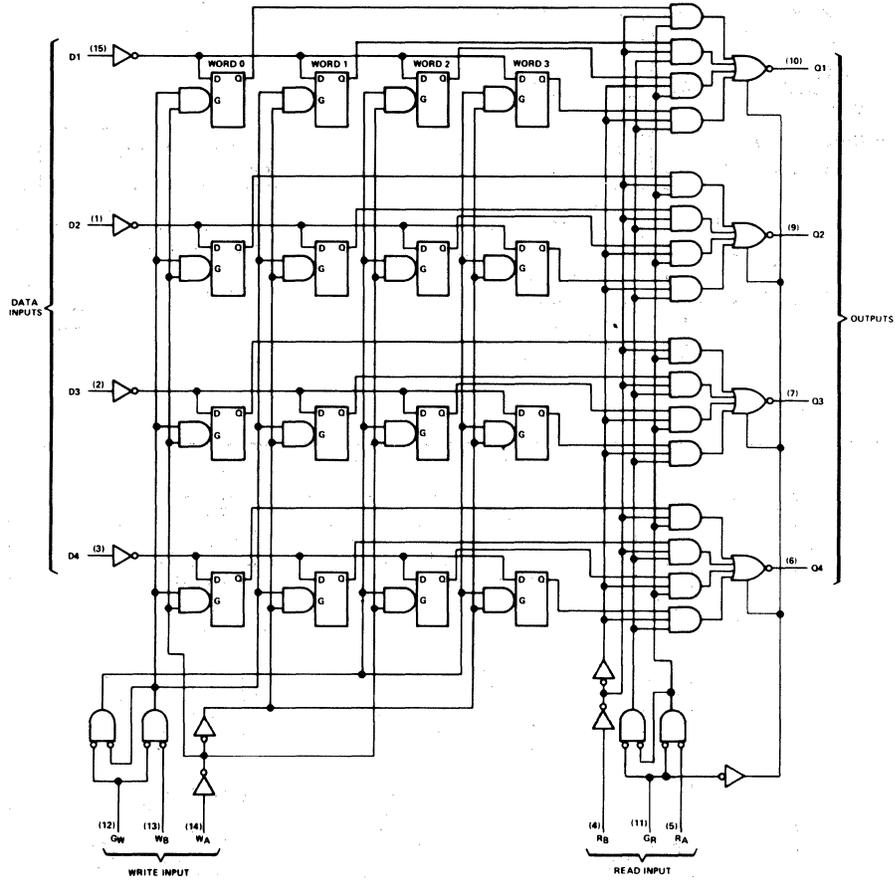
**A156**



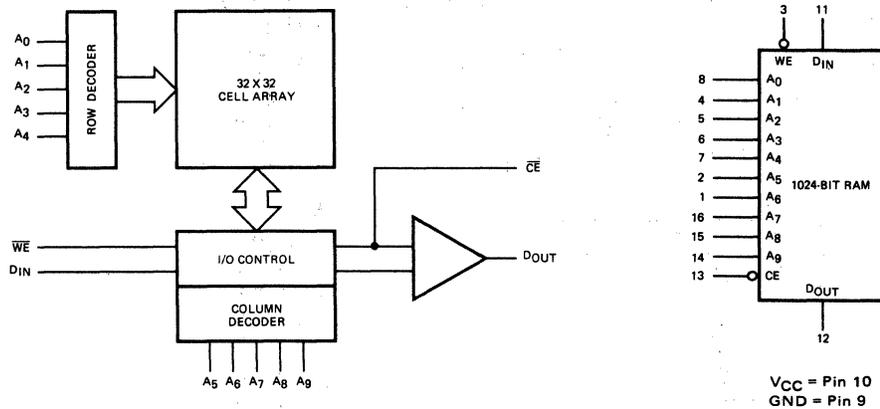
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

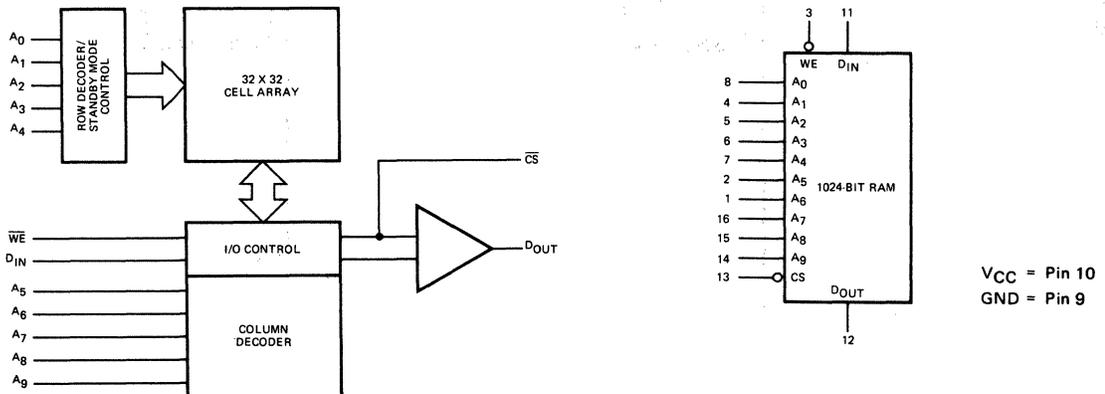
**A157**



**A158**



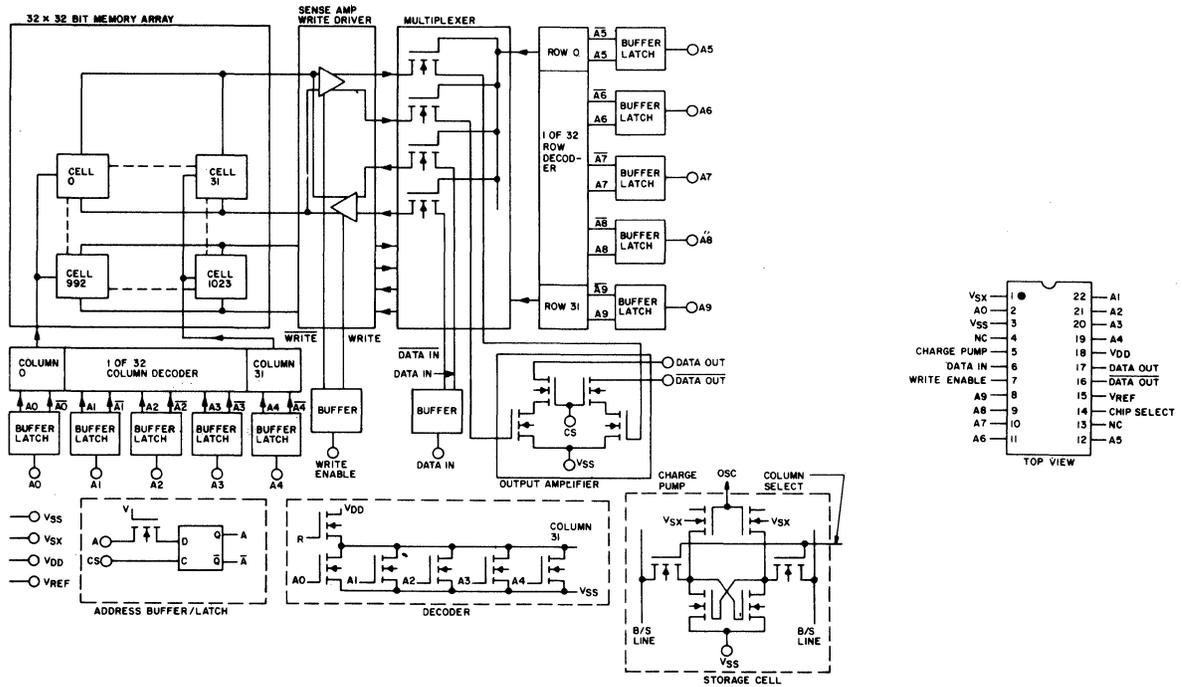
**A159**



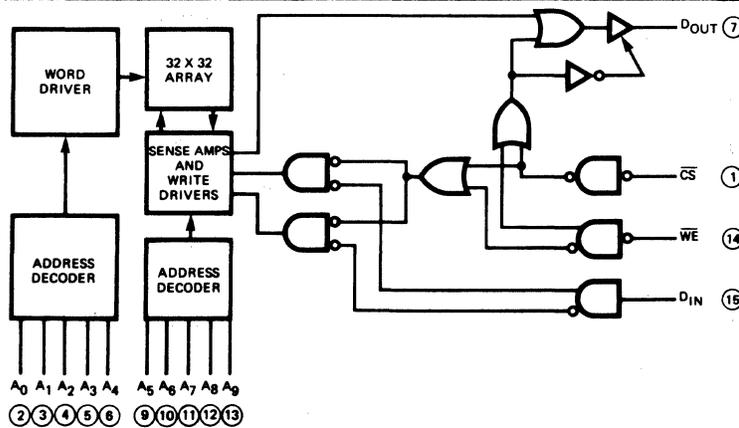
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

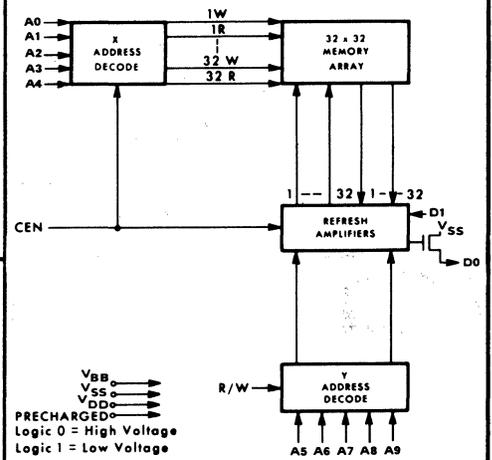
**A162**



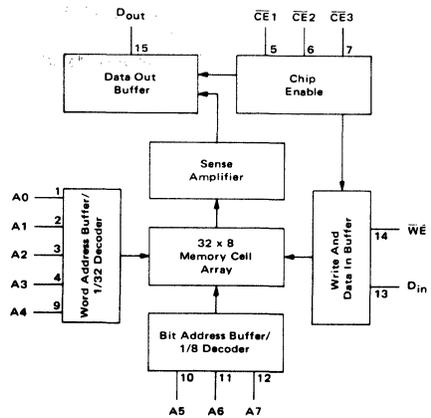
**A163**



**A164**



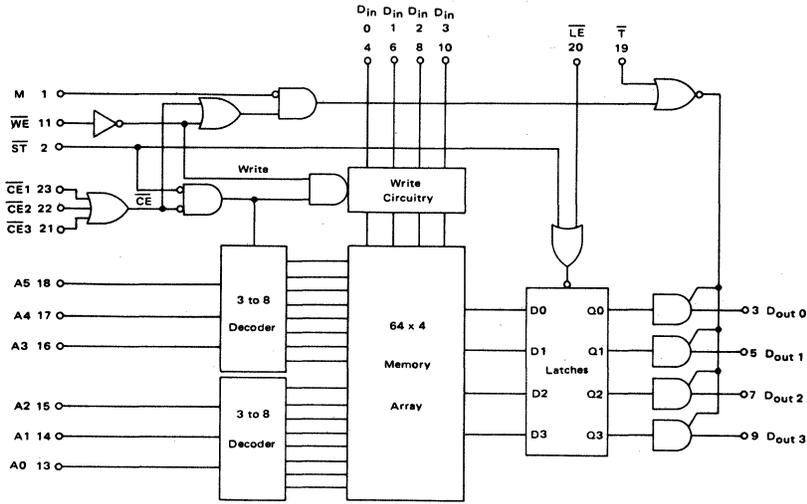
**A165**



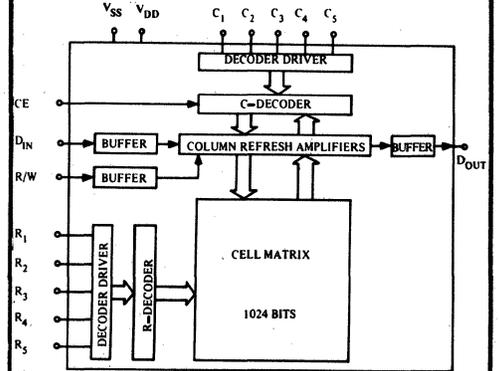
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

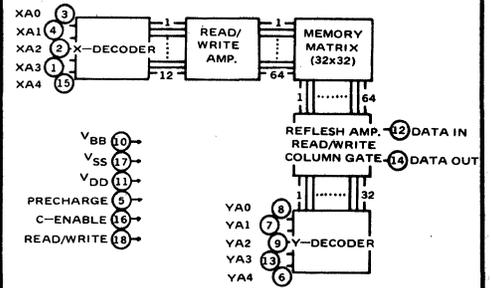
**A166**



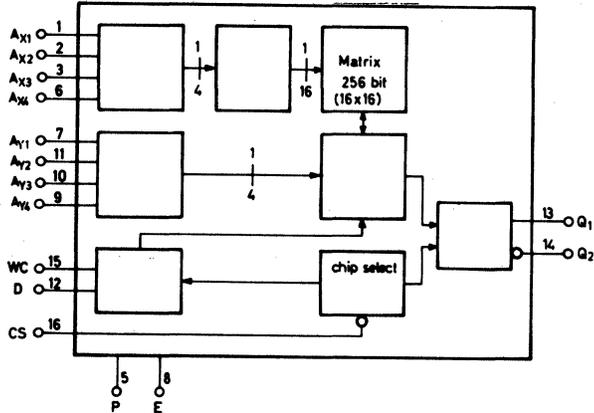
**A167**



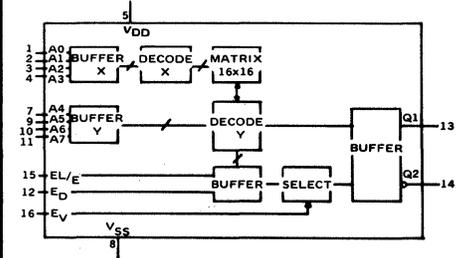
**A168**



**A170**

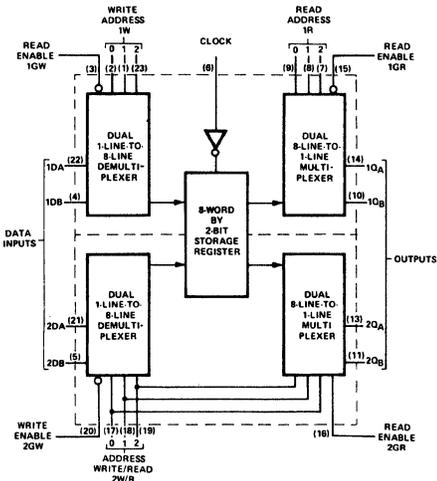


**A171**

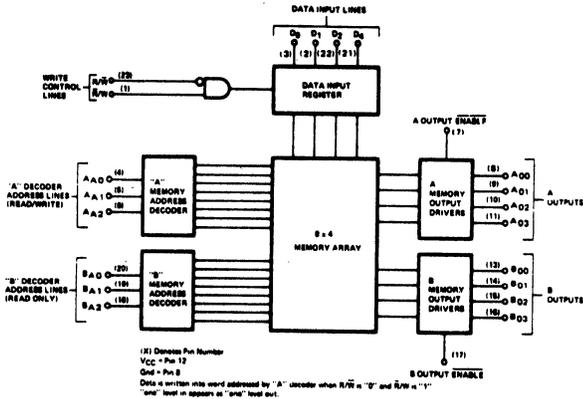


# 22. LOGIC/BLOCK DRAWINGS IN DRAWING NUMBER SEQUENCE

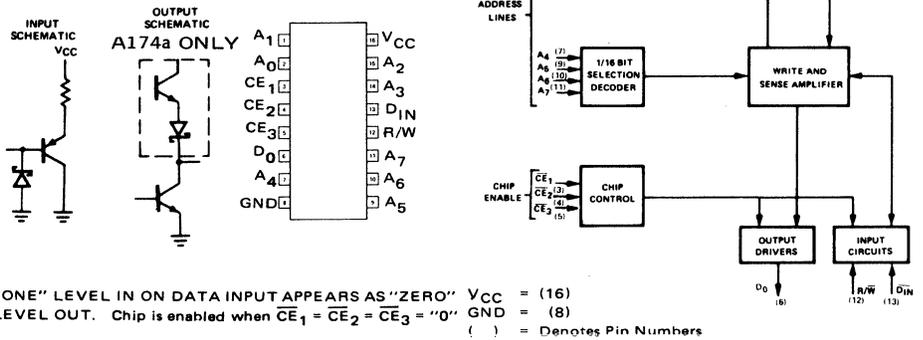
**A172**



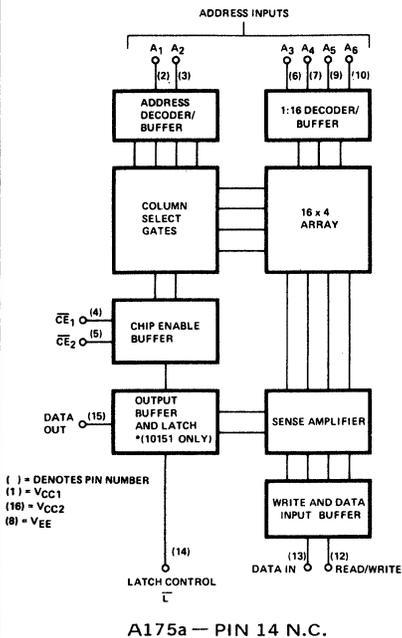
**A173**



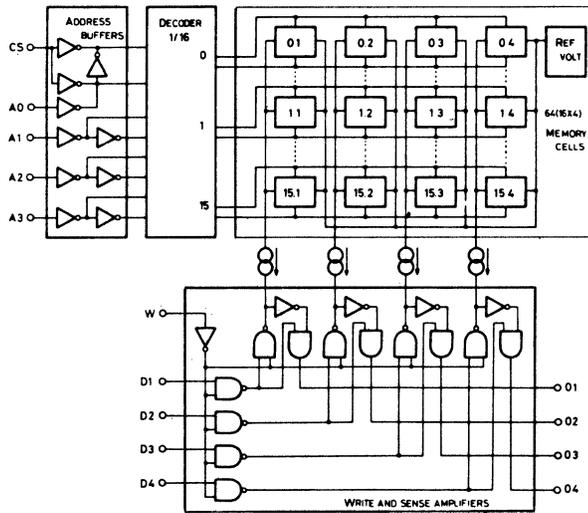
**A174**



**A175**



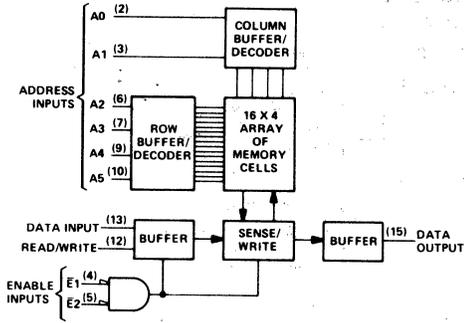
**A176**



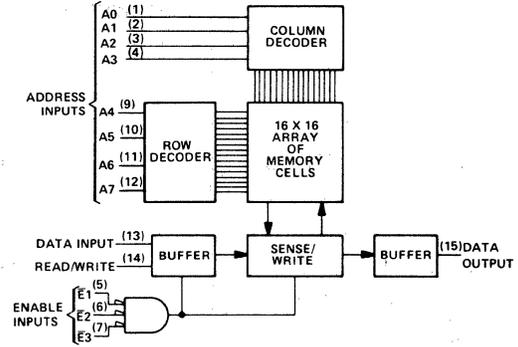
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

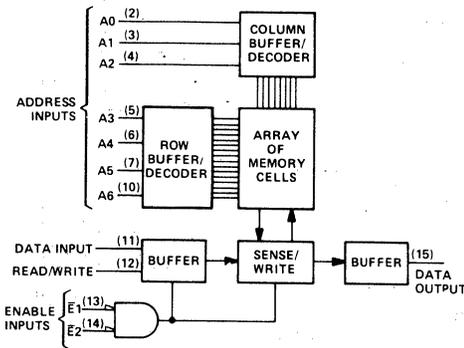
**A177**



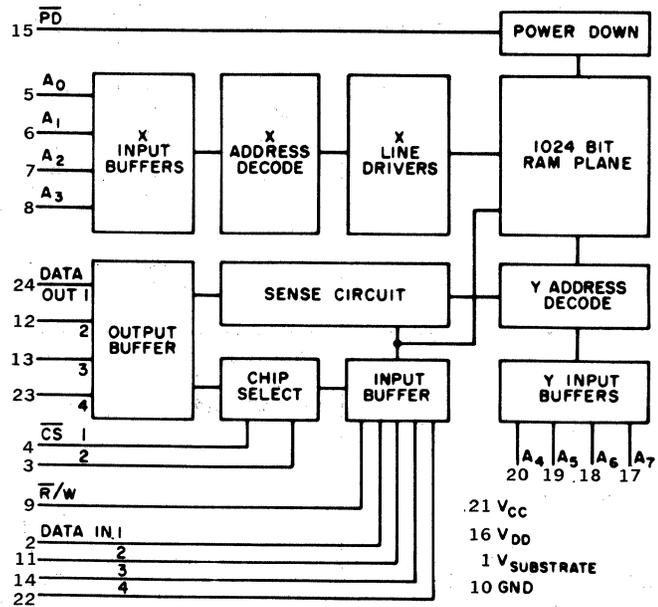
**A178**



**A180**



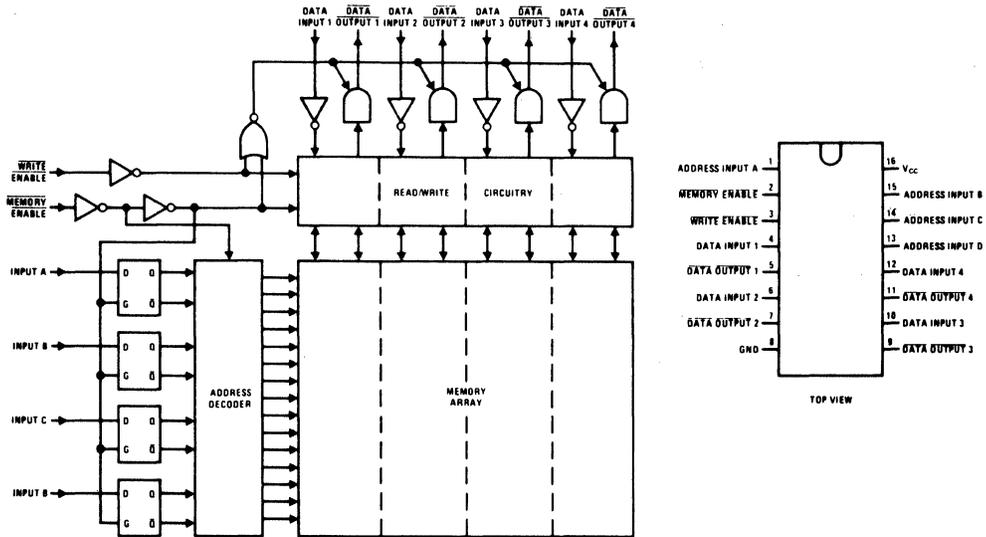
**A181**



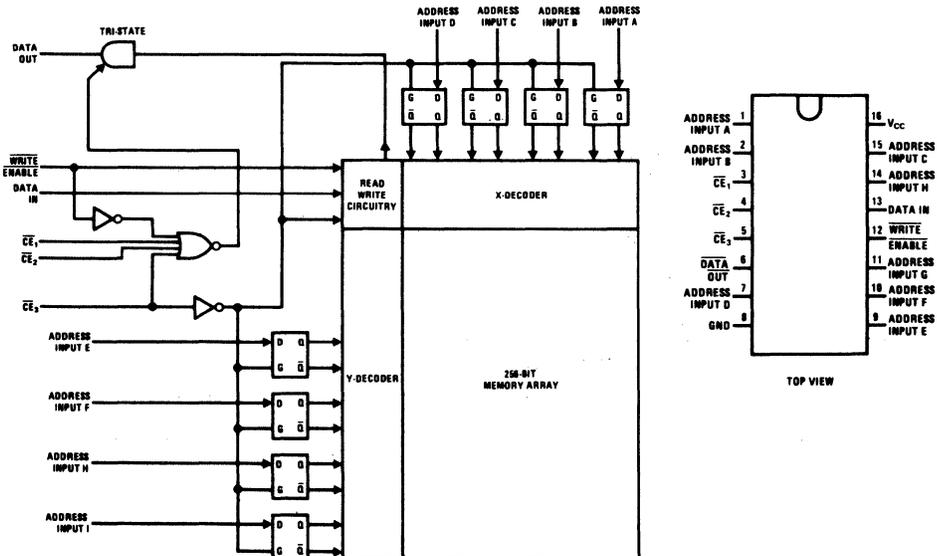
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

A182

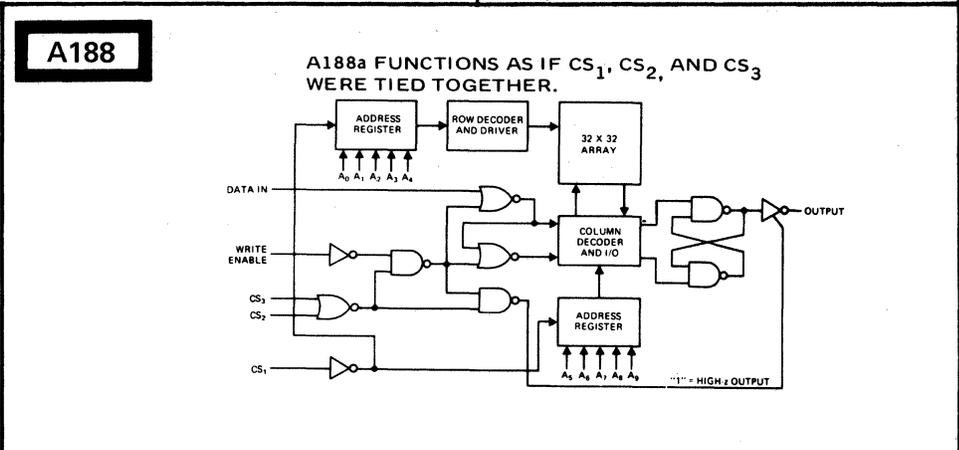
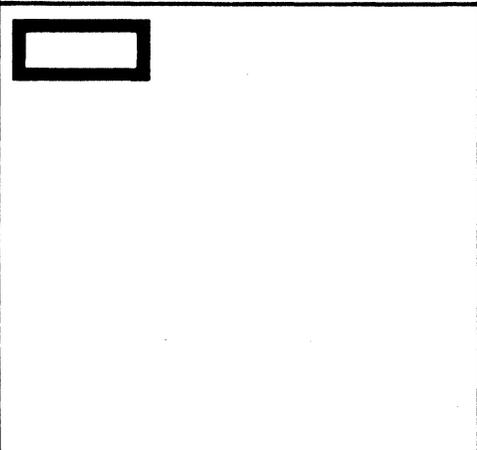
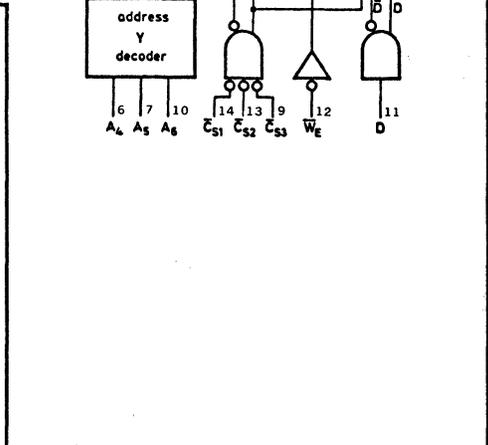
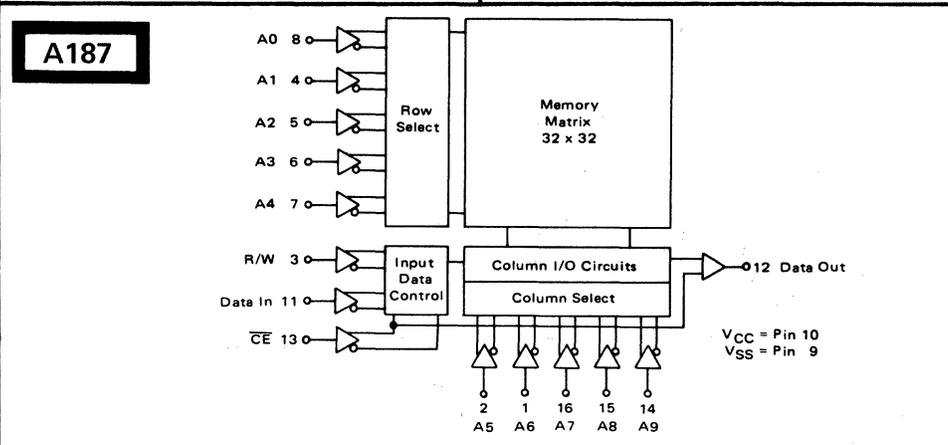
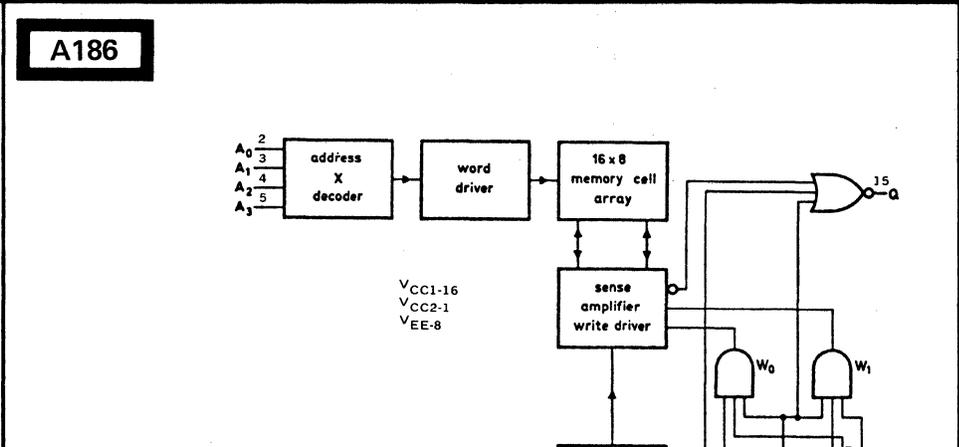
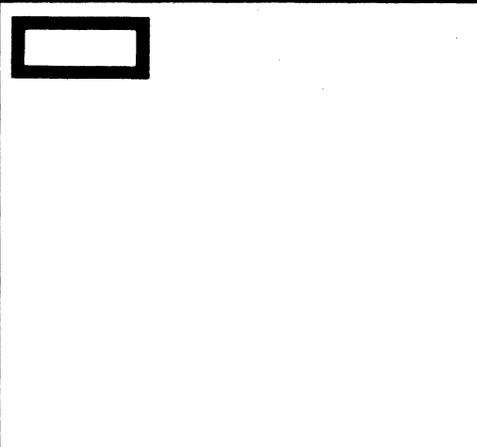
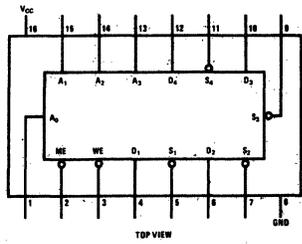
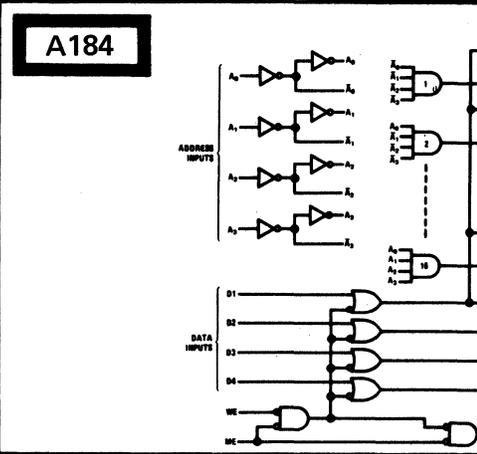


A183



# 22. LOGIC/BLOCK DRAWINGS

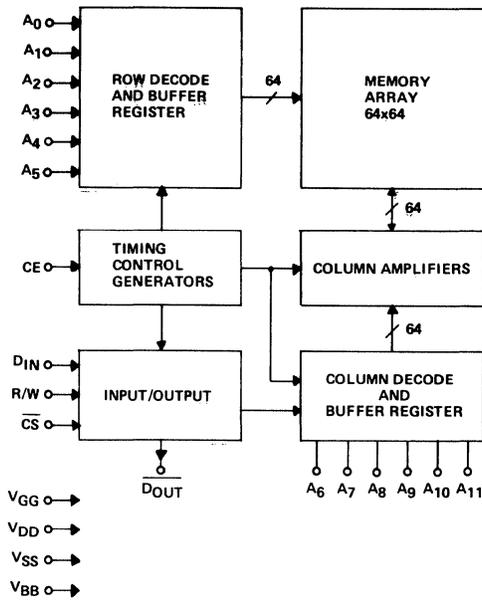
IN DRAWING NUMBER  
SEQUENCE



# 22. LOGIC/BLOCK DRAWINGS

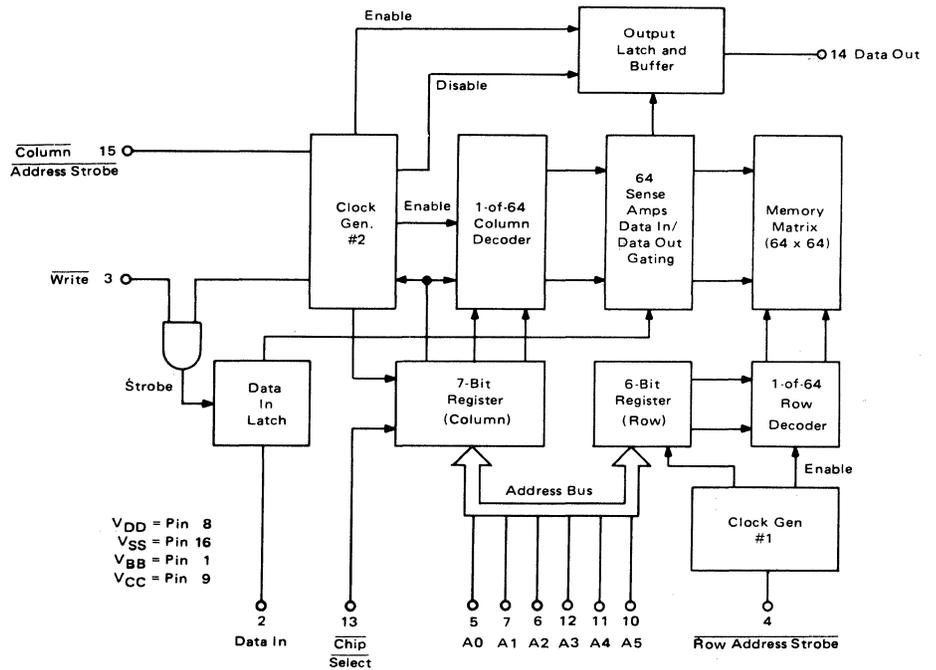
IN DRAWING NUMBER SEQUENCE

**A189**



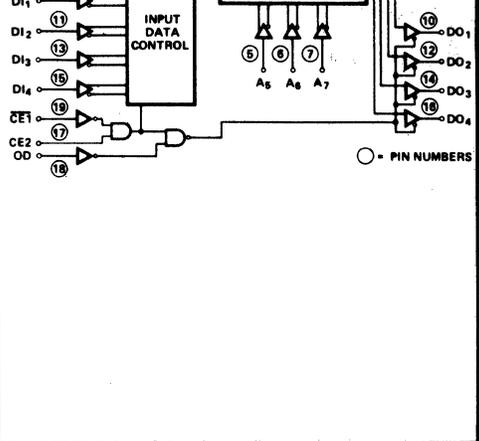
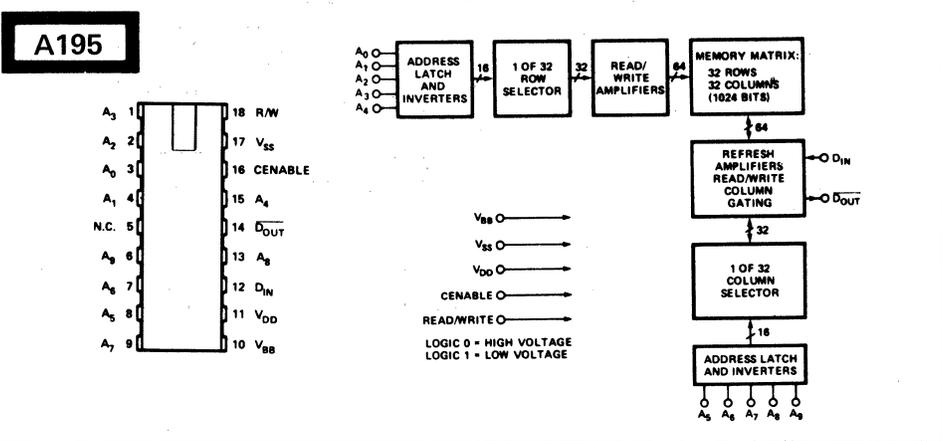
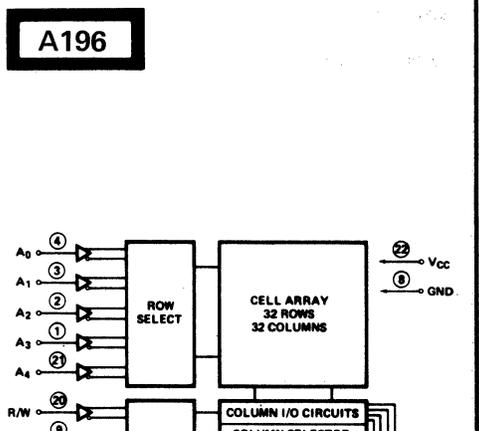
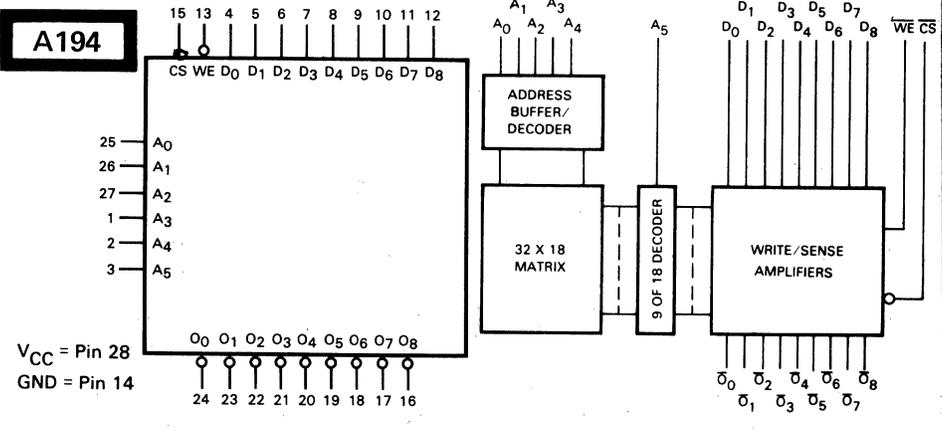
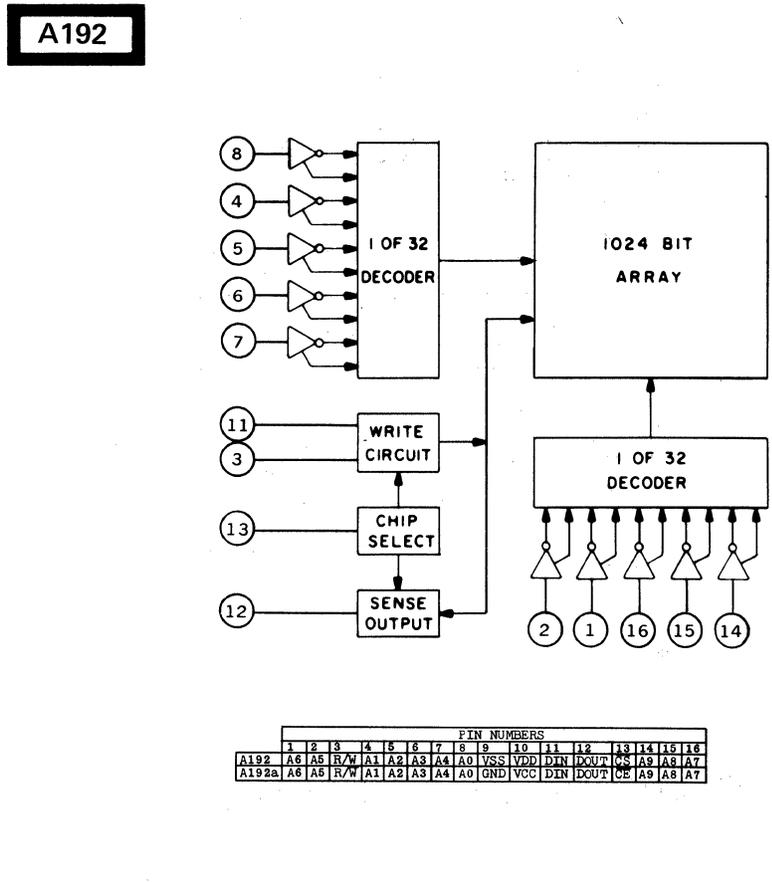
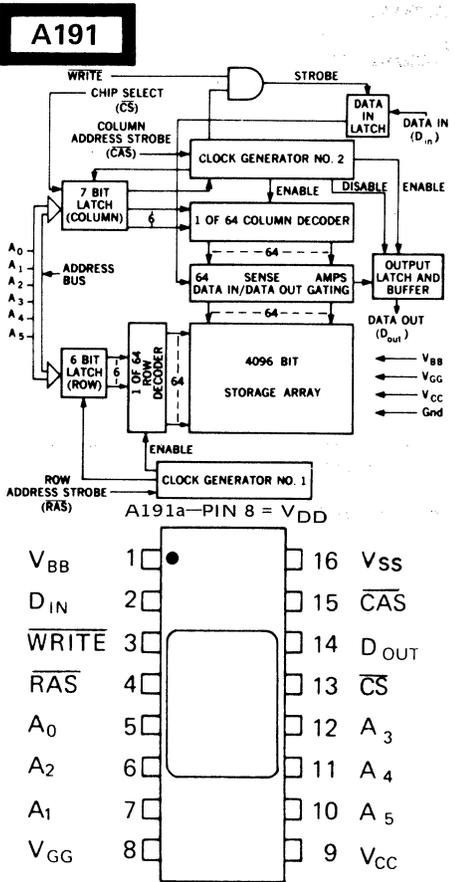
		PIN NUMBERS																					
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22
A189	VBB	A9	A10	A11	CS	DIN	DOUT	A0	A1	A2	VDD	R/W	A3	A4	A5	NC	CE	VGG	A6	A7	A8	VSS	
A189a	VBB	A9	A10	A11	CS	DIN	DOUT	A0	A1	A2	VCC	WE	A3	A4	A5	NC	CE	VDD	A6	A7	A8	VSS	

**A190**



# 22. LOGIC/BLOCK DRAWINGS

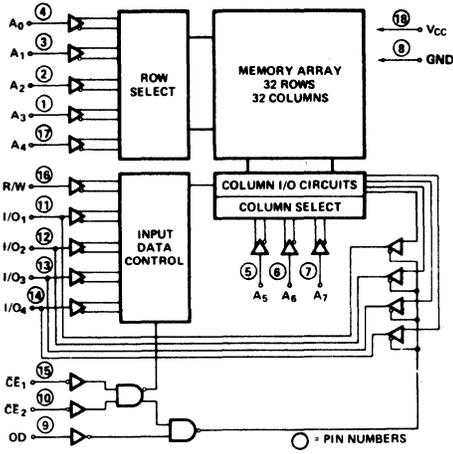
IN DRAWING NUMBER SEQUENCE



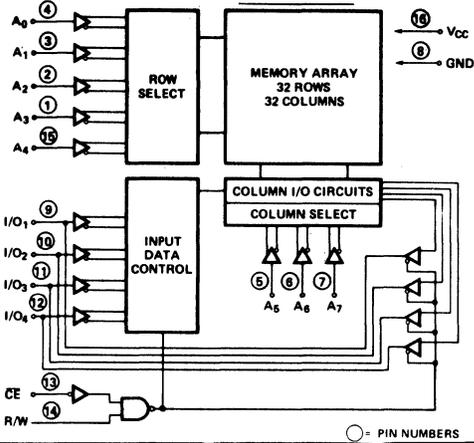
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

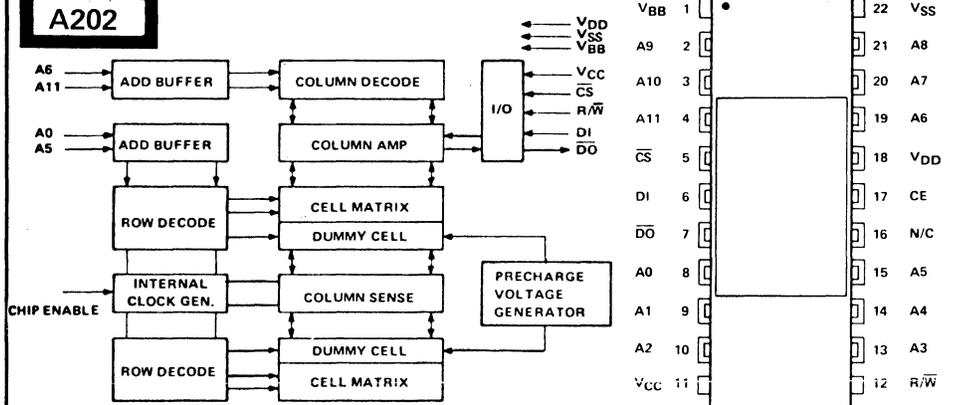
**A200**



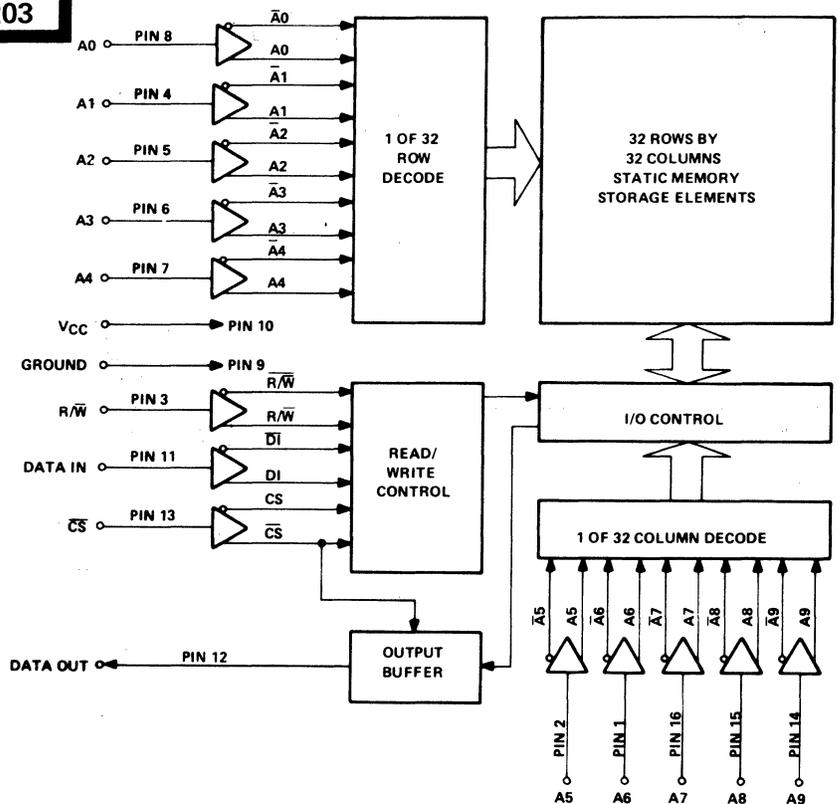
**A201**



**A202**



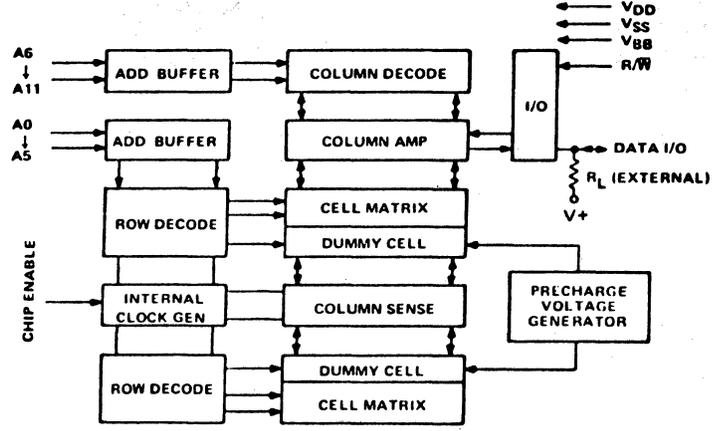
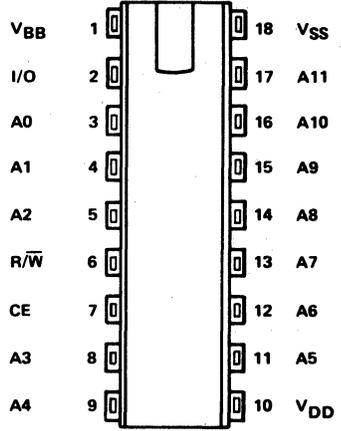
**A203**



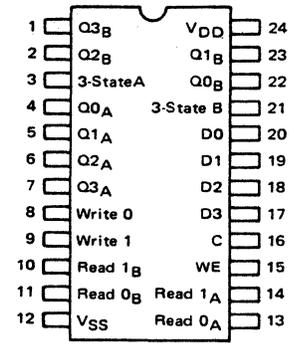
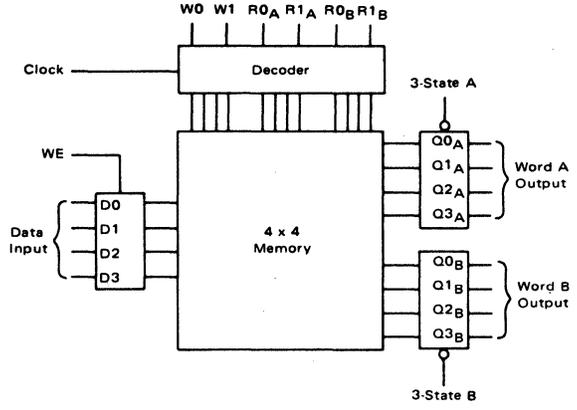
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

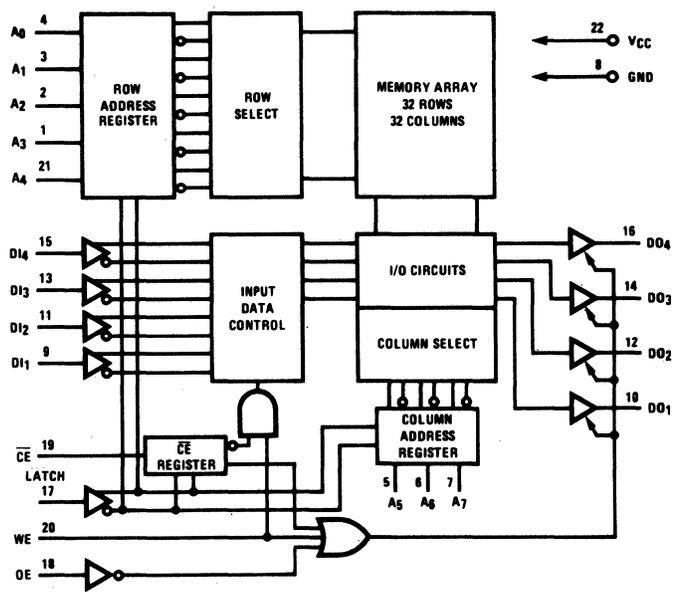
**A204**



**A205**



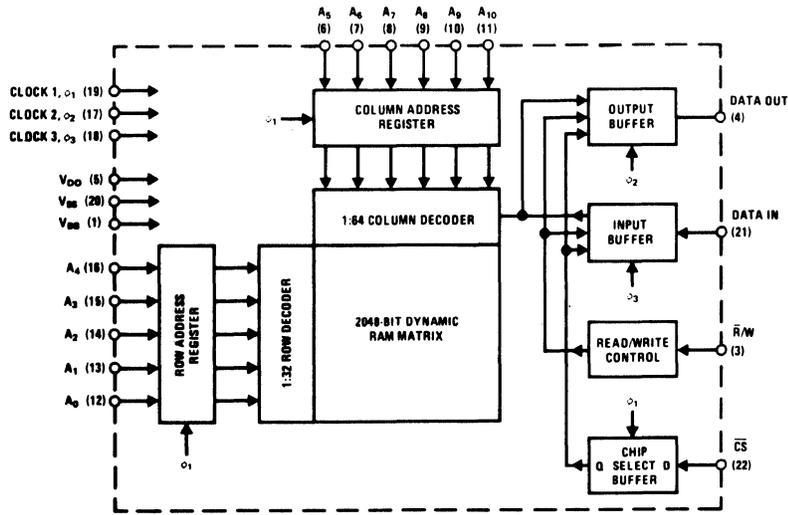
**A206**



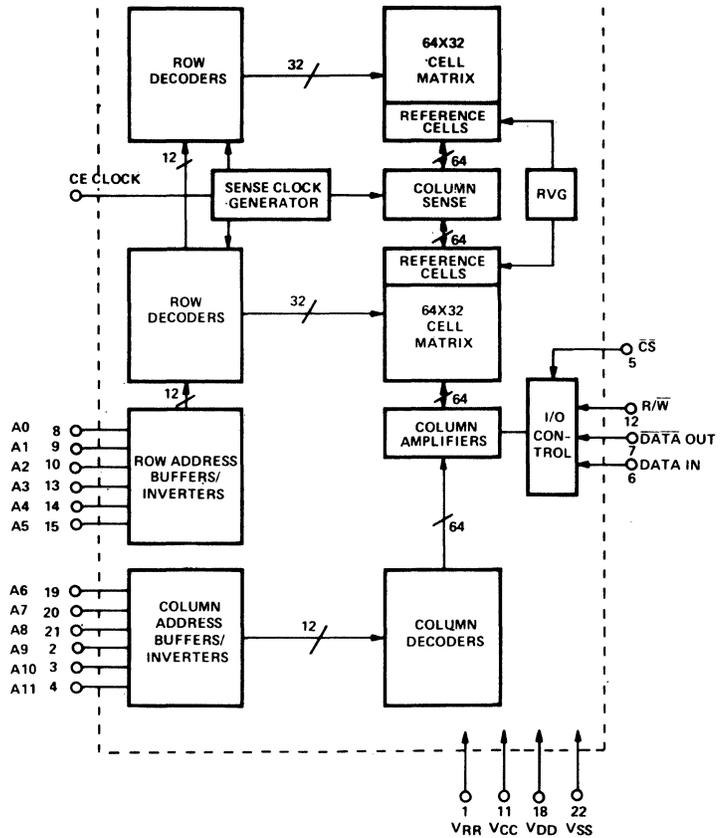
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

A207



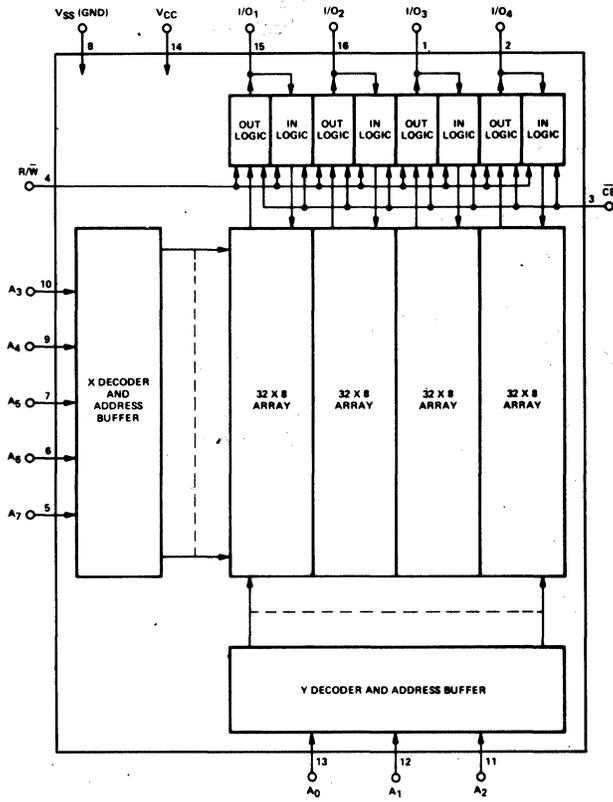
A208



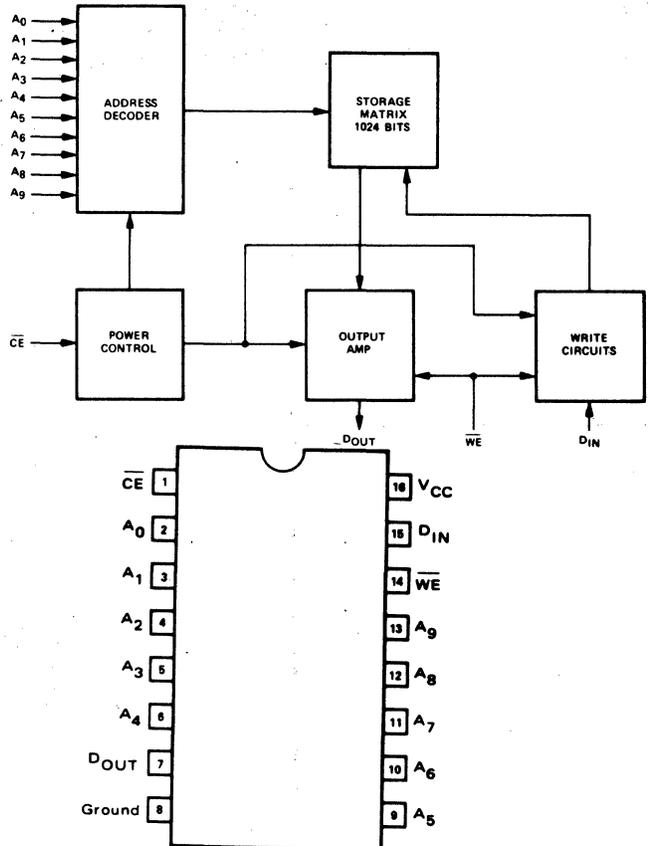
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

A209



A210

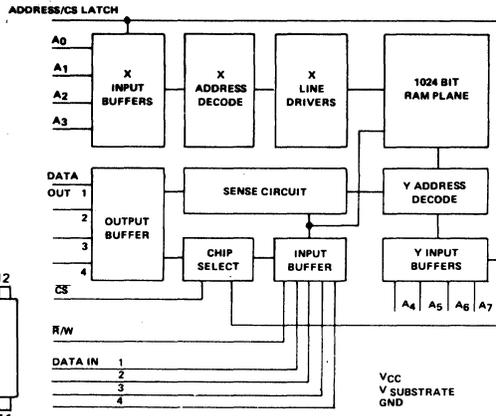
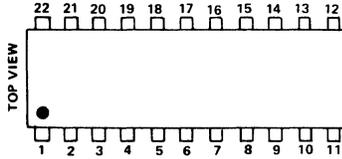


# 22. LOGIC/BLOCK DRAWINGS

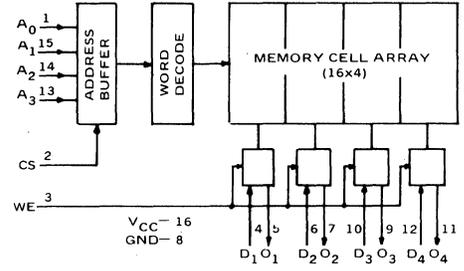
IN DRAWING NUMBER SEQUENCE

**A213**

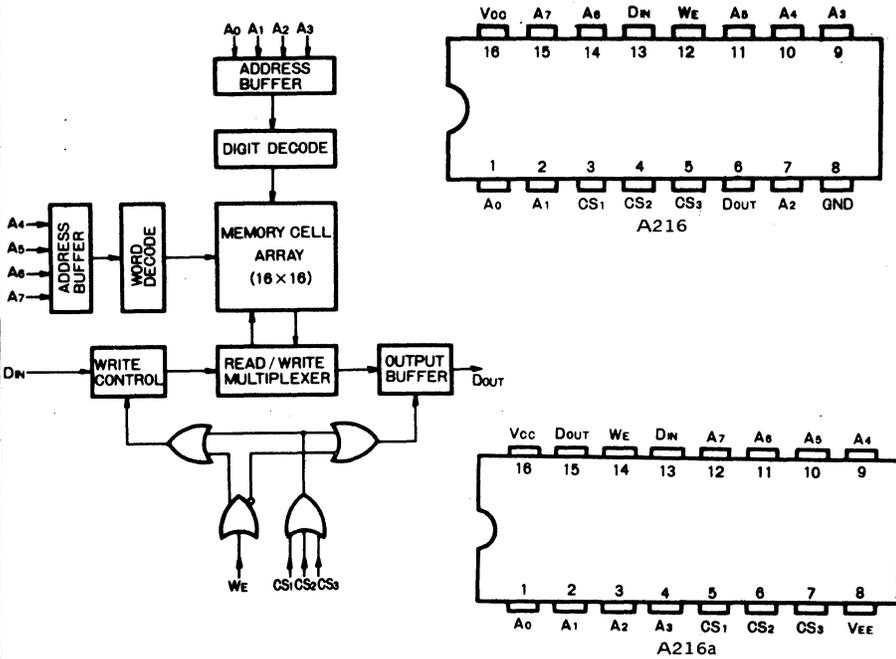
PIN	FUNCTION	PIN	FUNCTION
1	V Substrate	12	Data Out 1
2	Data Out 3	13	Data In 1
3	Data In 3	14	CS
4	A <sub>7</sub>	15	A <sub>0</sub>
5	A <sub>6</sub>	16	A <sub>1</sub>
6	A <sub>5</sub>	17	A <sub>2</sub>
7	A <sub>4</sub>	18	A <sub>3</sub>
8	Address/CS Latch	19	R/W
9	VCC	20	Gnd
10	Data In 4	21	Data In 2
11	Data Out 4	22	Data Out 2



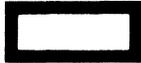
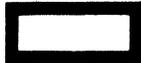
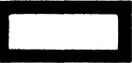
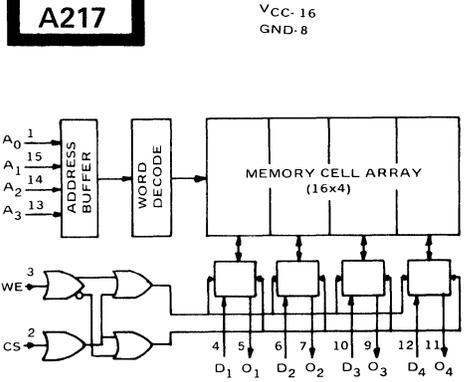
**A215**



**A216**



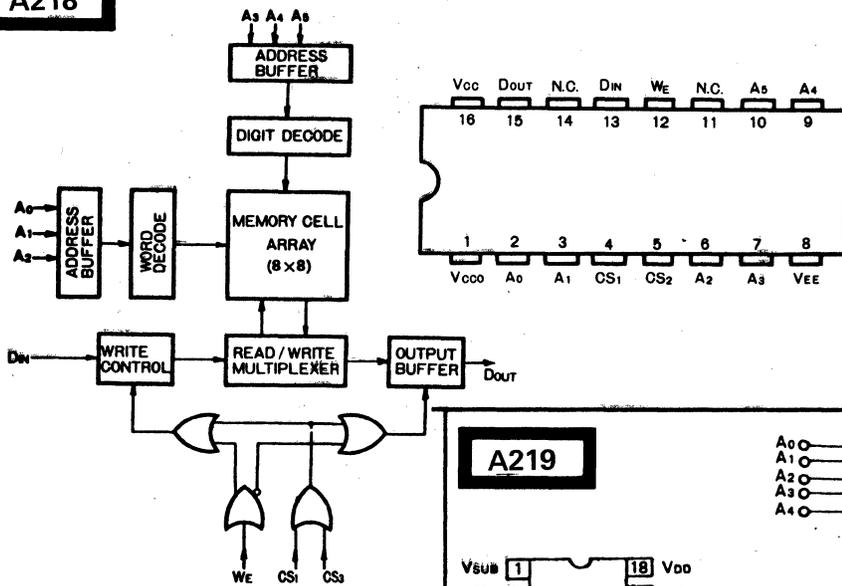
**A217**



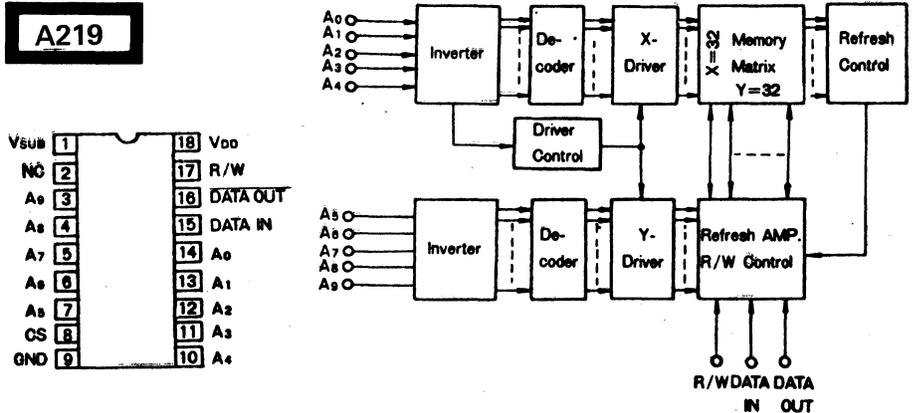
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

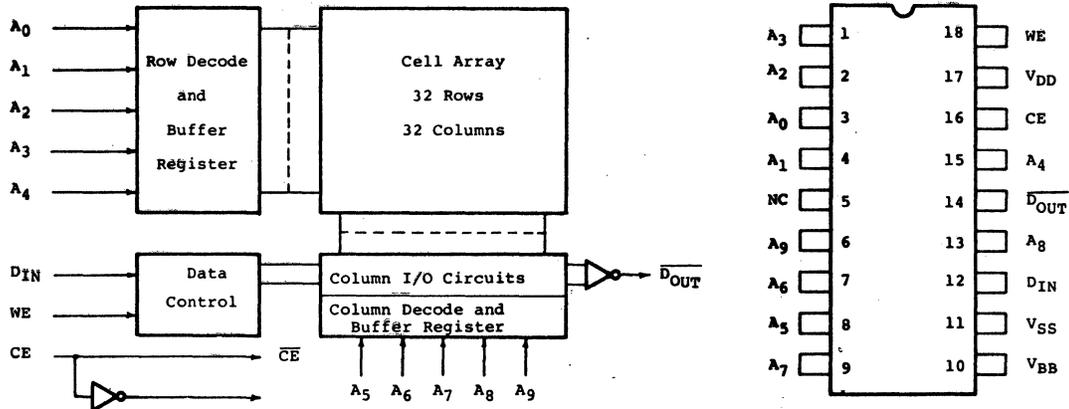
**A218**



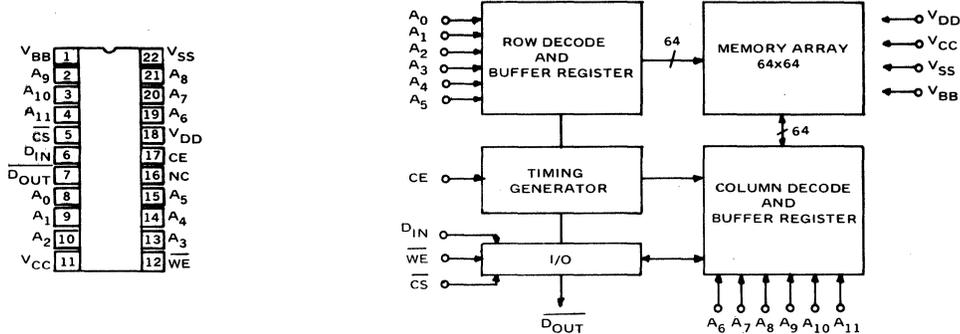
**A219**



**A220**



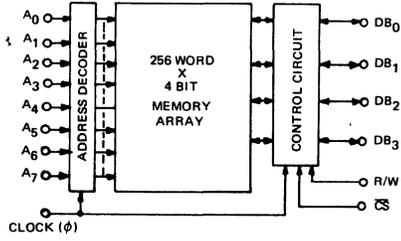
**A221**



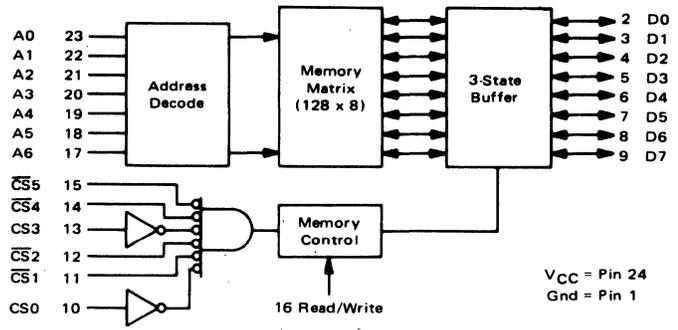
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

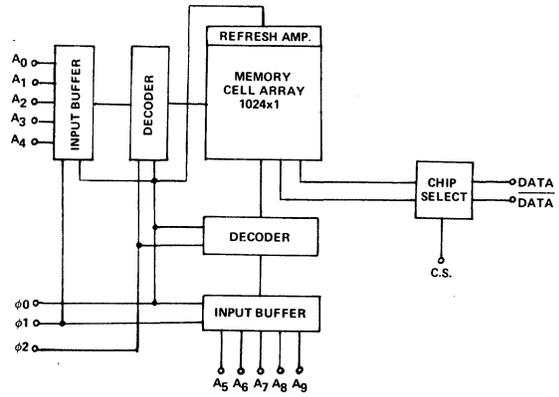
**A222**



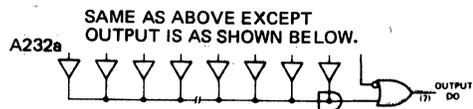
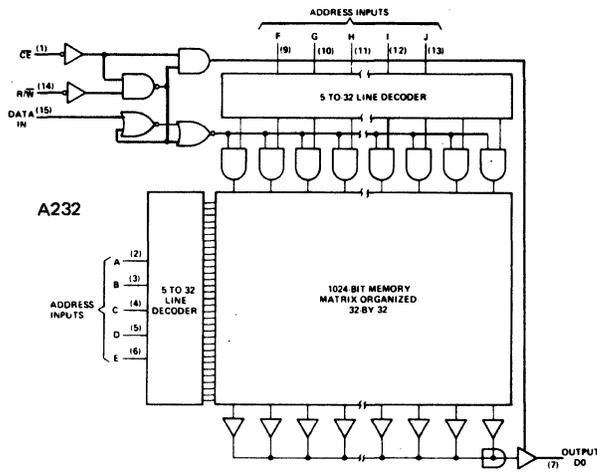
**A223**



**A226**



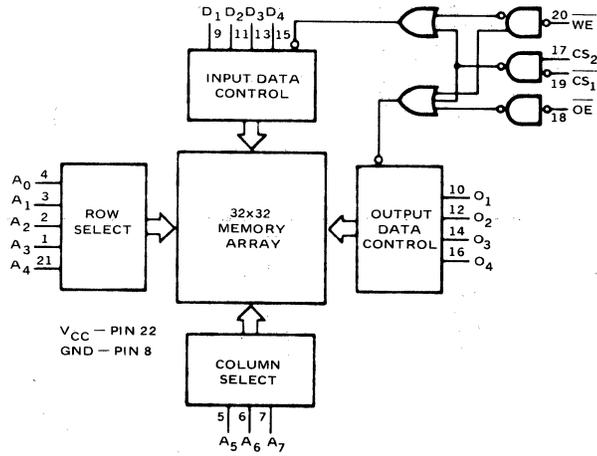
**A232**



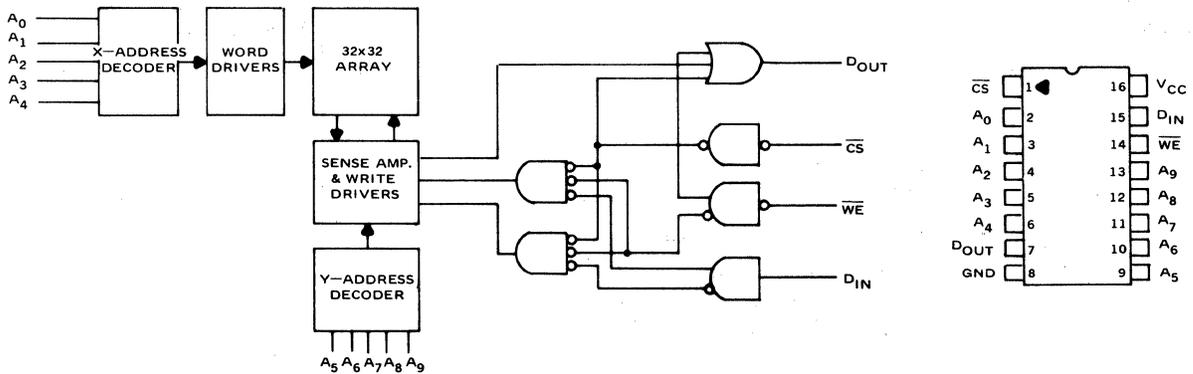
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

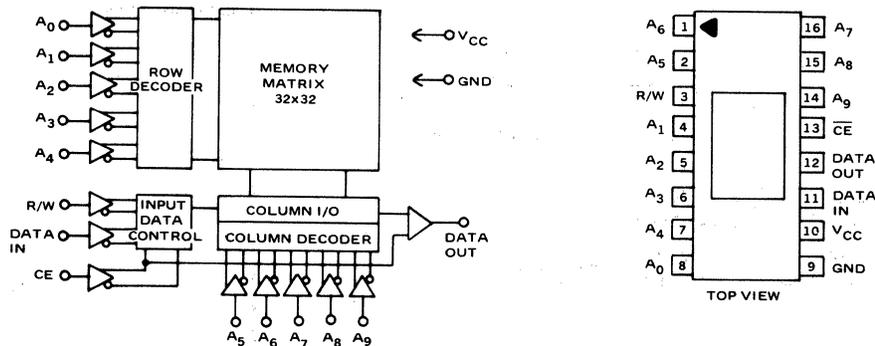
**A233**



**A235**



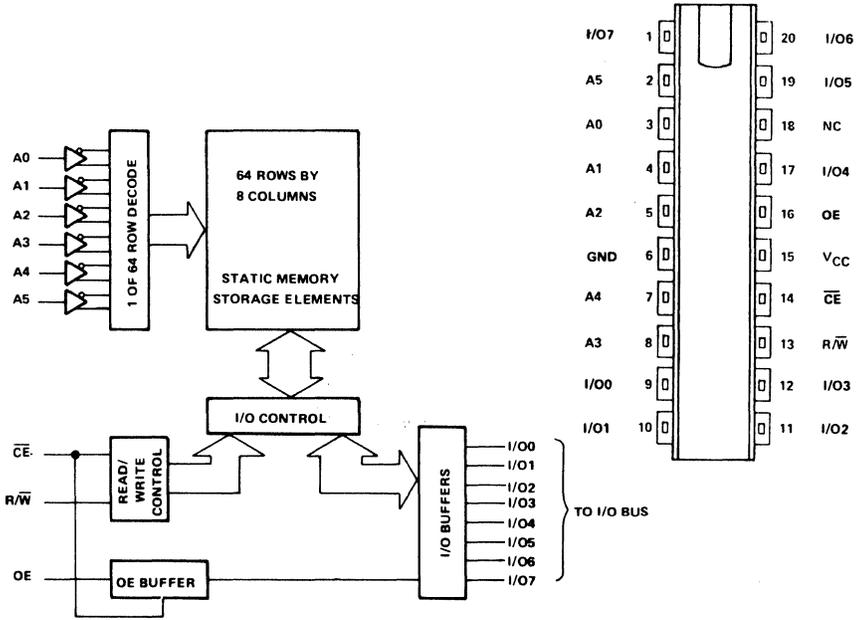
**A236**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A237**

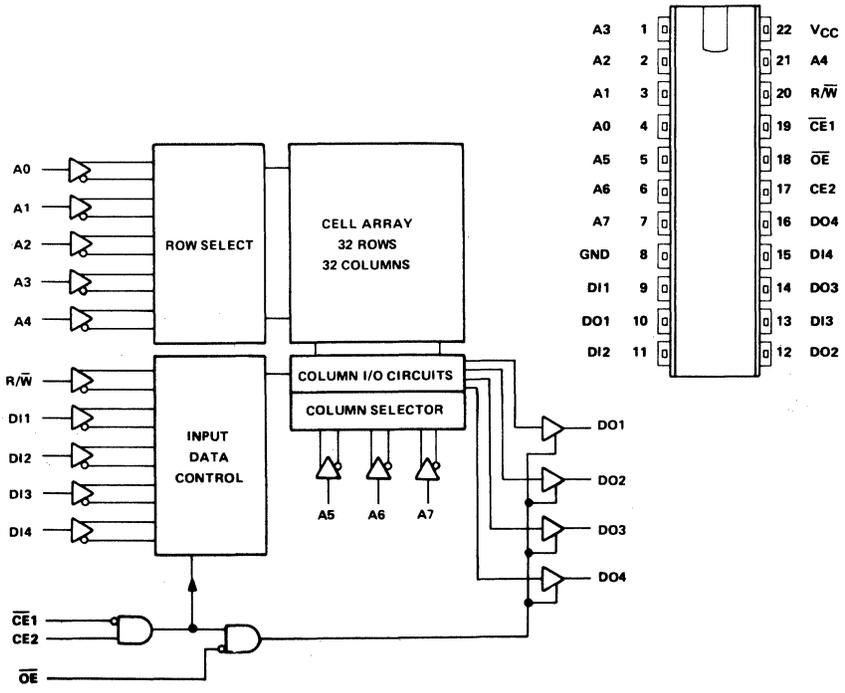


[ ]

[ ]

[ ]

**A238**

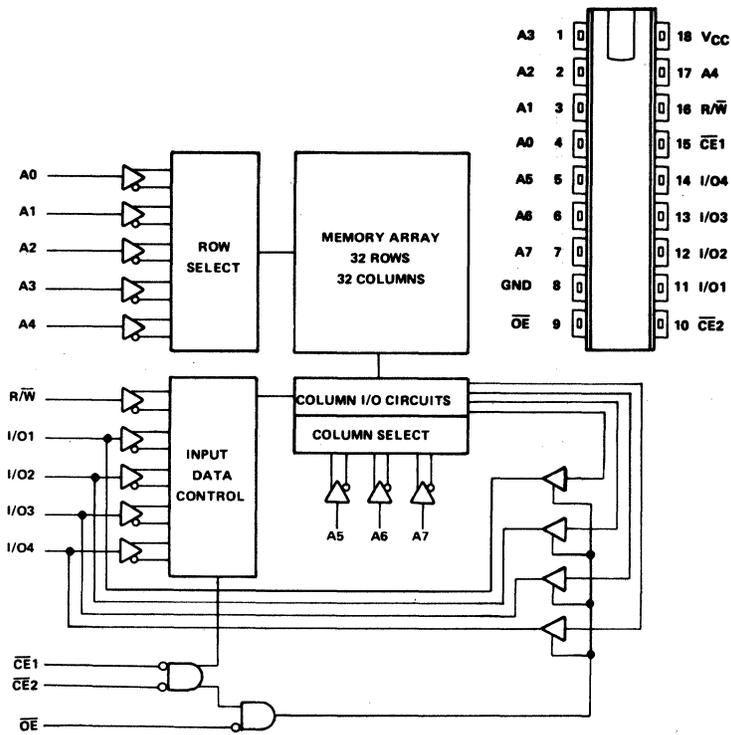


[ ]

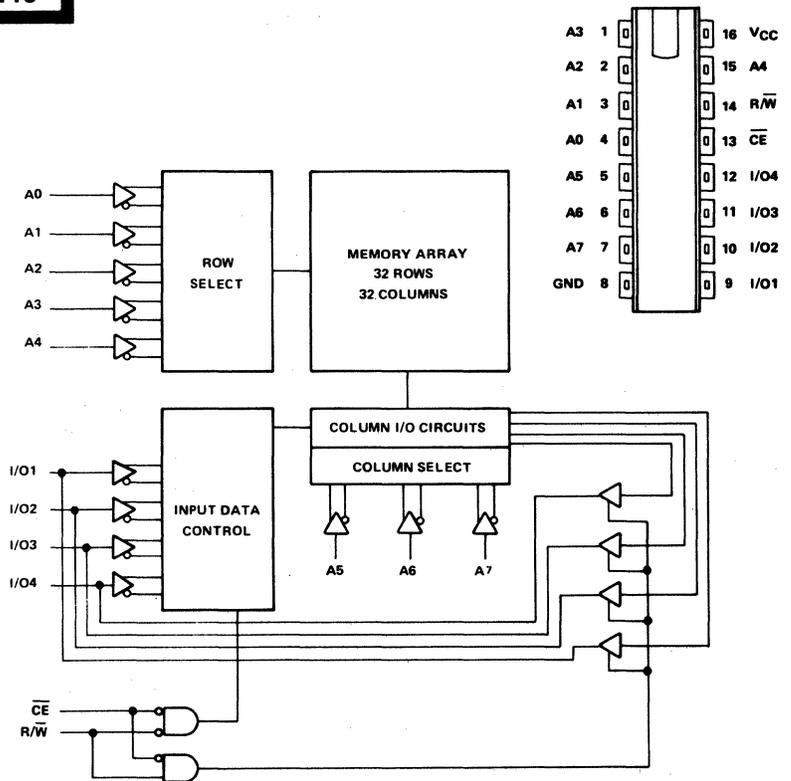
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

A239



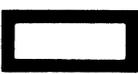
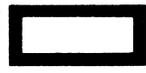
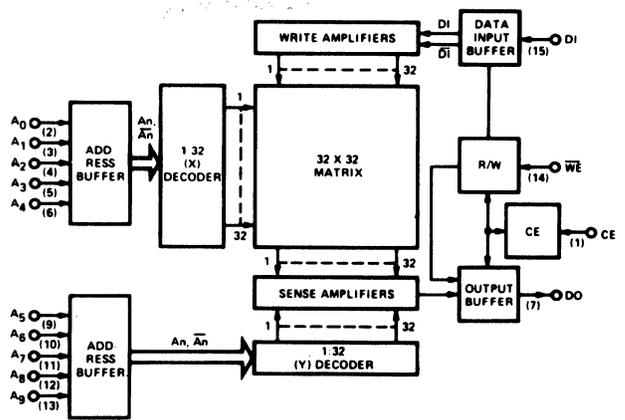
A240



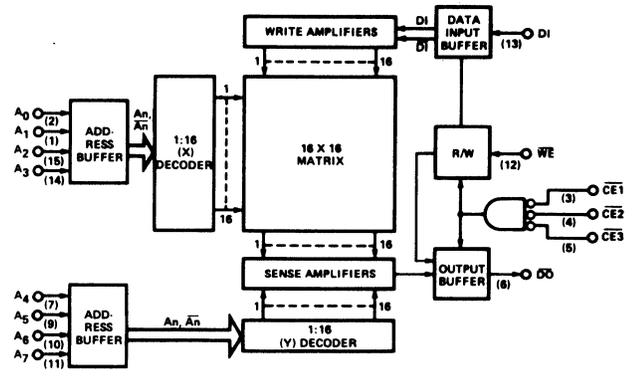
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

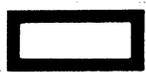
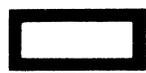
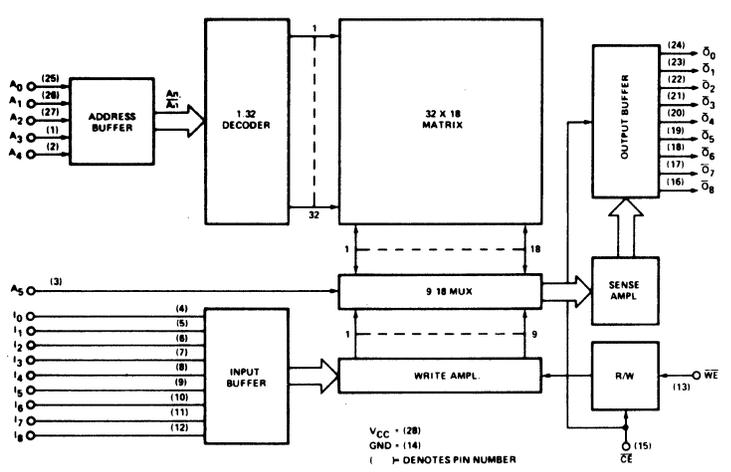
**A242**



**A243**



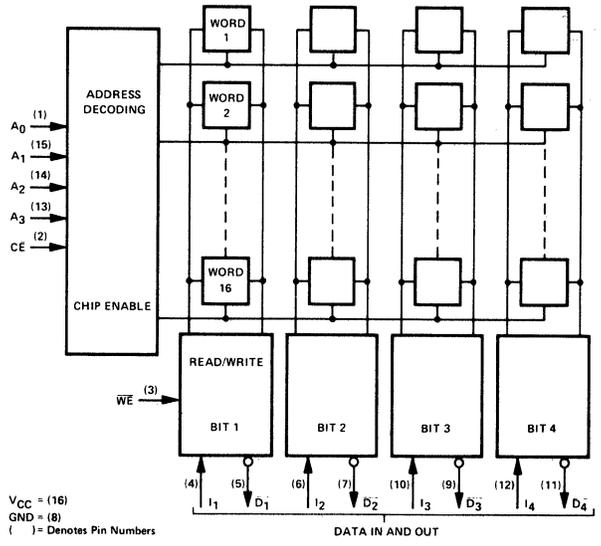
**A244**



# 22. LOGIC/BLOCK DRAWINGS

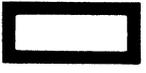
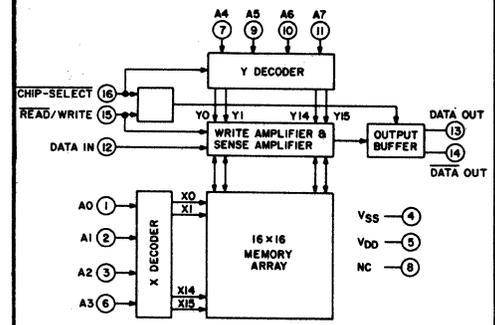
IN DRAWING NUMBER SEQUENCE

**A245**

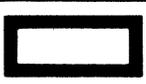
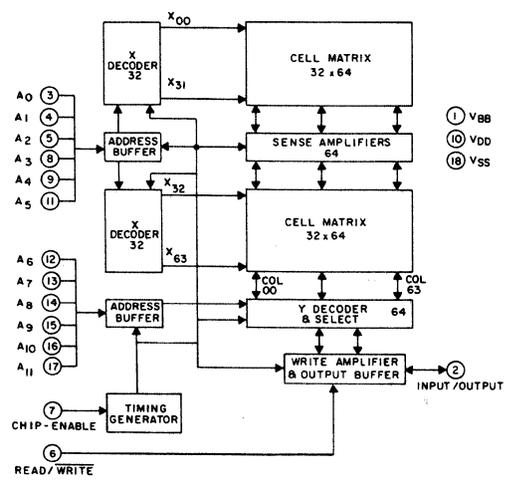


V<sub>CC</sub> = (16)  
GND = (8)  
( ) = Denotes Pin Numbers

**A247**



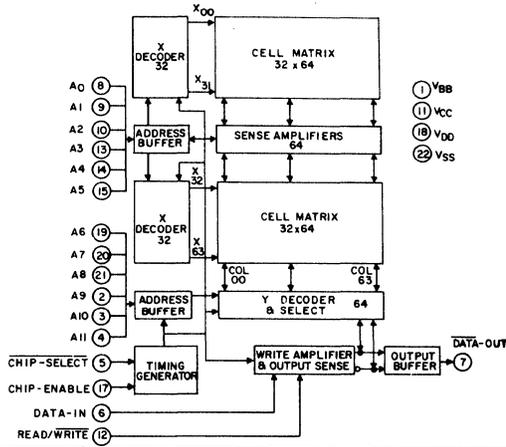
**A248**



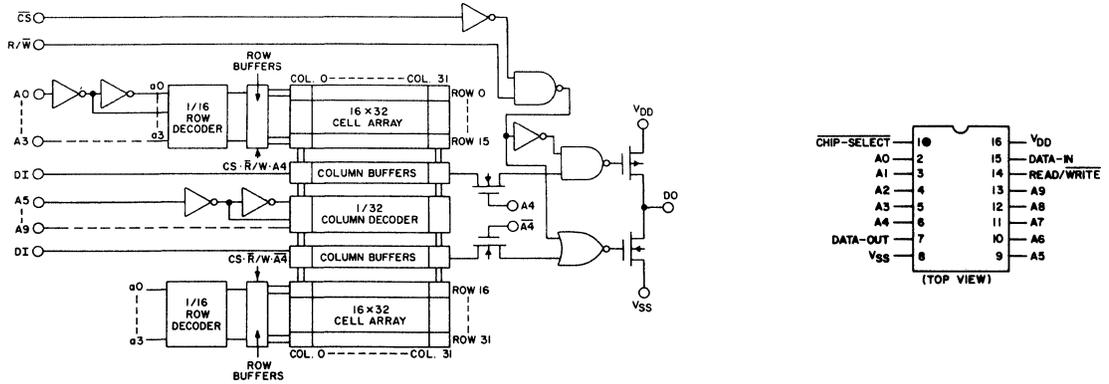
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

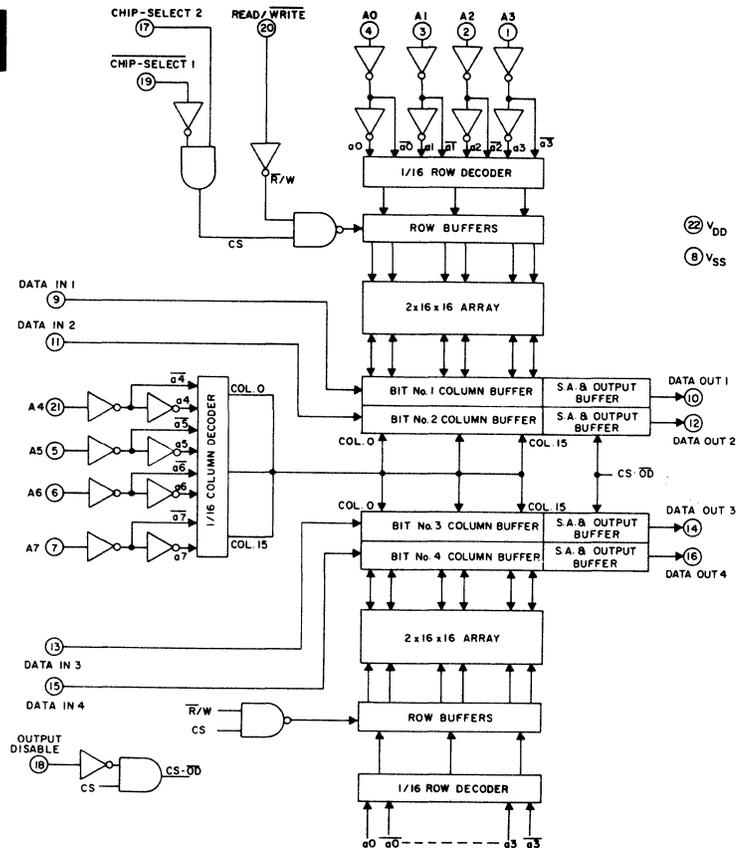
**A249**



**A251**



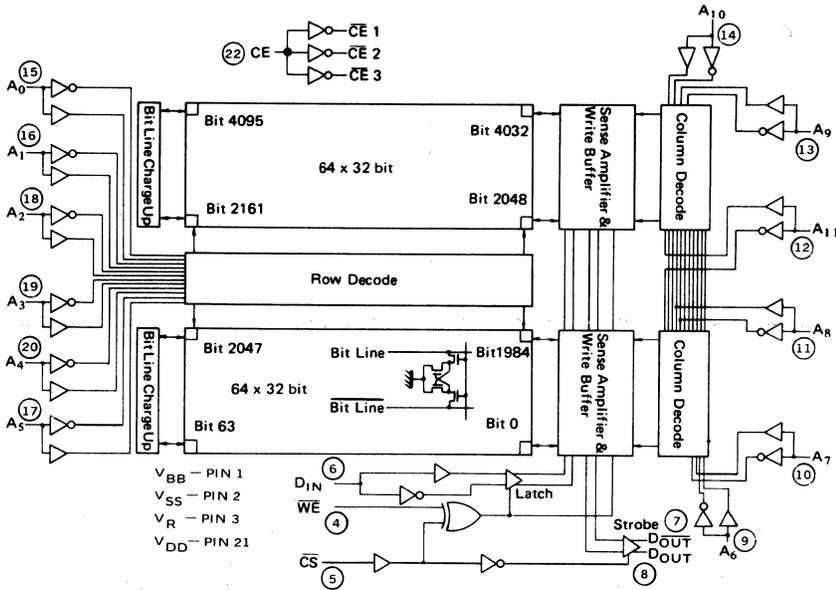
**A252**



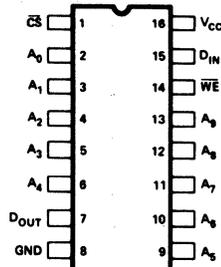
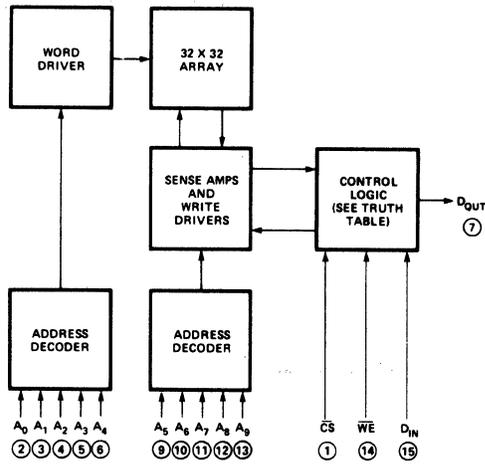
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A255**



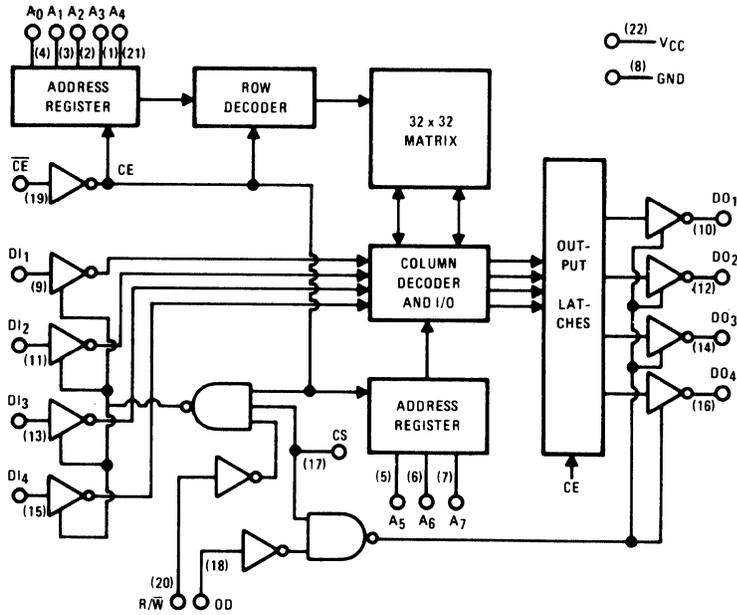
**A256**



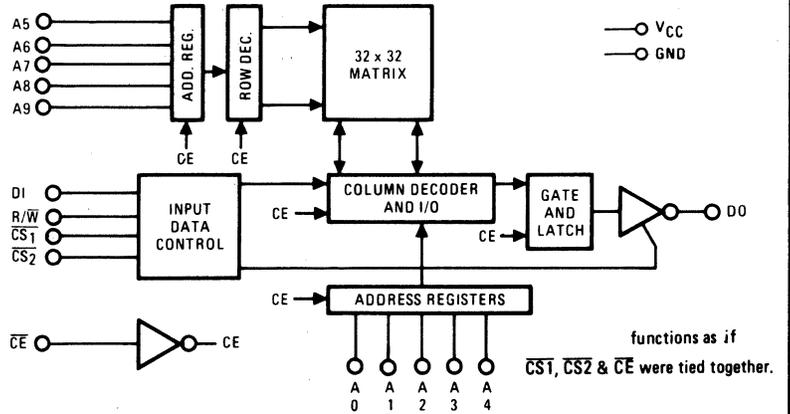
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

A257



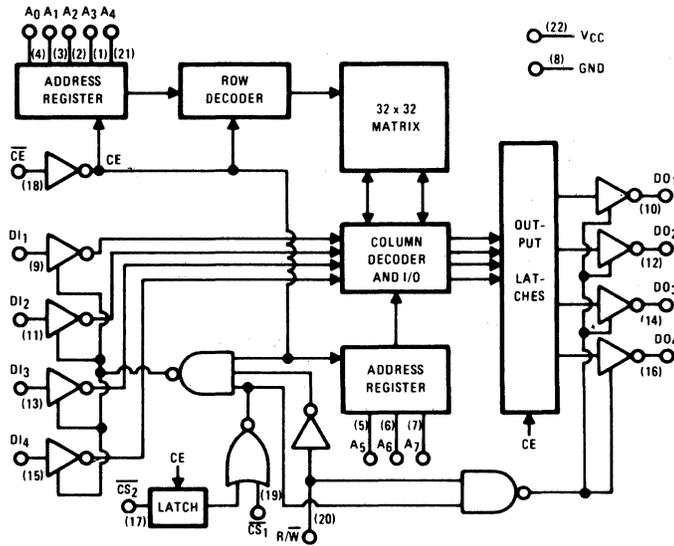
A258



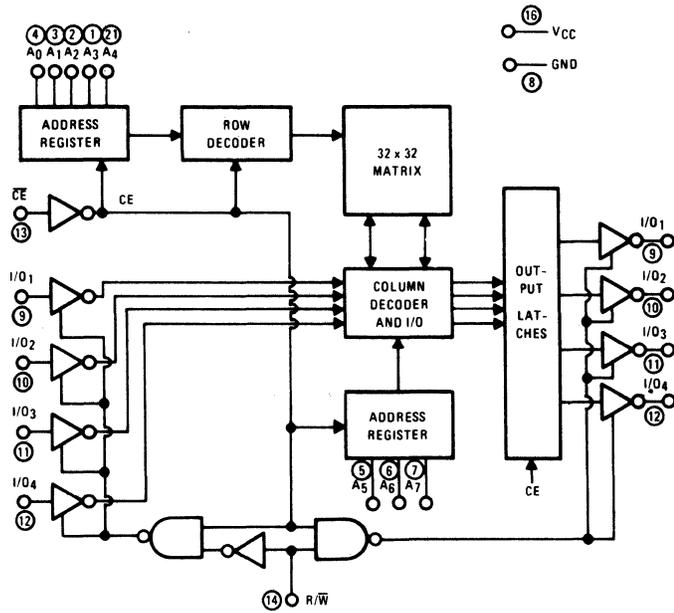
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A259**



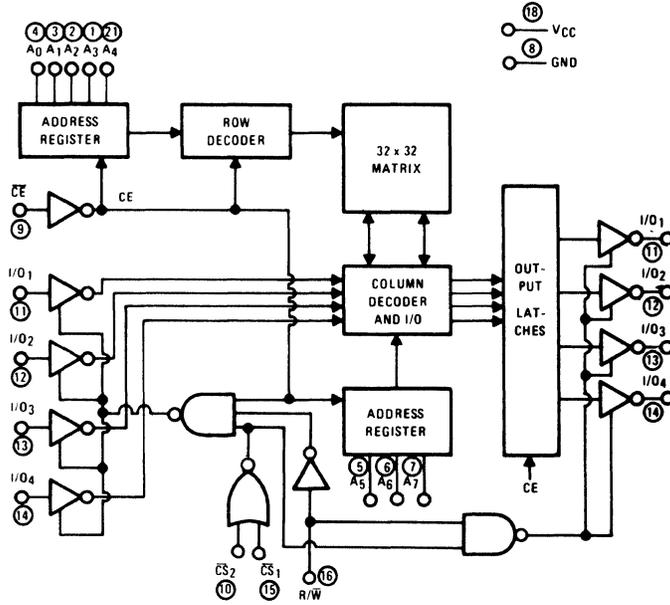
**A260**



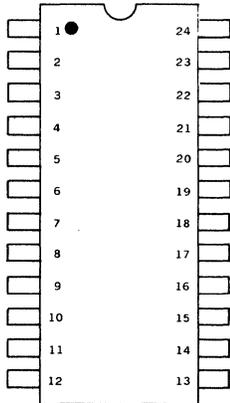
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A261**

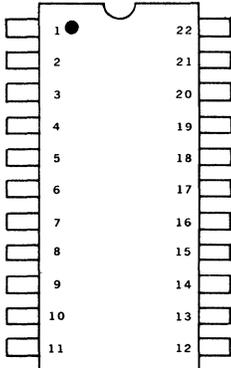


**A262**



PIN NUMBERS																								
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
A262	VSS	D0	D1	D2	D3	D4	D5	D6	D7	E0	E1	E2	E3	E4	E5	R/W	A6	A5	A4	A3	A2	A1	A0	VCC

**A263**

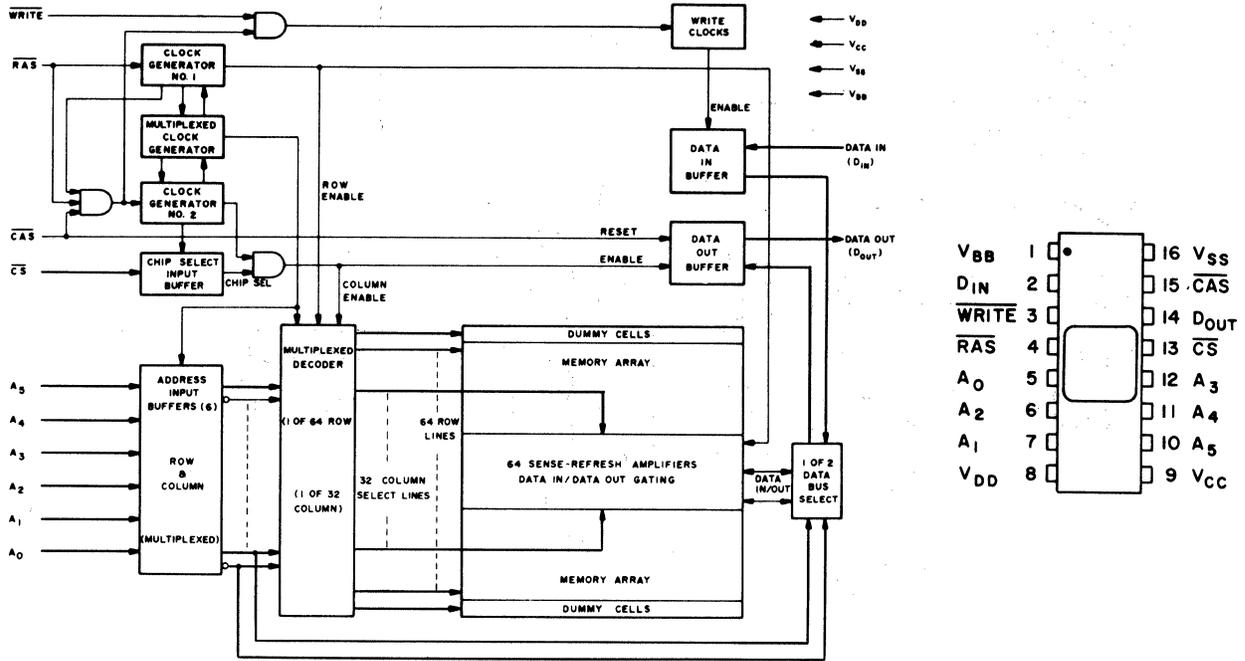


PIN NUMBERS																							
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	
A263	A3	A2	A1	A0	A5	A6	A7	VSS	DIN1	DOUT1	DIN2	DOUT2	DIN3	DOUT3	DIN4	DOUT4	CS	OUT	DISABLE	CS	R/W	A4	VDD
A263a	A3	A2	A1	A0	A5	A6	A7	VSS	D10	D00	D11	D01	D12	D02	D13	D03	CS2	TRD	CS1	MMR	A4	VDD	
A263b	A3	A2	A1	A0	A5	A6	A7	GND	D11	D01	D12	D02	D13	D03	D14	D04	CE2	OD	CE1	R/W	A4	VCC	
A263c	VSS	A0	VSS	NC	QF	DIN	WE	A9	A8	A7	A8	A5	A10	CS	VREF	DOUT	DOUT	VDD	A4	A3	A2	A1	
A263d	VBB	A9	A10	A11	CS	DIN	DOUT	A0	A1	A2	VCC	WE	A3	A4	A5	A12	CE	VDD	A6	A7	A8	VSS	
A263e	A3	A2	A1	A0	A5	A6	A7	VSS	D11	D01	D12	D02	D13	D03	D14	D04	CE2	OD	CE1	WE	A4	VCC	
A263f	VBB	A3	Preset	DIN	A11	CE	DOUT	CS	A4	A2	VCC	VSS	A0	R/W	A5	A6	A7	A8	A9	A10	A1	VDD	

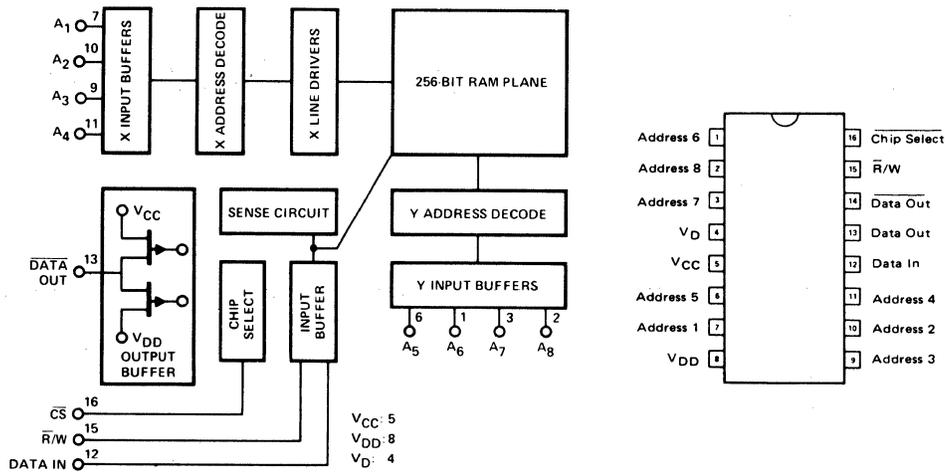
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A264**



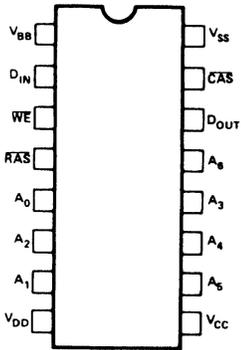
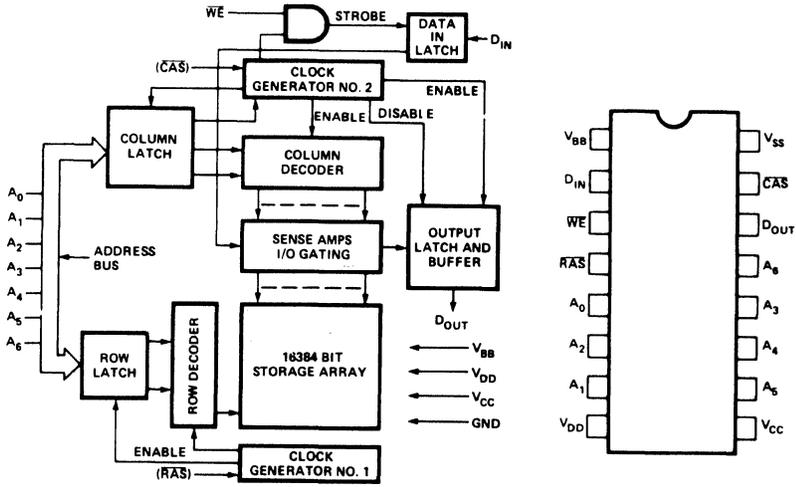
**A265**



# 22. LOGIC/BLOCK DRAWINGS

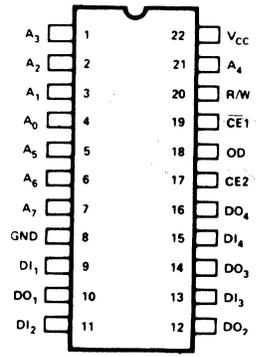
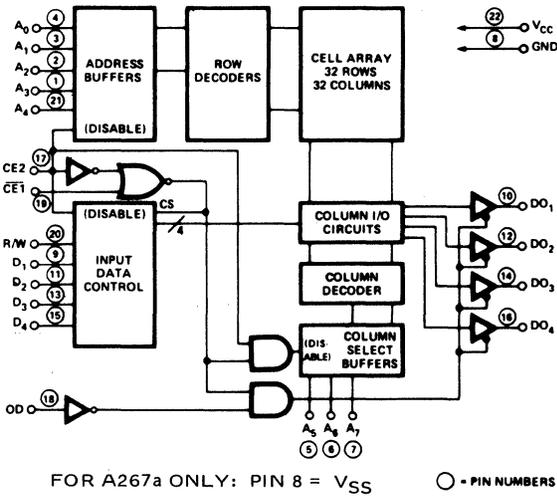
IN DRAWING NUMBER  
SEQUENCE

**A266**



[Empty box]

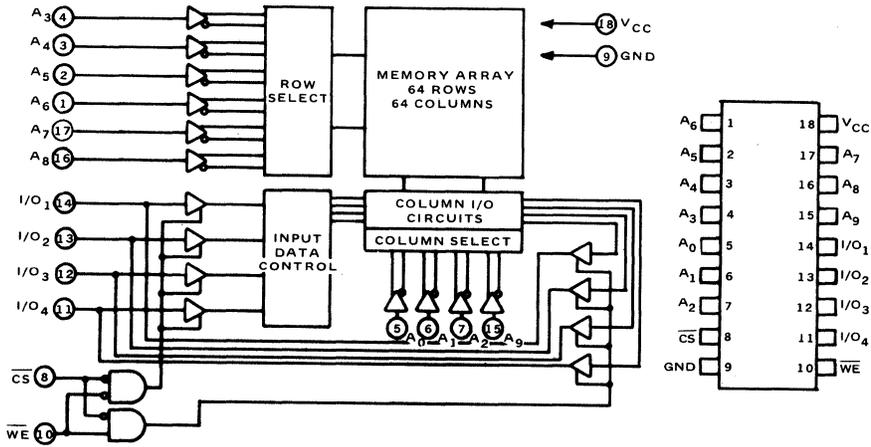
**A267**



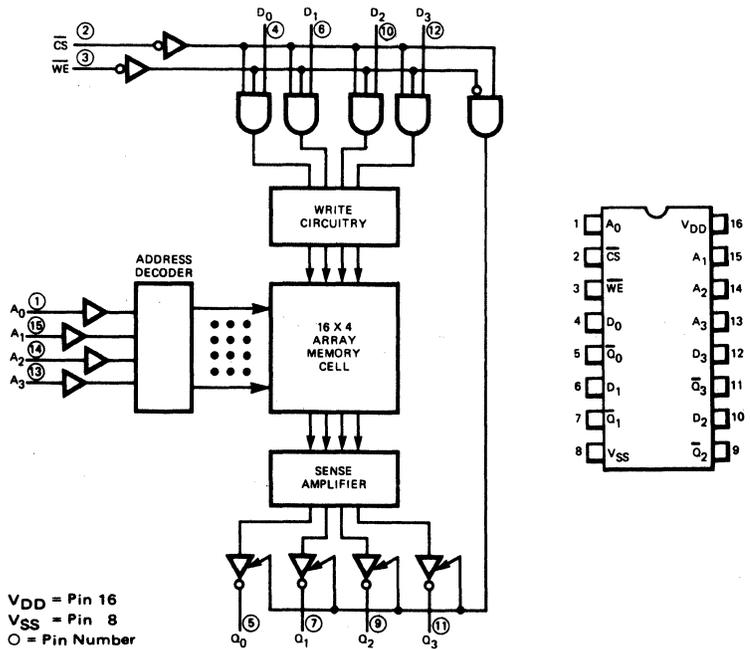
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A268**



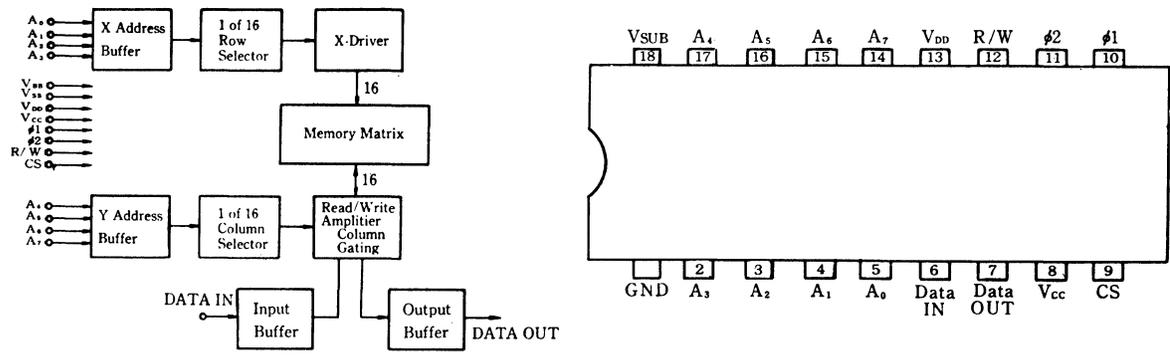
**A269**



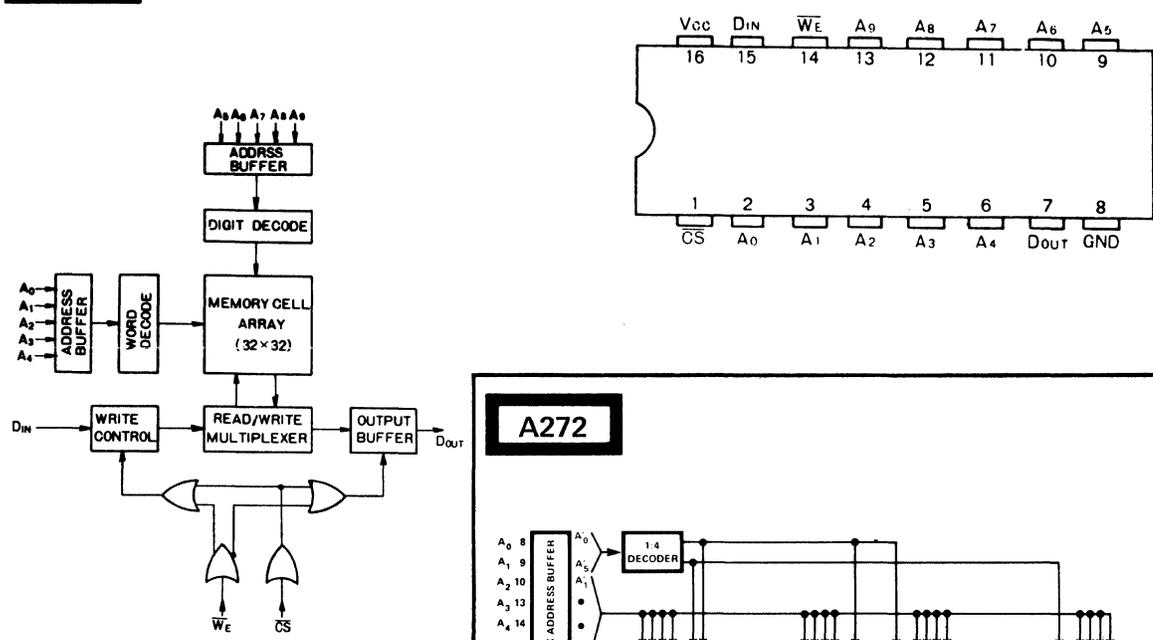
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

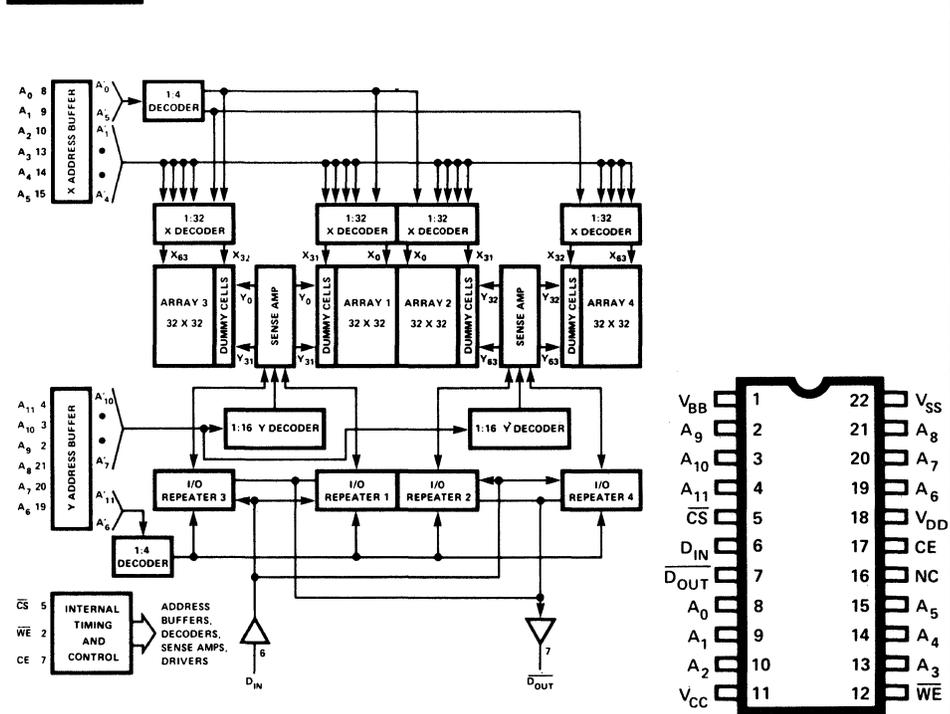
**A270**



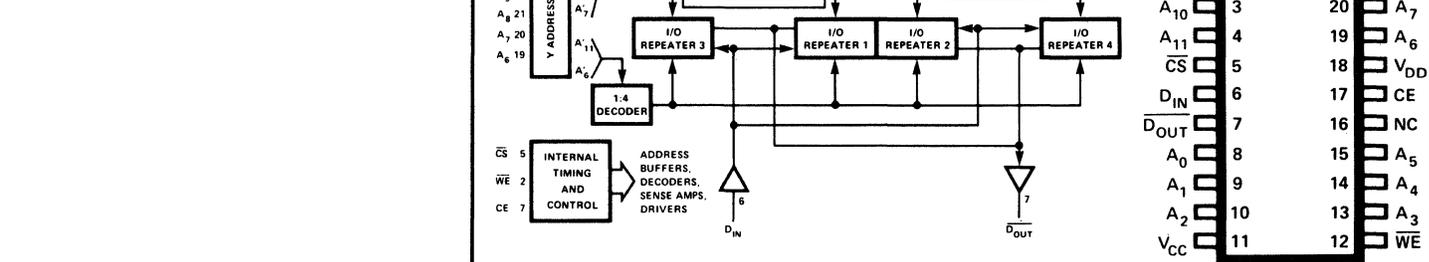
**A271**



**A272**



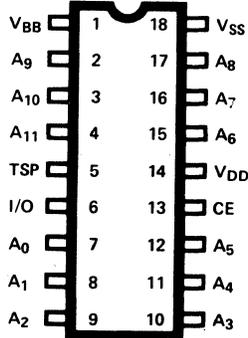
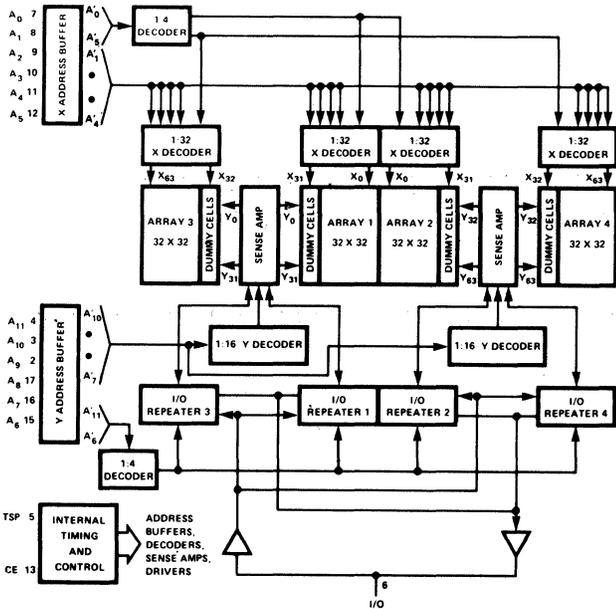
**A273**



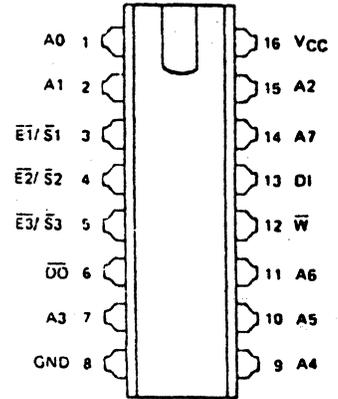
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A273**

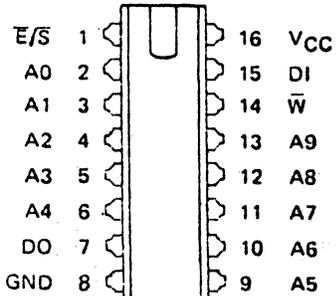


**A275**



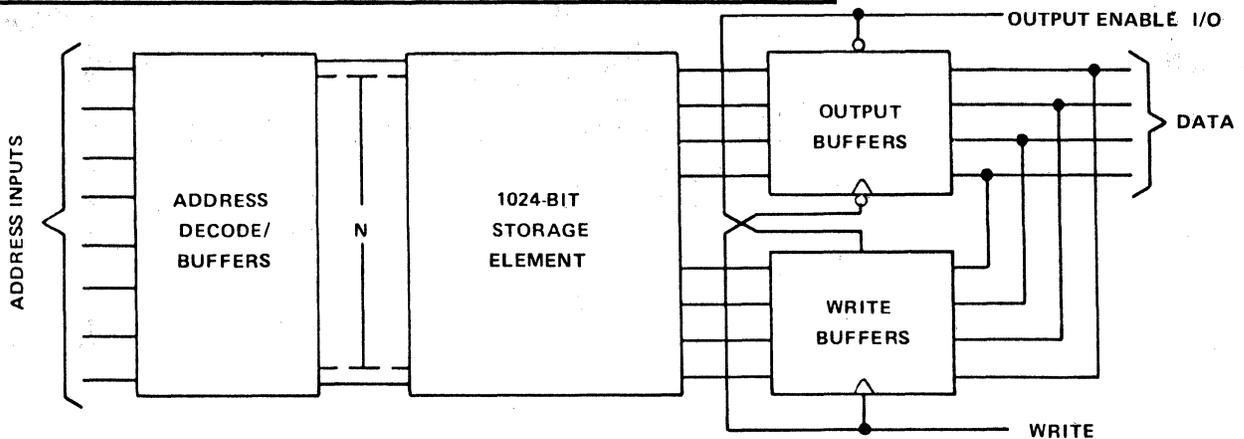
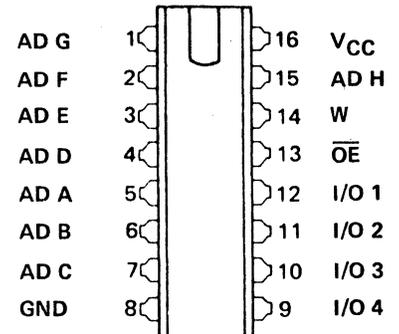
Pin assignments are same for all packages  
 $\bar{E}1, \bar{E}2, \bar{E}3$  = Chip-Enable for A275a  
 $\bar{S}1, \bar{S}2, \bar{S}3$  = Chip-Select for A275

**A276**

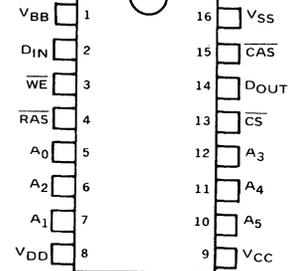
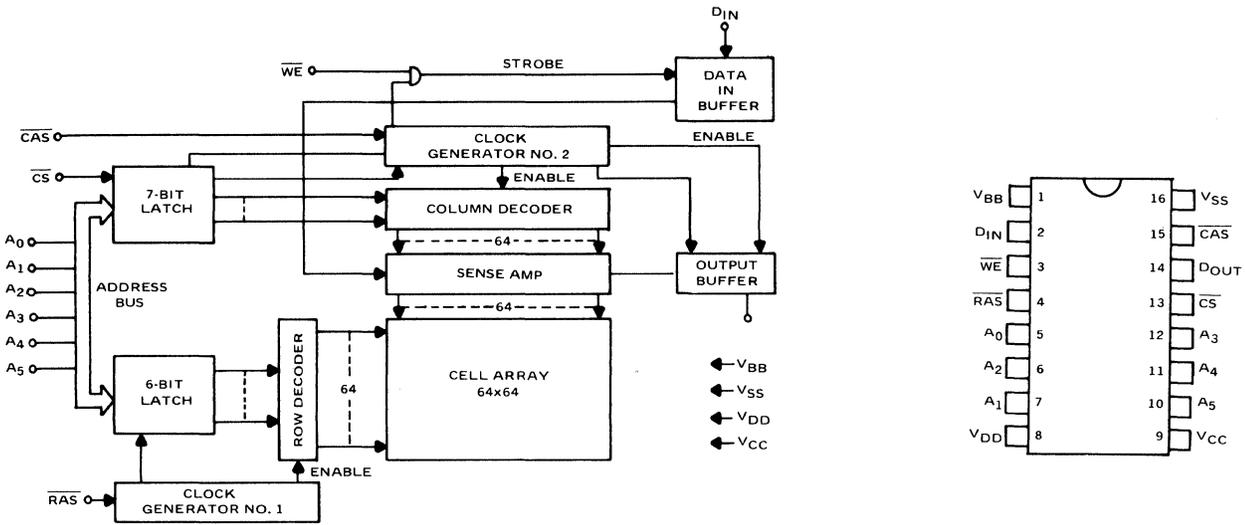


Pin assignments are same for all packages  
 $\bar{E}$  = Chip-Enable for A276a  
 $\bar{S}$  = Chip-Select for A276

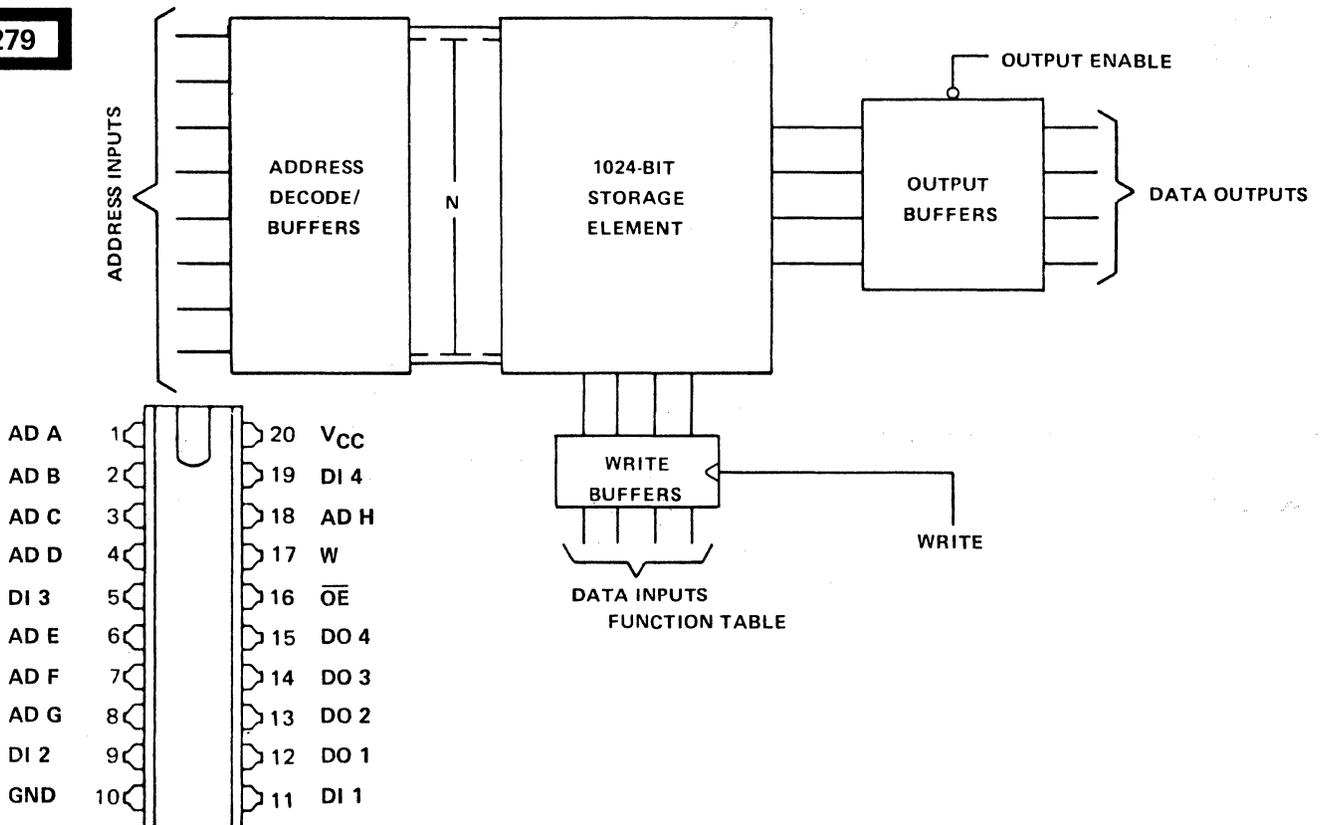
**A277**



**A278**



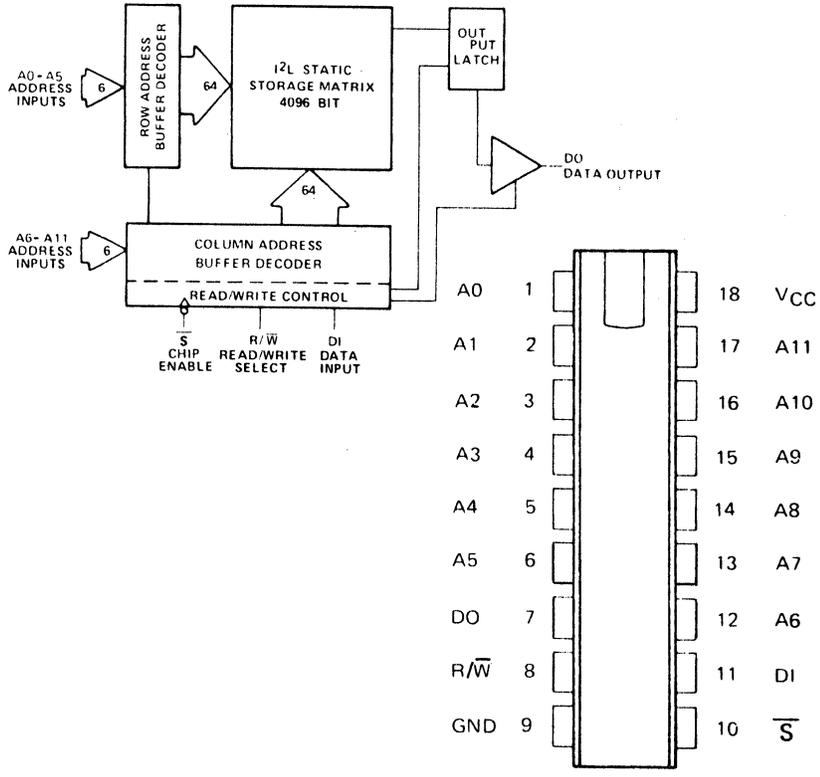
**A279**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A280**

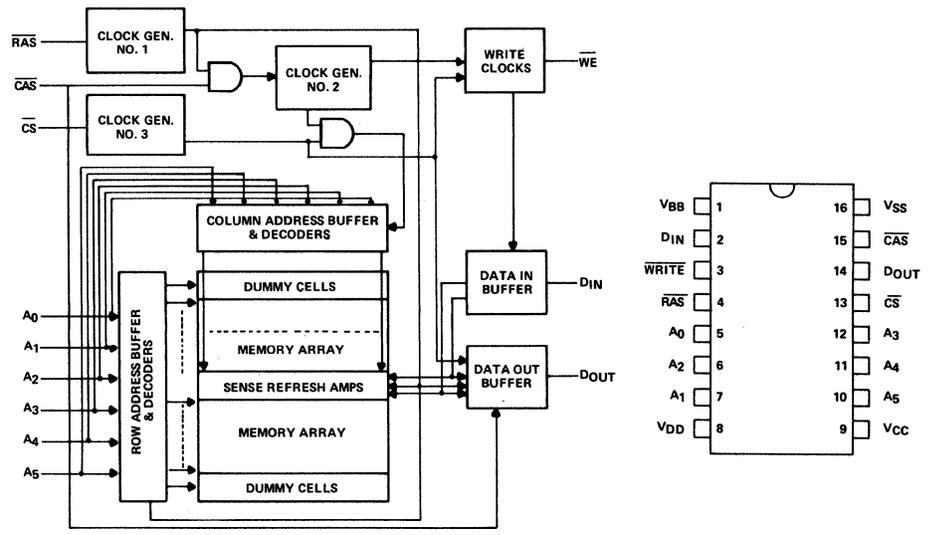


[Empty box]

[Empty box]

[Empty box]

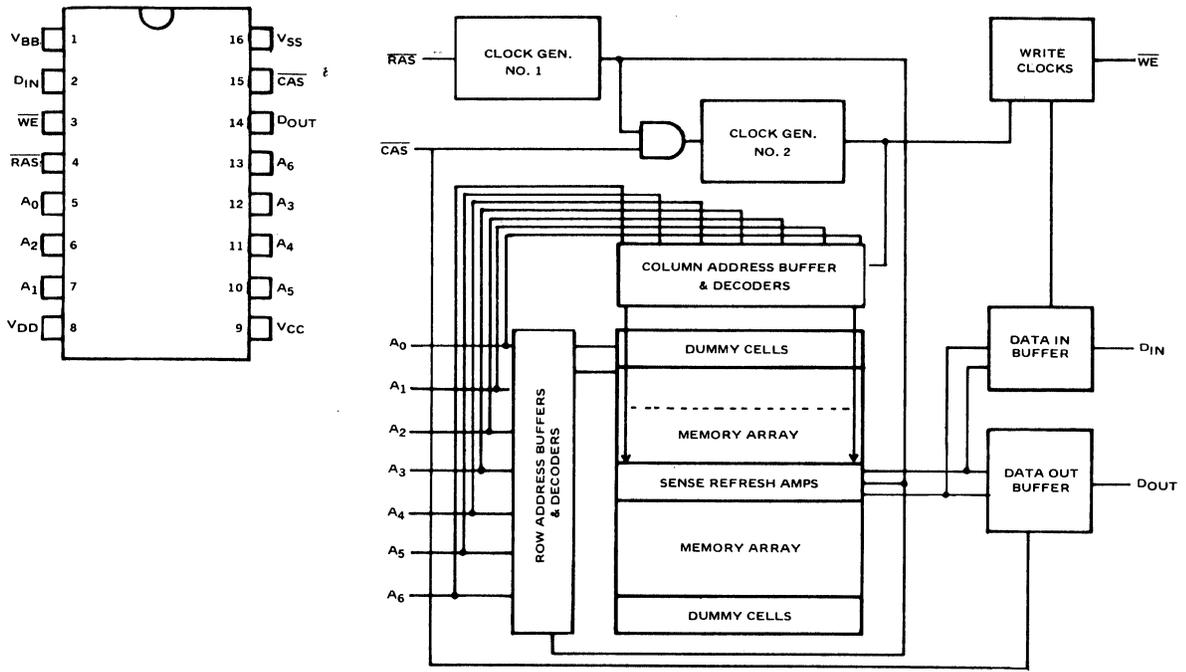
**A281**



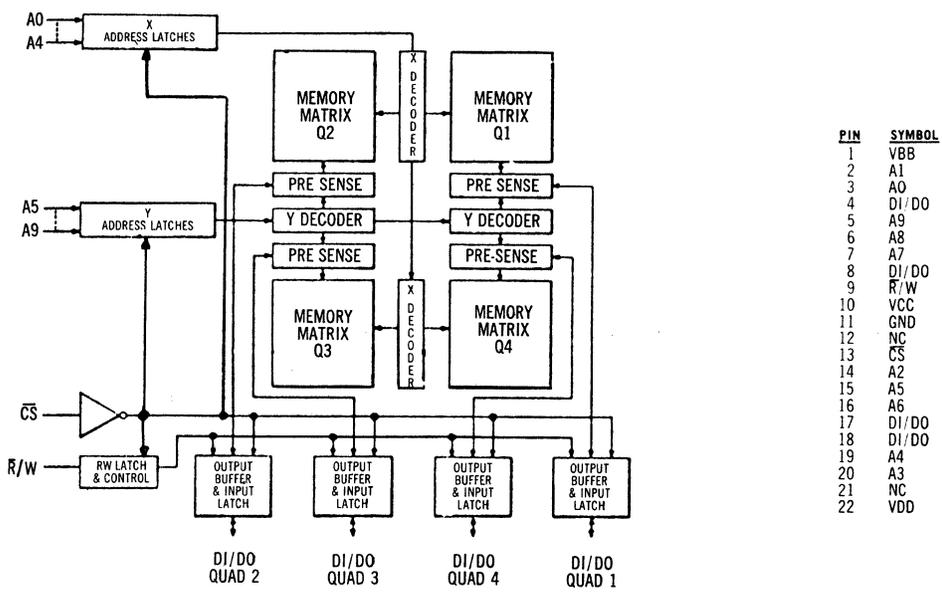
[Empty box]

# 22. LOGIC/BLOCK DRAWINGS IN DRAWING NUMBER SEQUENCE

**A282**



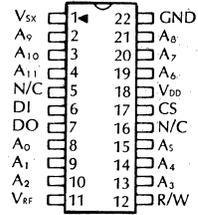
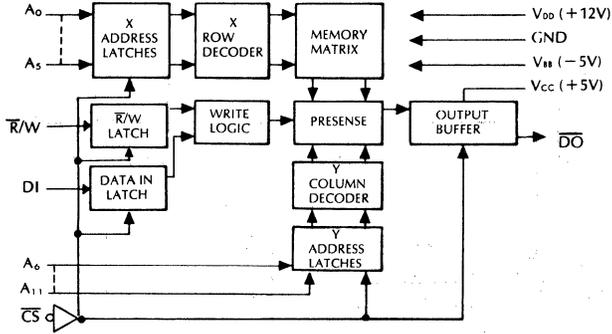
**A283**



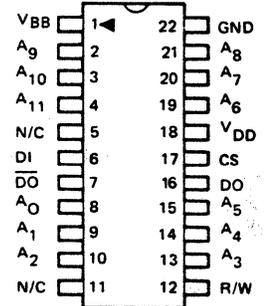
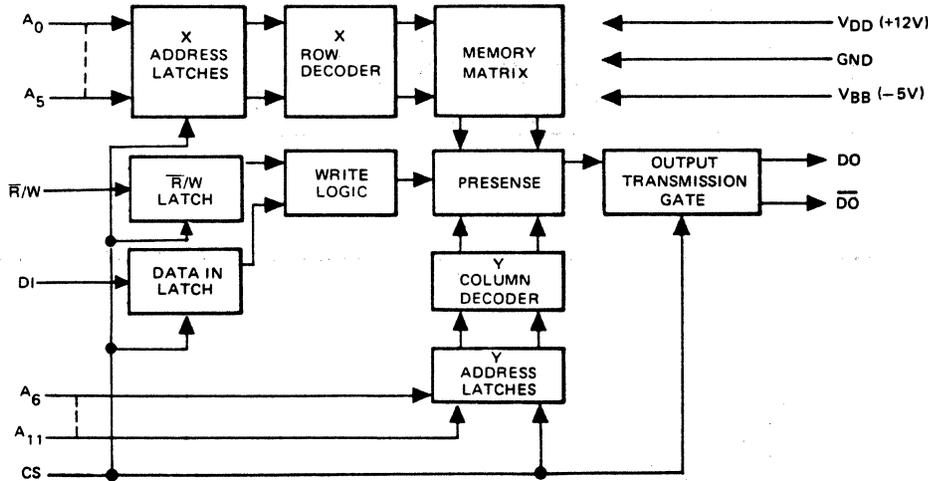
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A284**



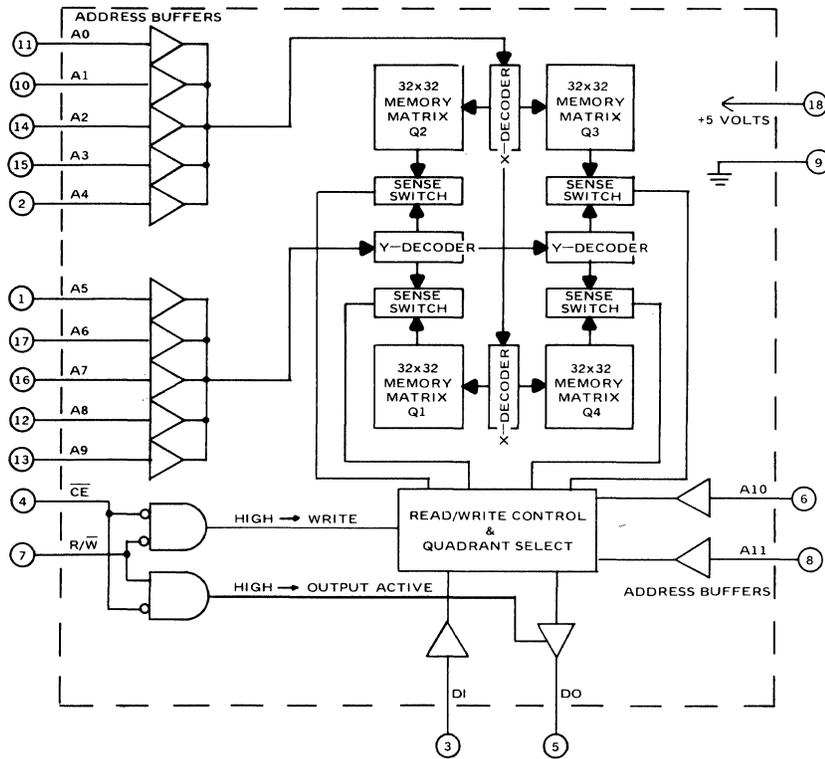
**A285**



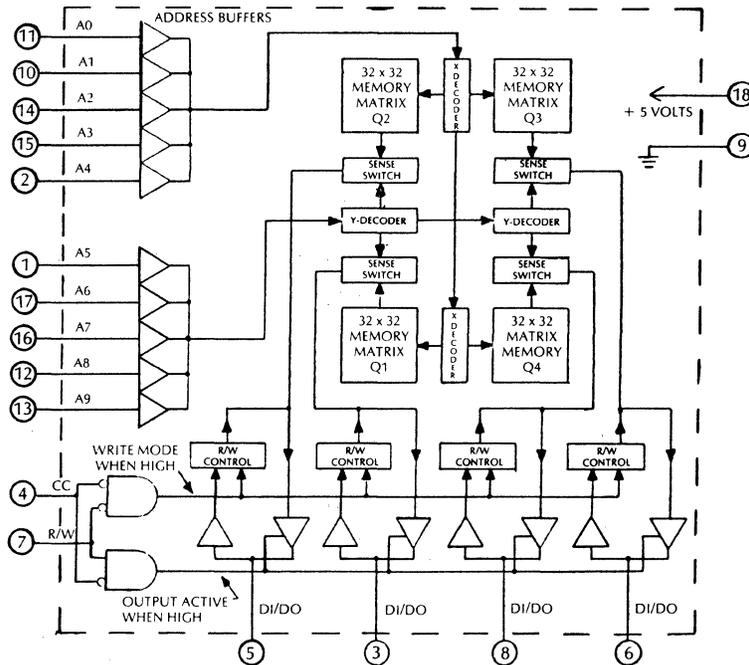
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A286**



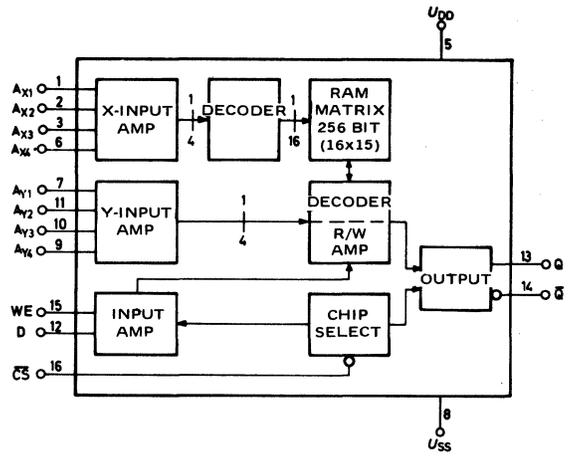
**A287**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

A288

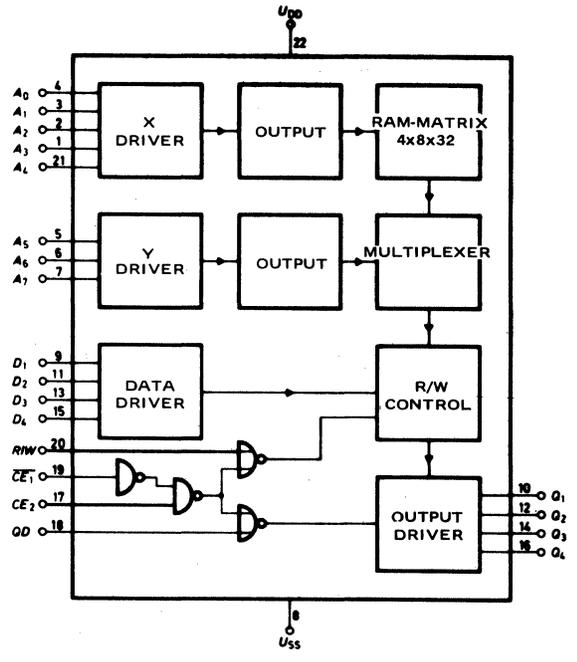


[ ]

[ ]

[ ]

A289

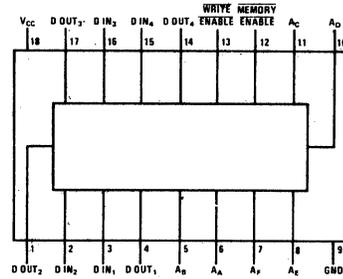
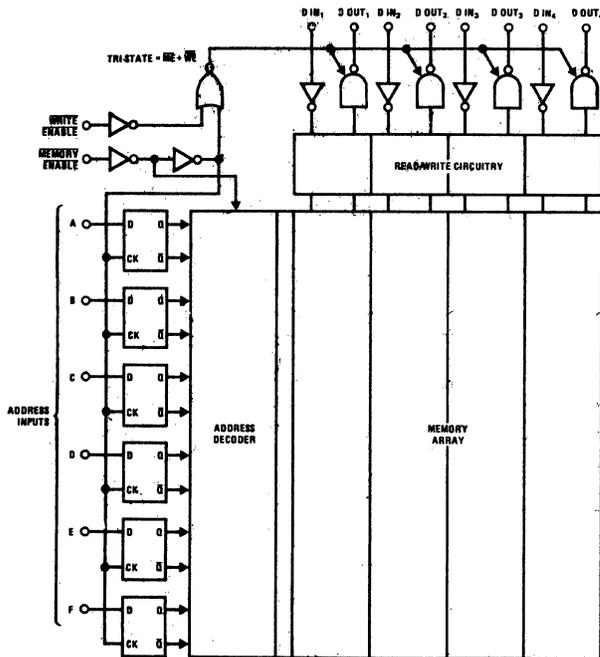


[ ]

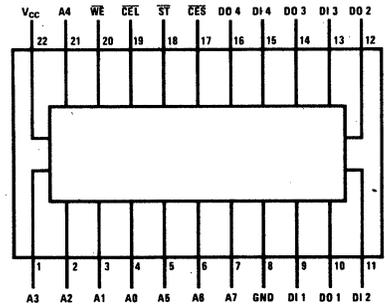
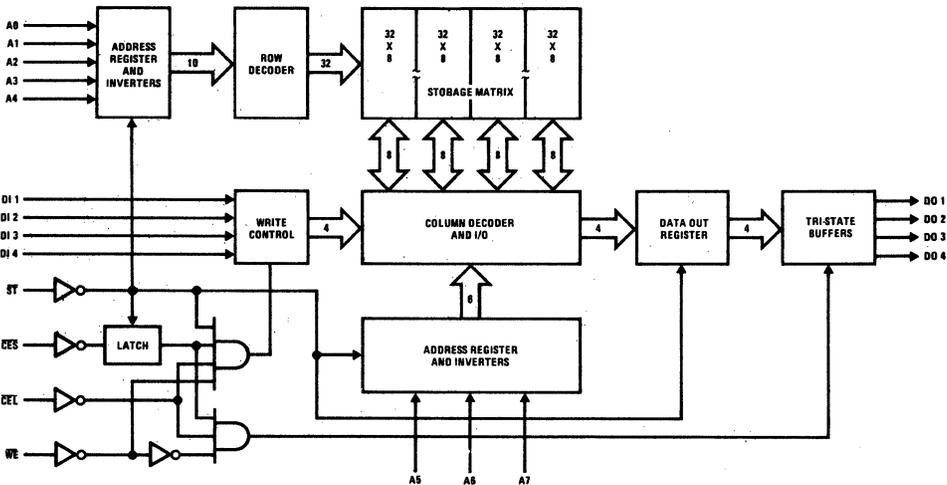
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A290**



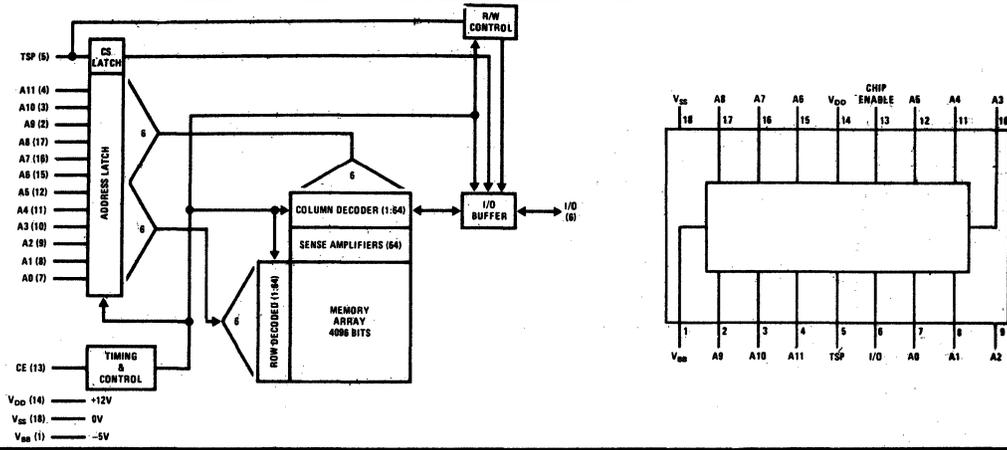
**A291**



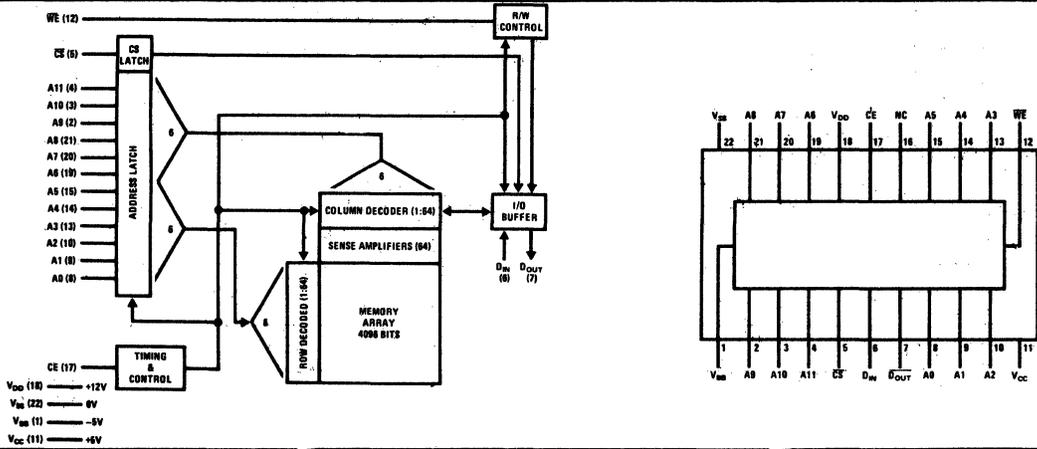
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

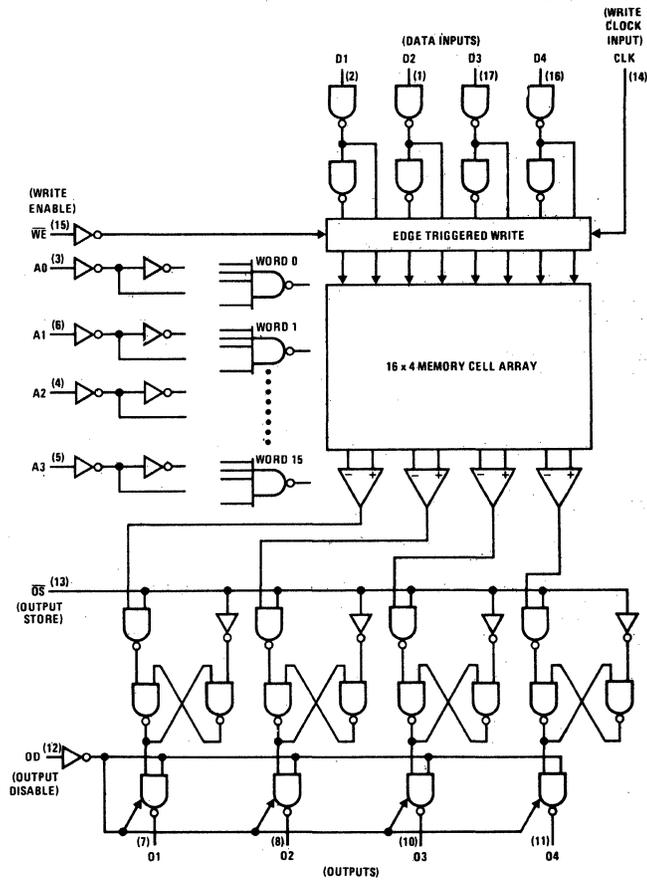
**A292**



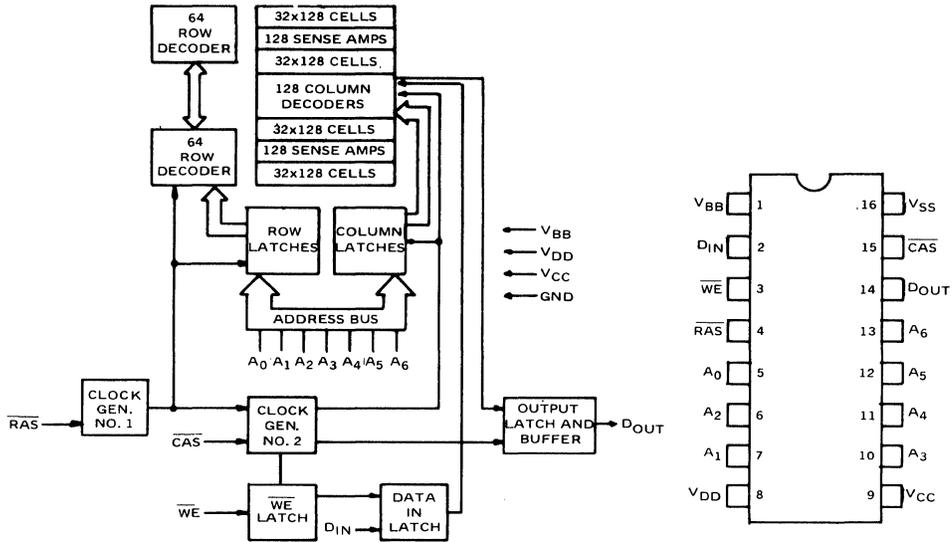
**A293**



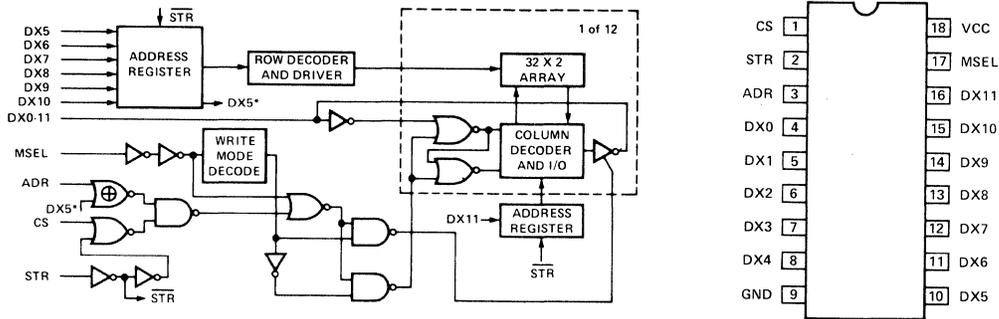
**A294**



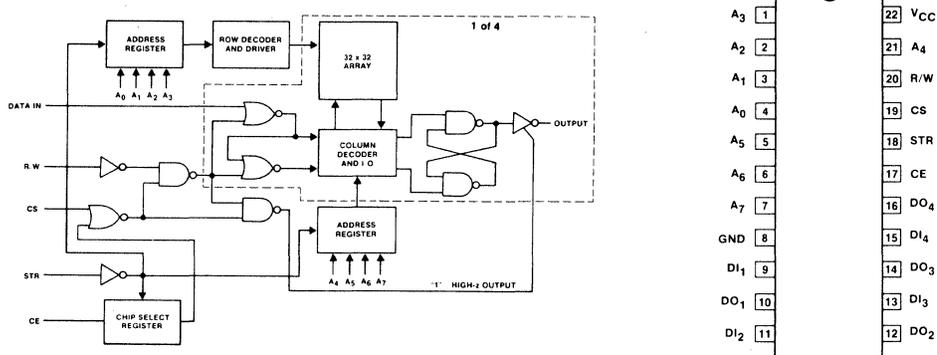
## A295



## A296



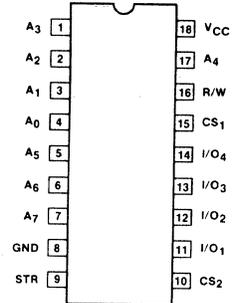
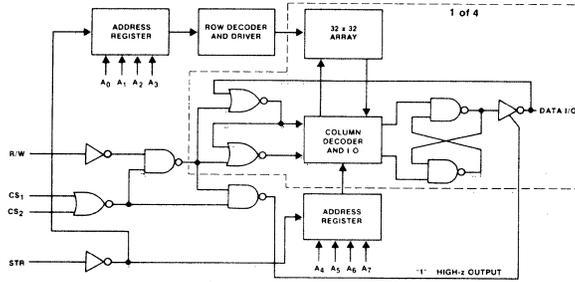
## A297



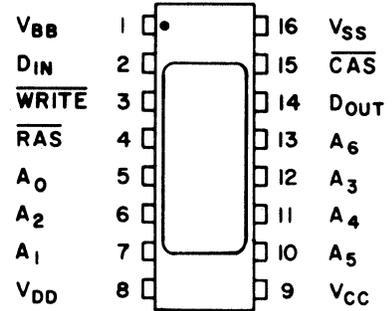
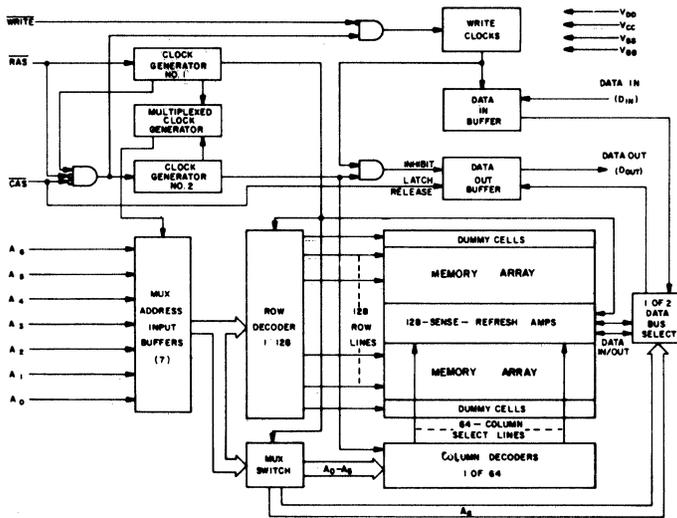
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A298**



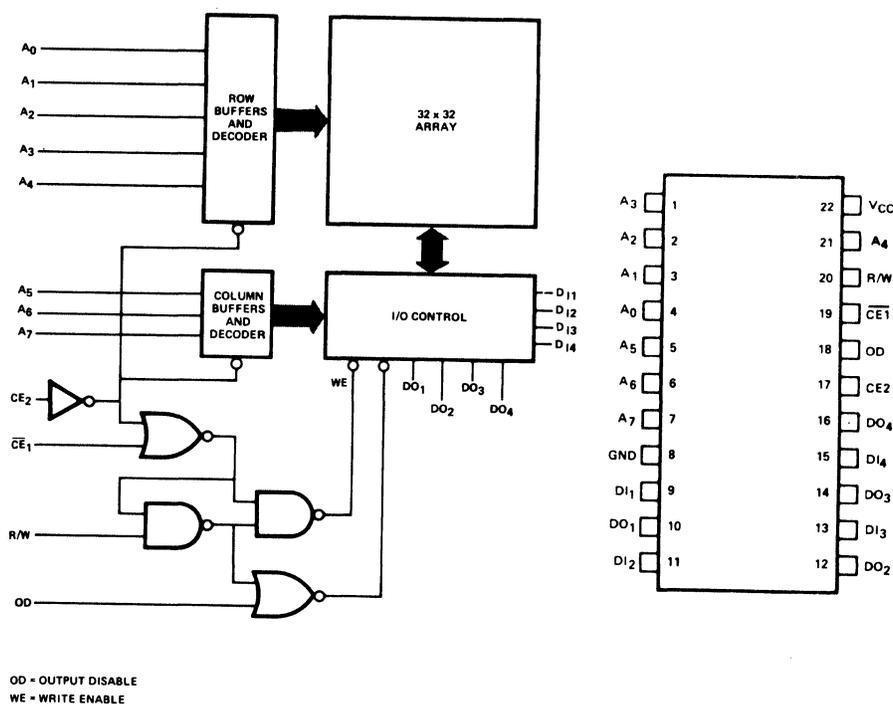
**A299**



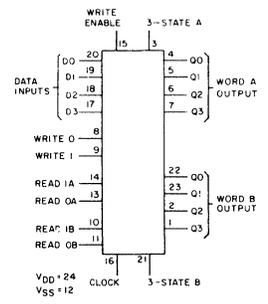
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

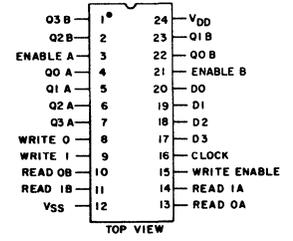
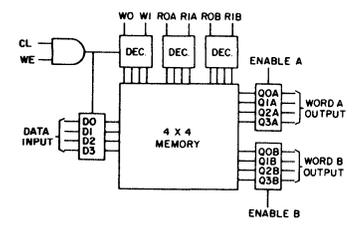
**A300**



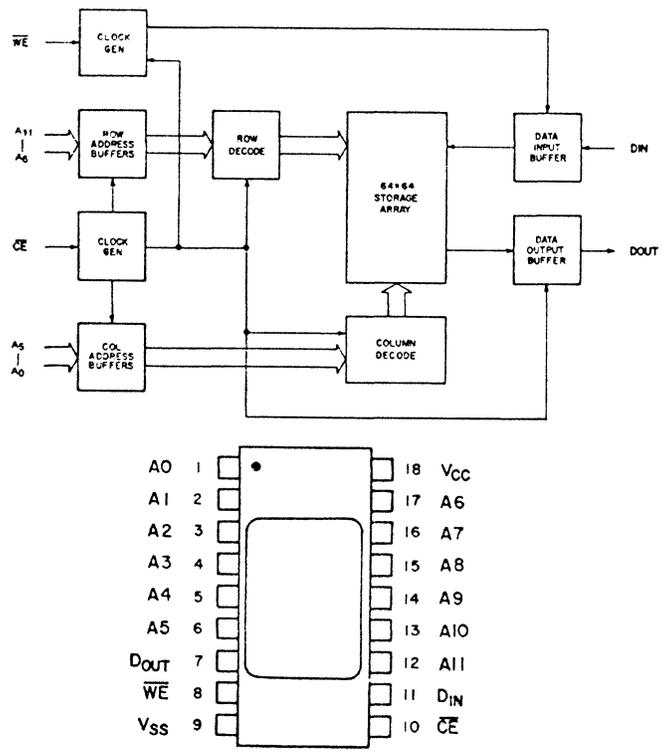
**A301**



**A302**



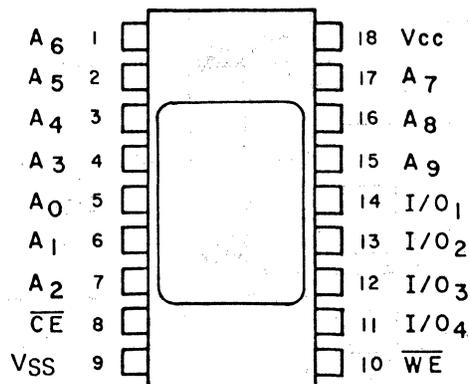
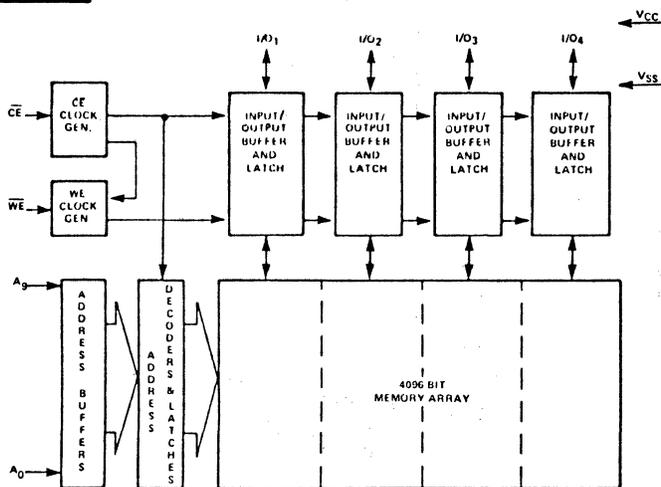
**A303**



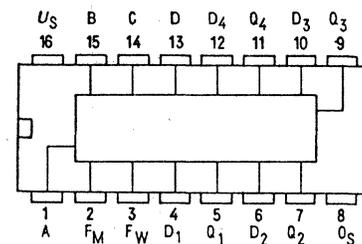
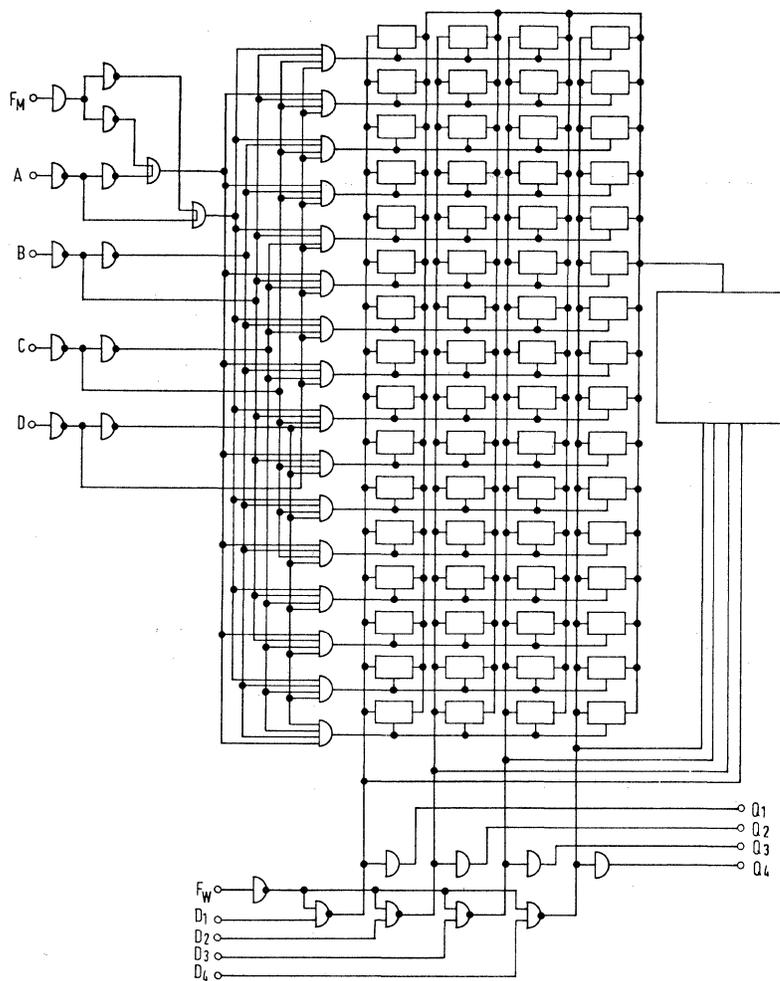
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A304**



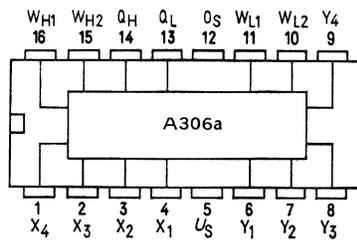
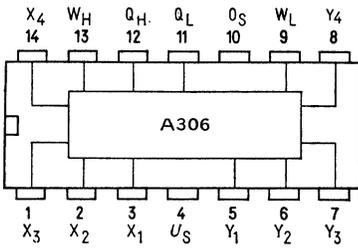
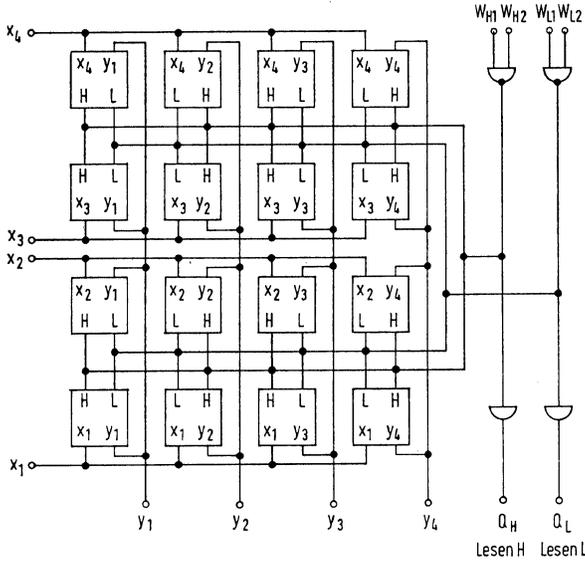
**A305**



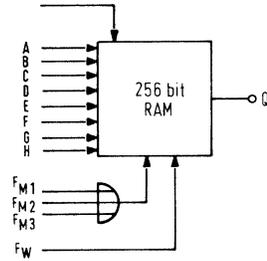
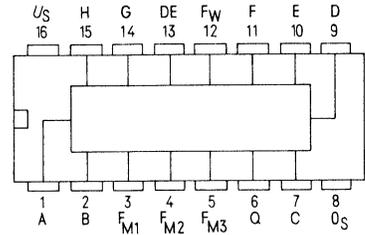
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

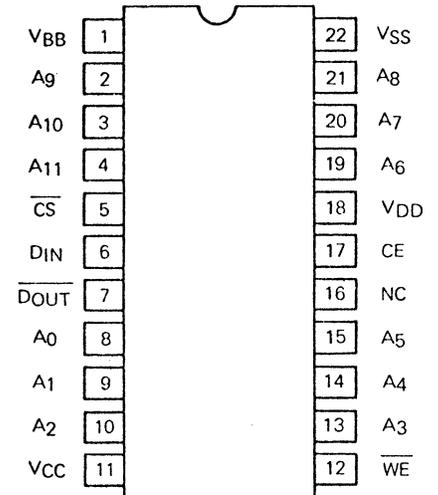
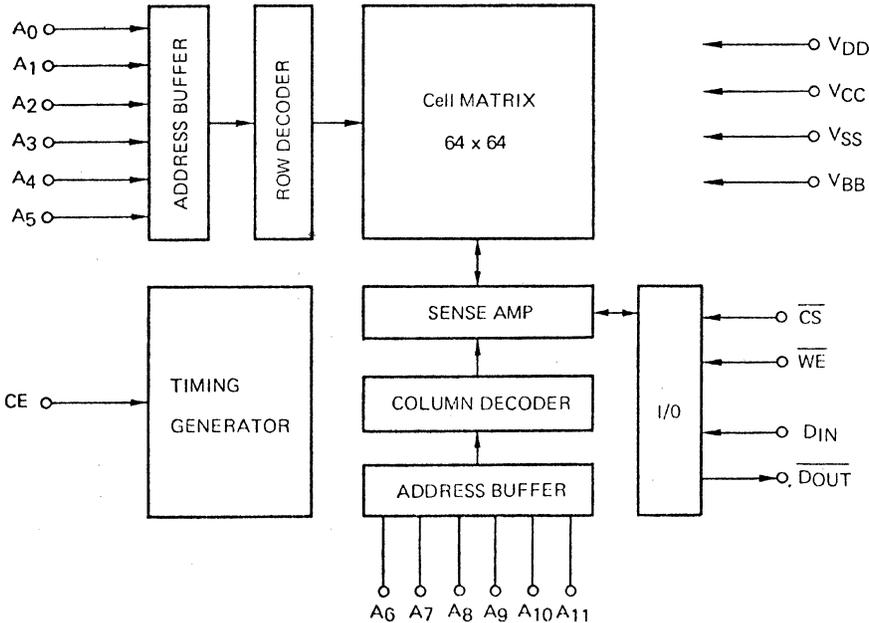
**A306**



**A307**



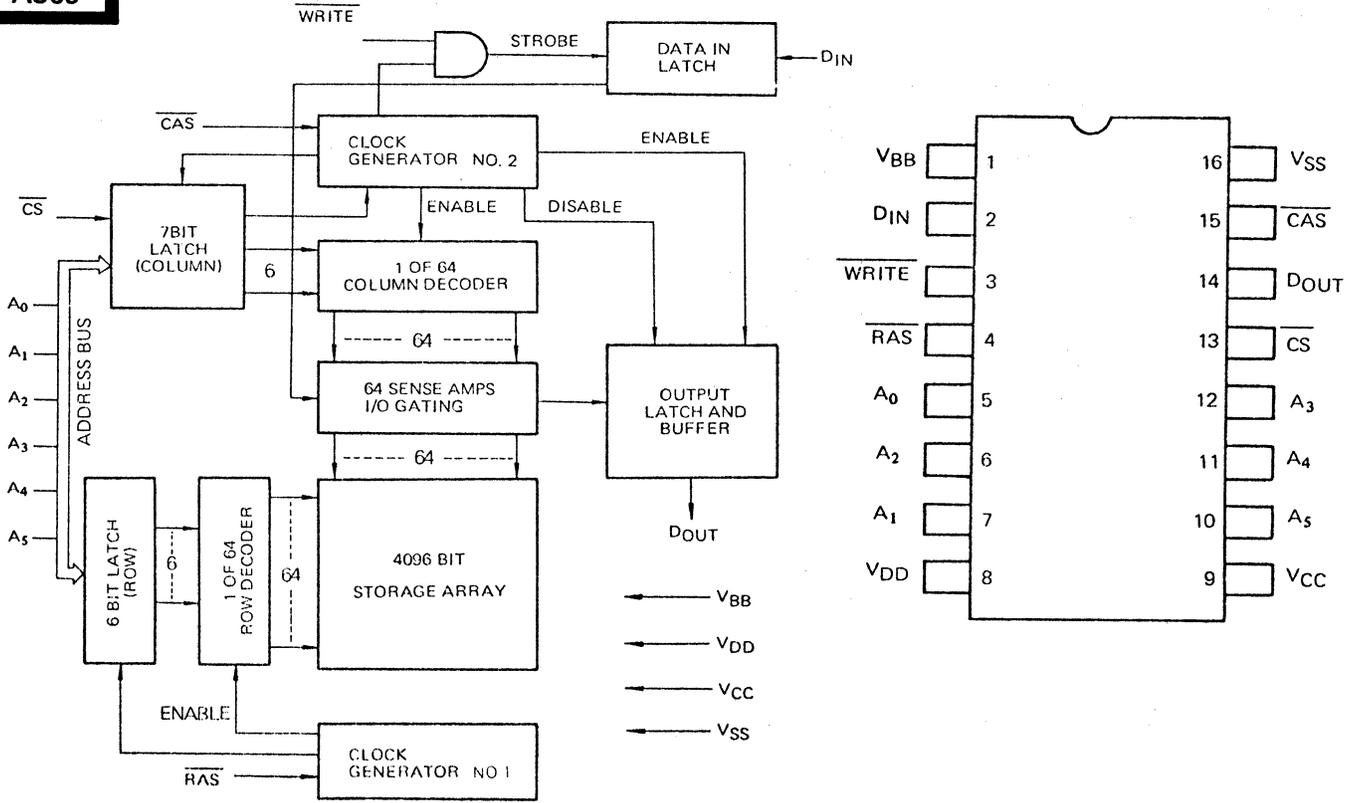
**A308**



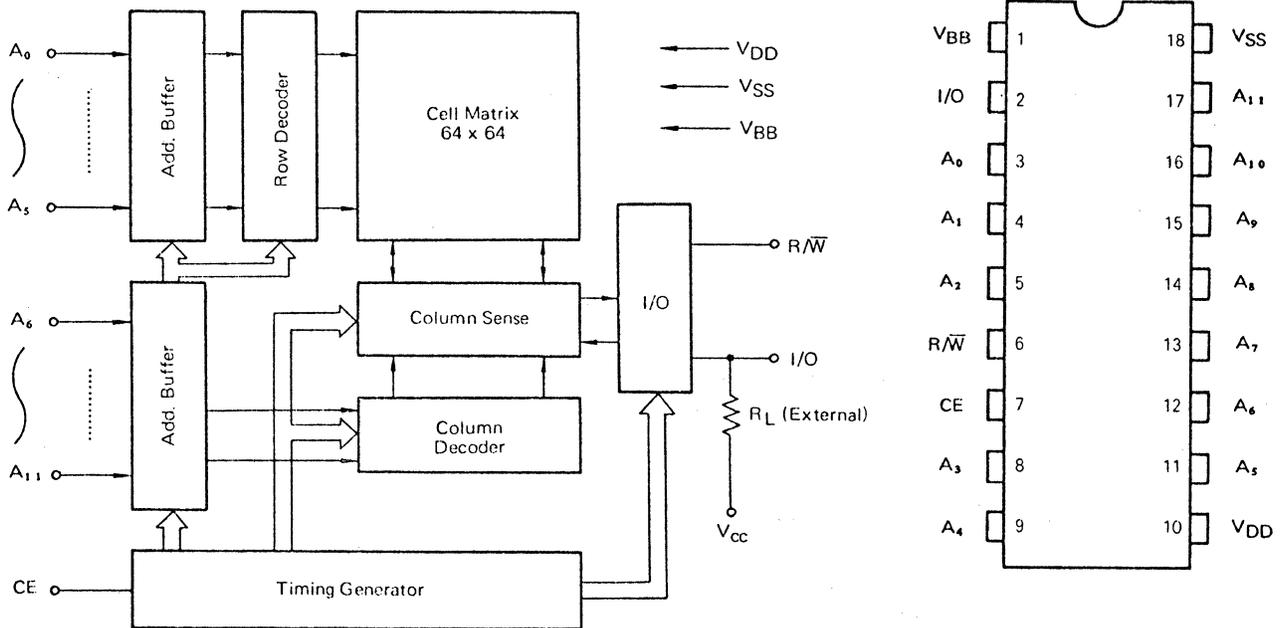
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A309**



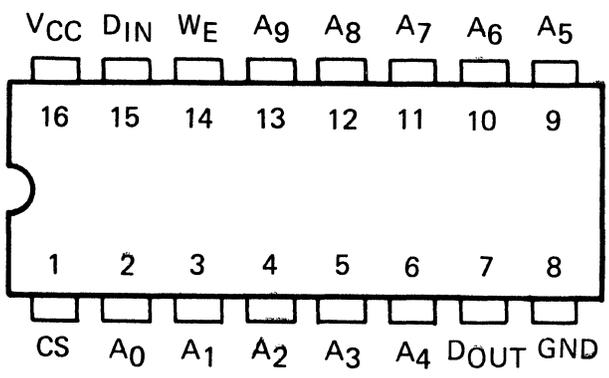
**A310**



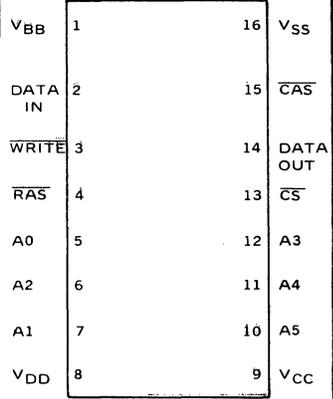
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

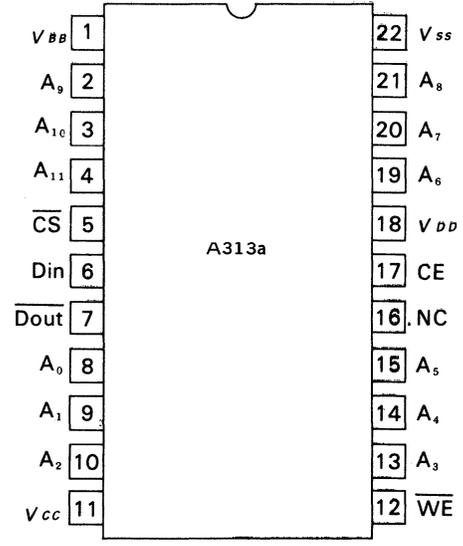
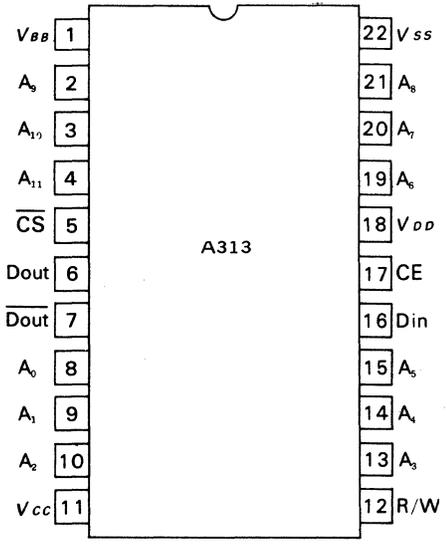
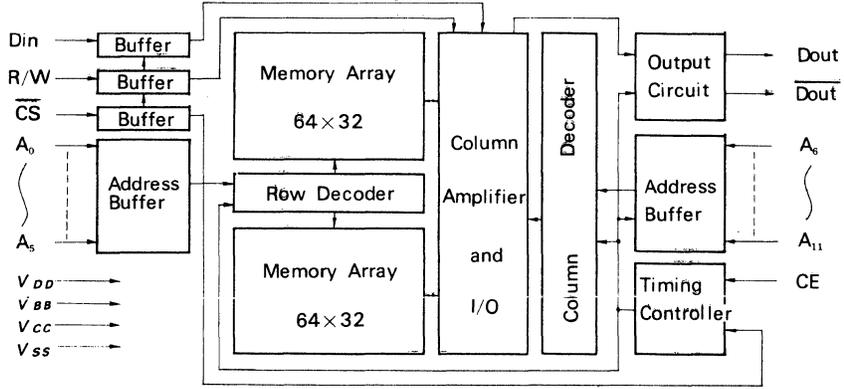
**A311**



**A312**



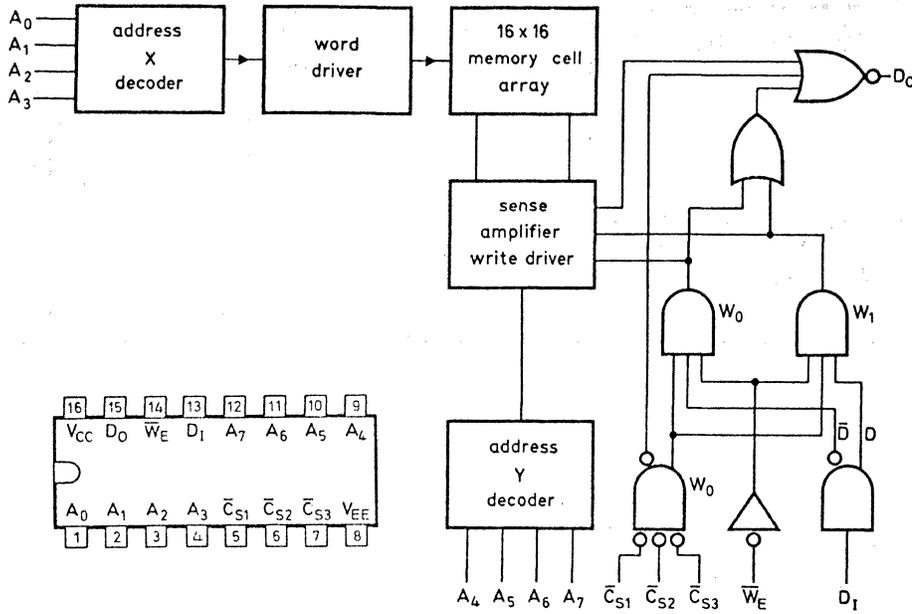
**A313**



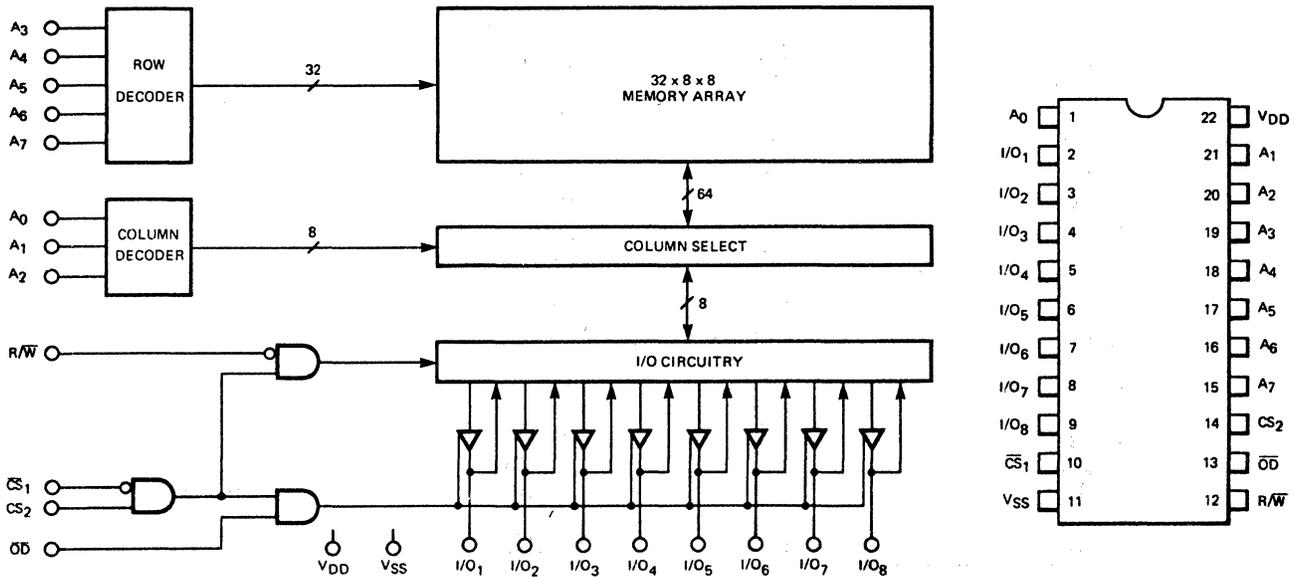
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A314**



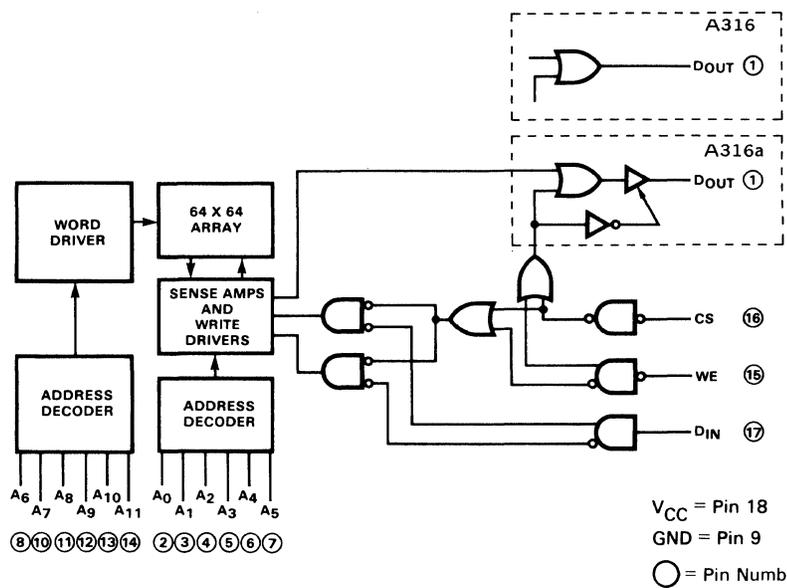
**A315**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A316**

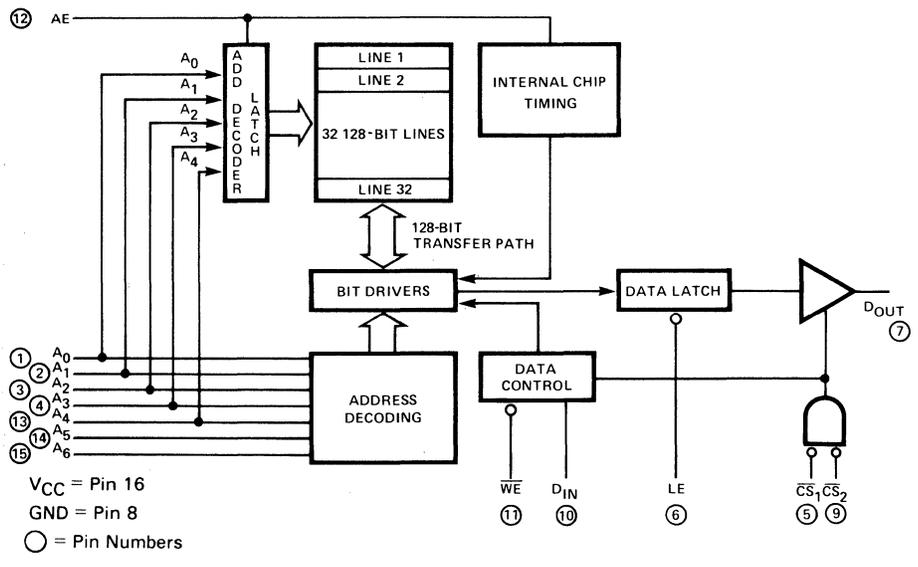


[Empty box]

[Empty box]

[Empty box]

**A317**

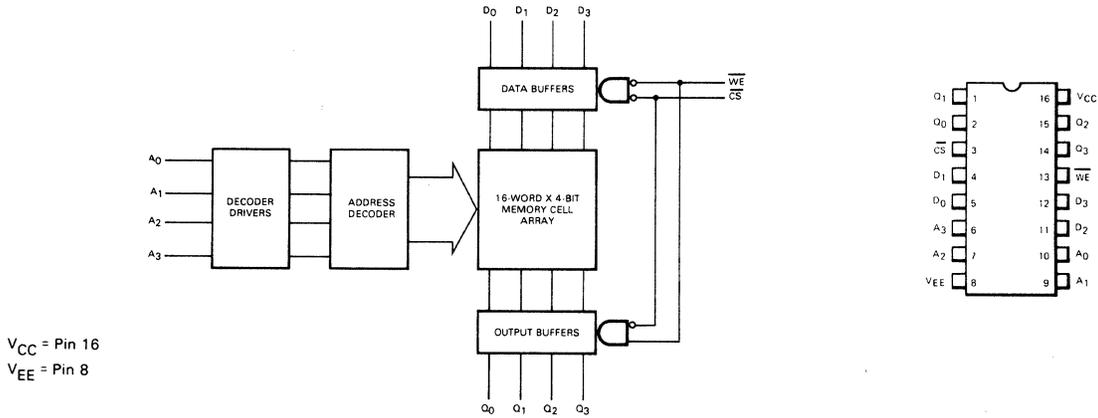


[Empty box]

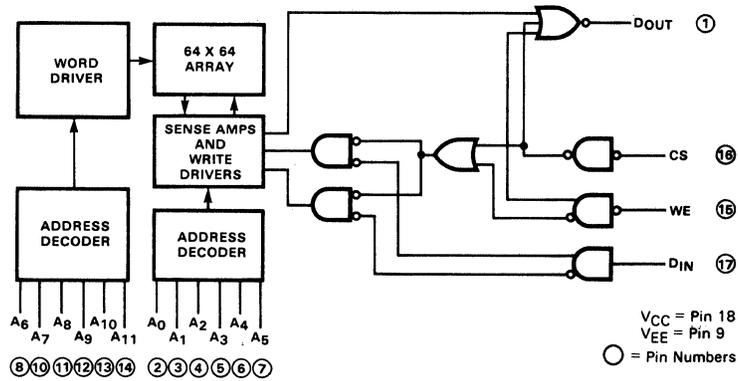
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

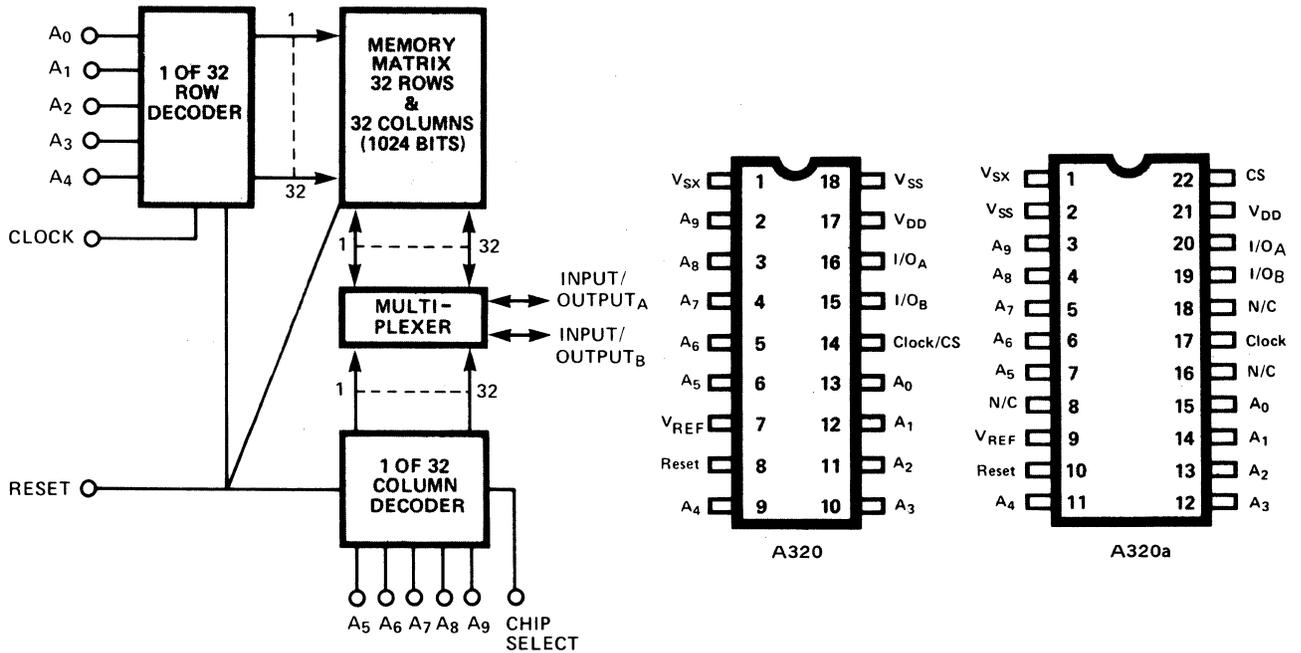
**A318**



**A319**



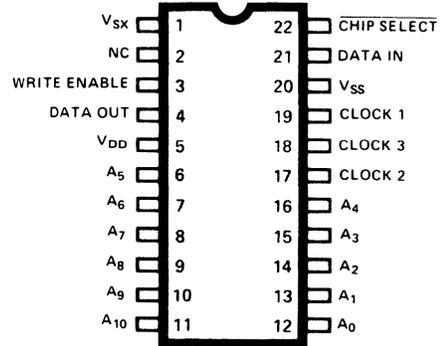
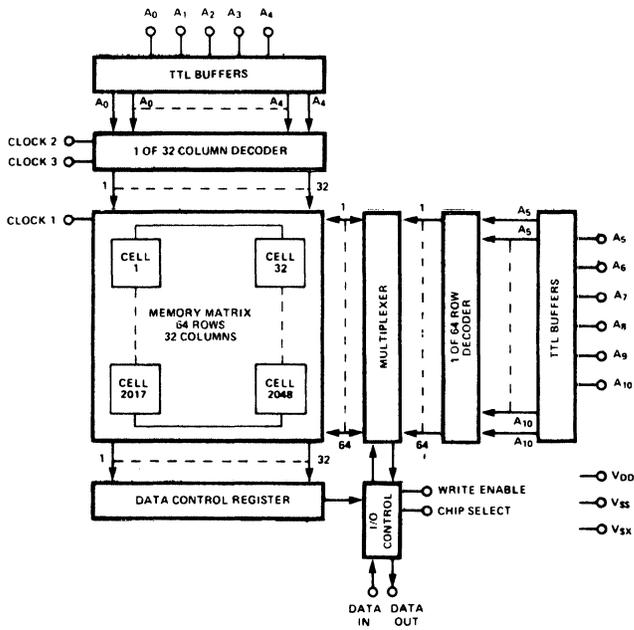
**A320**



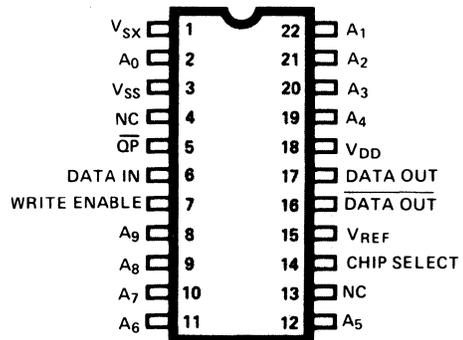
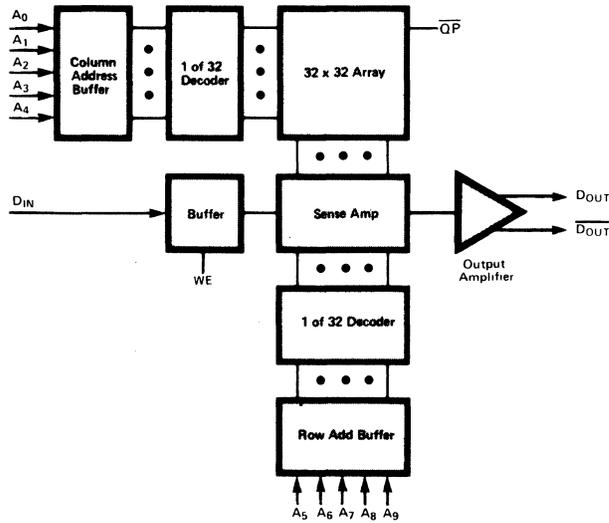
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A321**



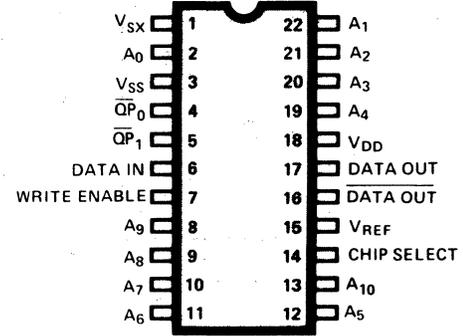
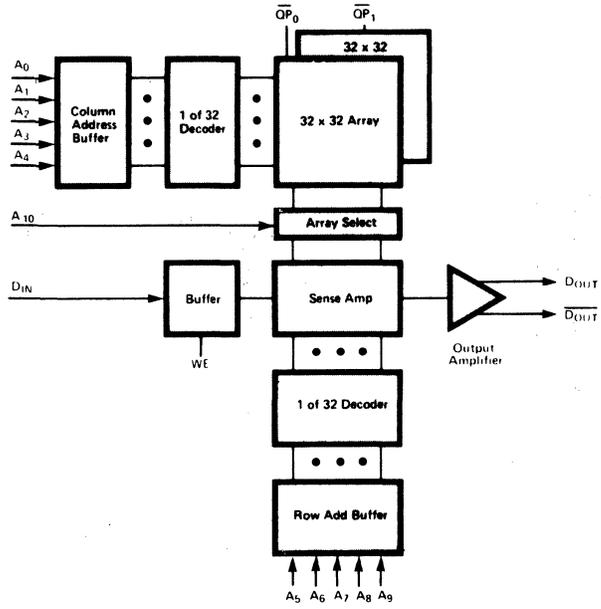
**A322**



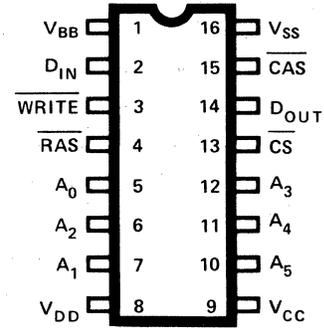
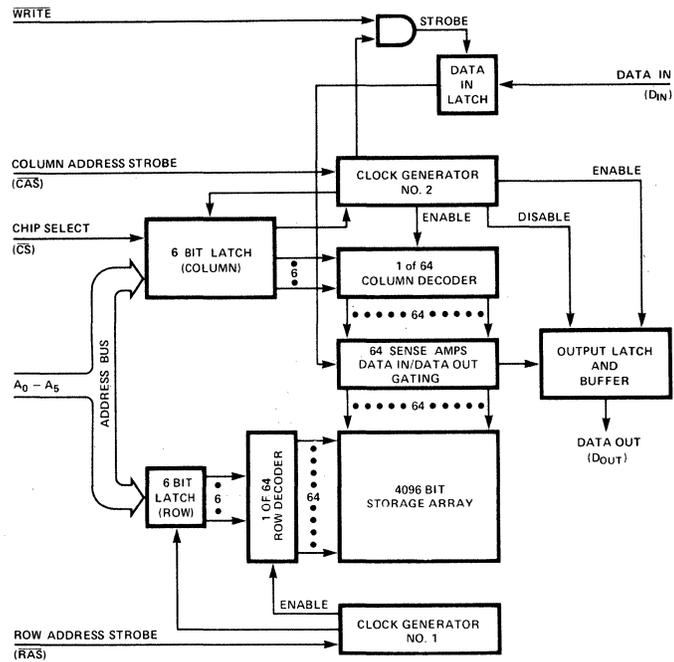
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**A323**



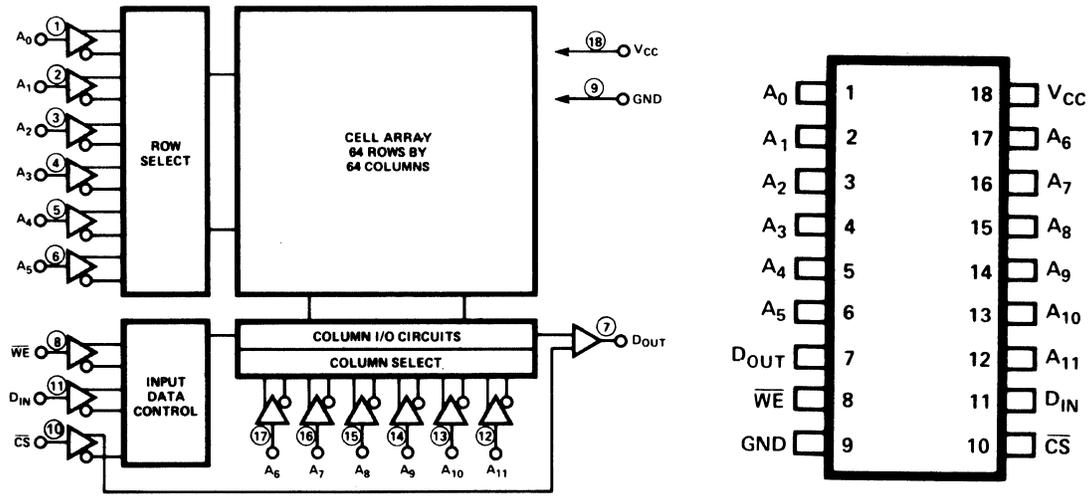
**A324**



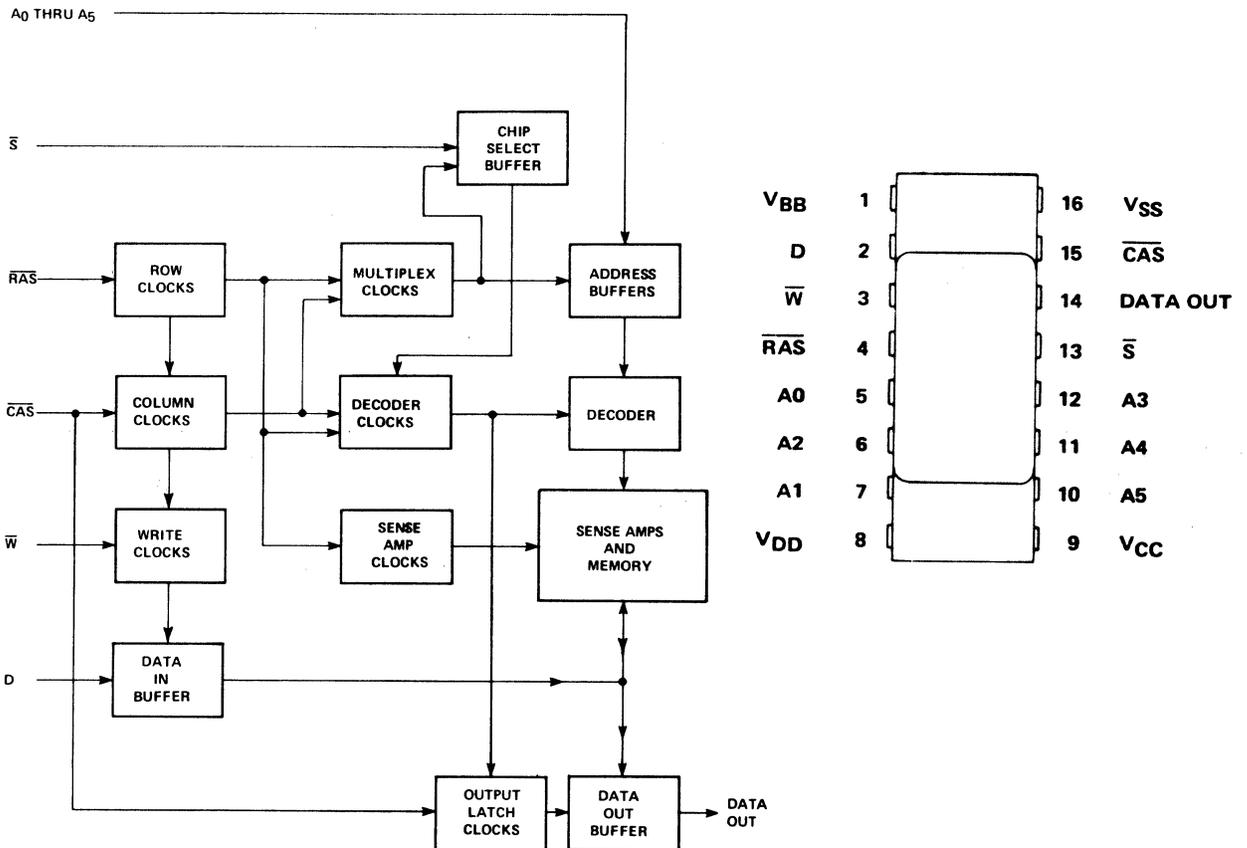
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**A325**



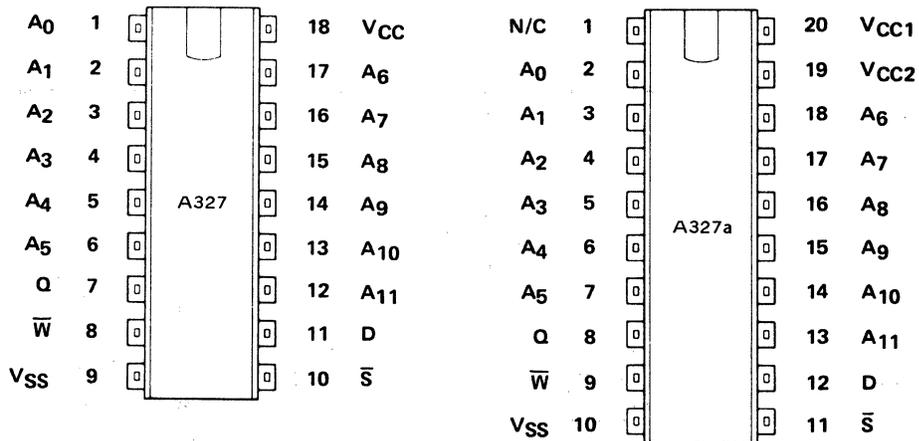
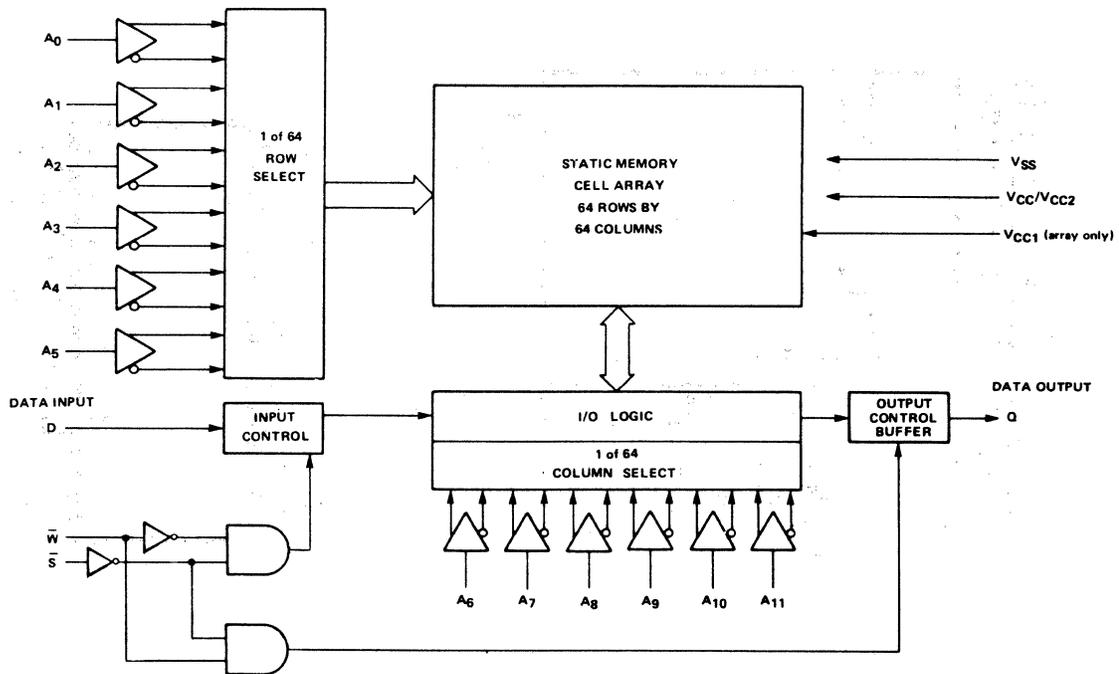
**A326**



# 22. LOGIC/BLOCK DRAWINGS

IN-DRAWING NUMBER  
SEQUENCE

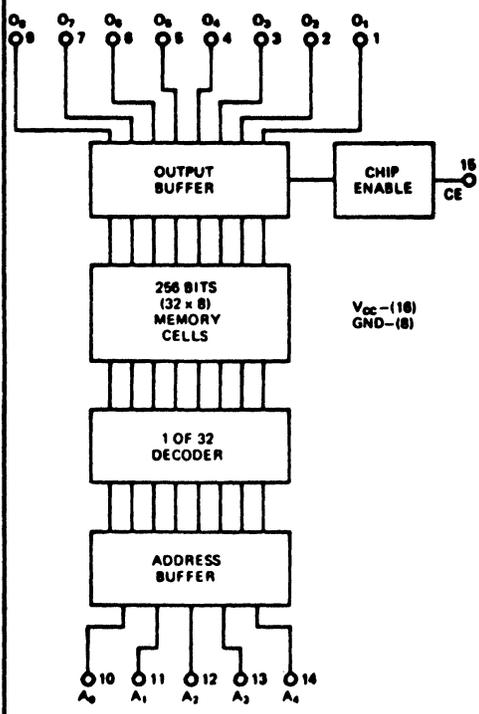
**A327**



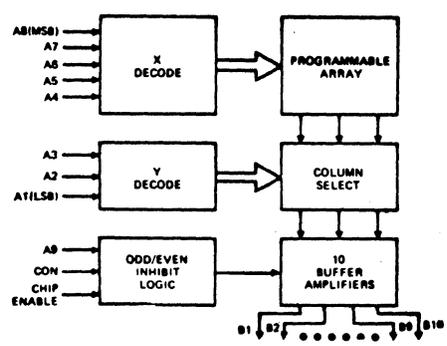
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B1**

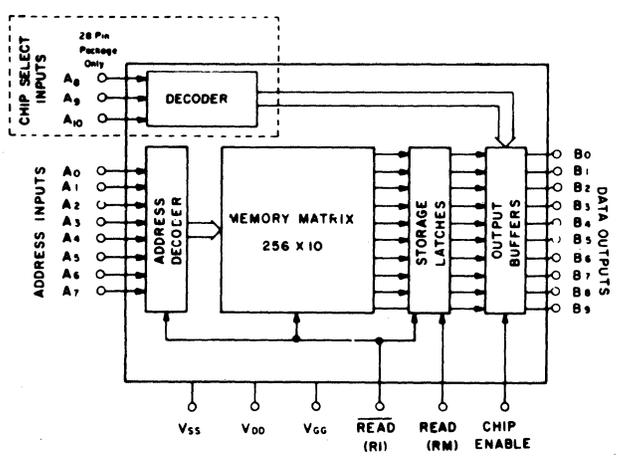


**B3**



	A										B										CON	CHIP ENABLE	VSS	VDD	VGG	
	1	2	3	4	5	6	7	8	9	10	1	2	3	4	5	6	7	8	9	10	15	14	12	24	16	
B3	3	2	1	21	20	19	18	17	13	4	5	6	7	8	9	10	11									
B3a	3	2	1	21	20	19	18	17	16	7	8	9	10	11												
B3b	1	24	23	21	20	19	18	17	13	2	3	4	5	6	7	8	9	10	11	15	14	12	22	16		
B3c	23	22	21	20	19	18	17	16	13	11	10	9	8	7	6	5	4	3	2	14	15	24	12	1		

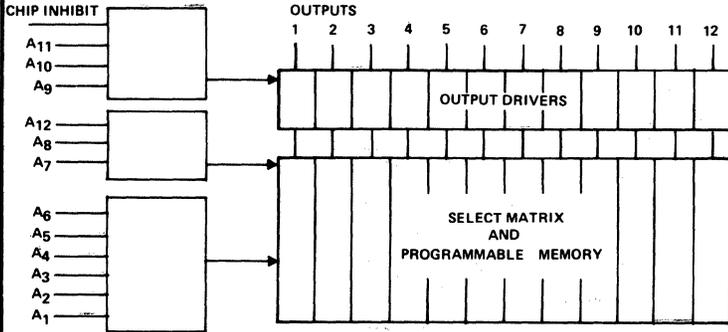
**B7**



# 22. LOGIC/BLOCK DRAWINGS

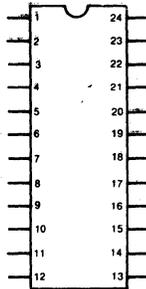
IN DRAWING NUMBER SEQUENCE

## B9



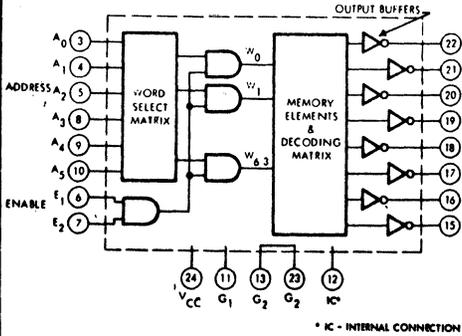
C1	A												C1-P15												VCC	VDD	VGG			
	1	2	3	4	5	6	7	8	9	10	11	12	1	2	3	4	5	6	7	8	9	10	11	12						
B9	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	NC	NC	NC	NC							
B9a	10	20	19	18	16	15	14	22	23	13	12	11	8	7	6	5	4	3	2	1	NC	NC	NC	NC						
B9b	17	25	24	22	21	20	19	26	27	16	15	14	12	11	10	9	8	7	6	5	4	3	2	1						
B9c	3	2	1	24	23	22	21	20	19	6	5	4	18	16	15	14	13	12	11	10	9									
B9d	5	4	3	2	1	28	27	22	21	8	7	6	20	18	17	16	15	14	13	12	11									
B9e	11	21	20	19	17	16	15	22	23	14	13	12	NC	8	7	6	5	4	3	2	1	NC	NC	NC	NC					
B9f	9	19	18	17	16	15	NC	14	13	12	11	10	NC	8	7	6	5	4	3	2	1	NC	NC	NC	NC					
B9g	12	24	19	18	17	16	15	4	3				2	6	7	8	9	10												
B9h	12	24	23	22	21	20	19	4	3				2	6	7	8	9	10												
B9j	16	22	21	20	19	18	17	23	24	15	14	13		11	10	9	8	7	6	5	4	3	2	1	28					
B9k	9	19	18	17	16	15	14	13	12	11				8	7	6	5	4	3	2	1				20	21	23			
B9m	9	19	18	17	16	15	14	13	12	11				8	7	6	5	4	3	2	1				20	21	23			
B9n	9	19	18	17	16	15	14	13	12	11				8	7	6	5	4	3	2	1				20	21	23			
B9p	9	19	18	17	16	15	14	13	12	11	10			8	7	6	5	4	3	2	1				20	21	23			

## B11

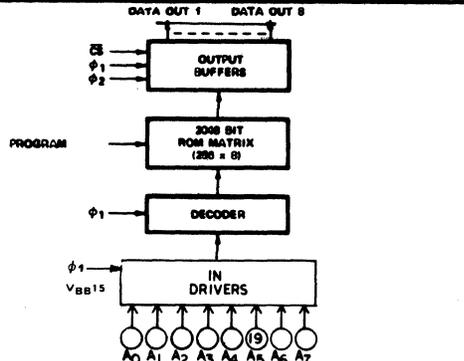


PIN NUMBERS																								
B11a	A8	NC	NC	NC	A5	A4	A3	A2	A1	B1	NC	GND	B2	B3	B4	B5	B6	B7	B8	CT1	CT2	NC	A0	VCC
B11b	A3	A2	A1	B1	B2	B3	B4	B5	B6	B7	B8	VSS	A9	CT	CM	VGG	A8	A7	A6	A5	A4	NC	NC	VDD
B11c	VSS	VGG	A5	A4	A3	A2	A1	O1	O2	O3	O4	O5	O6	O7	O8	O9	O10	Inh	A11	A10	A9	A8	A7	A6
B11d	VSS	VGG	A5	A4	A3	A2	A1	O1	O2	O3	O4	O5	O6	O7	O8	O9	O10	Inh	A11	A10	A9	A8	A7	A6
B11e	VSS	VGG	A5	A4	A3	A2	A1	O1	O2	O3	O4	O5	O6	O7	O8	O9	O10	Inh	A11	A10	A9	A8	A7	A6
B11f	VSS	VGG	A5	A4	A3	A2	A1	O1	O2	O3	O4	O5	O6	O7	O8	O9	O10	Inh	A11	A10	A9	A8	A7	A6
B11g	A6	A5	A4	A3	A2	A1	A0	O8	O7	O6	O5	GND	C4	O3	O2	O1	CS	CS	HS	HS	HS	A7	VCC	
B11h	A7	A6	A5	A4	A3	A2	A1	A0	O1	O2	O3	GND	C4	O5	O6	O7	O8	CS4	CS3	CS2	CS1	VCC2	A8	VCC1
B11i	A7	A6	A5	A4	A3	A2	A1	A0	BUS0	BUS1	BUS2	VSS	BUS3	BUS4	BUS5	BUS6	BUS7	CEO	MRD	CS2	CS1	NC	CLOCK	VDD
B11k	A7	A6	A5	A4	A3	A2	A1	A0	BUS0	BUS1	BUS2	VSS	BUS3	BUS4	BUS5	BUS6	BUS7	CEO	MRD	CS2	CS1	NC	CLOCK	VDD
B11m	A7	A6	A5	A4	A3	A2	A1	A0	O1	O2	O3	VSS	C4	O5	O6	O7	O8	NC	NC	CS	NC	NC	A8	VDD
B11n	CS	A0	A1	A2	SA	A3	A4	A5	A8	A7	Ø	VBB	VSS	VDD	MS	Ø	O6	O5	O4	O3	O2	O1	O0	VCC
B11p	A7	A6	A5	A4	A3	A2	A1	A0	O1	O2	O3	VSS	O4	O5	O6	O7	O8	CS2	VDD	CS1	VBB	A9	A8	VCC
B11q	ADH	ADU	ADP	ADL	ADD	ADC	ADS	ADA	DOI	DO2	DO3	GND	DO4	DO5	DO6	DO7	DO8	S4	S3	S2	S1	NC	ADY	VCC
B11r	A2	A1	A0	O1	O2	O3	O4	O5	O6	O7	O8	VCC	PRD	CS	VBB	VGG	A7	A6	A5	A4	A3	VCC	VCC	VDD

## B17

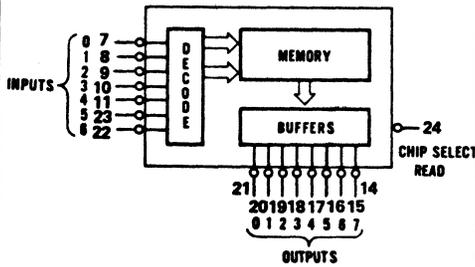


## B18



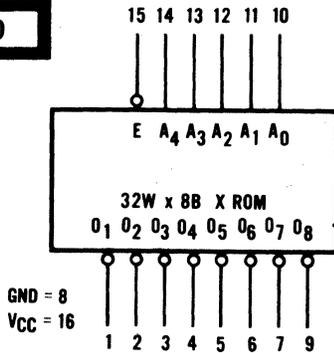
C1	A												CS	Ø1	Ø2	PROG	DATA OUT								VDD	VCC	VGG	VBB
	1	2	3	4	5	6	7	8	23	22	13	12					3	2	1	21	20	19	18	17				
B18	4	5	6	7	8	9	10	11	14	23	22	13	3	2	1	21	20	19	18	17	24	12	16	15				
B18a	4	5	6	7	8	9	10	11	14	23	22	NC	3	2	1	21	20	19	18	17	24	12	16	15				

## B19



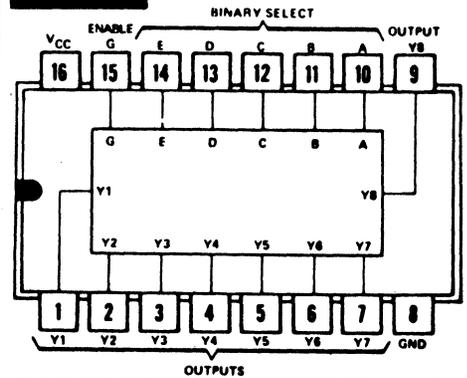
\*WHEN CHIP SELECT READ IS AT GROUND THE OUTPUTS ARE FLOATING.

## B20



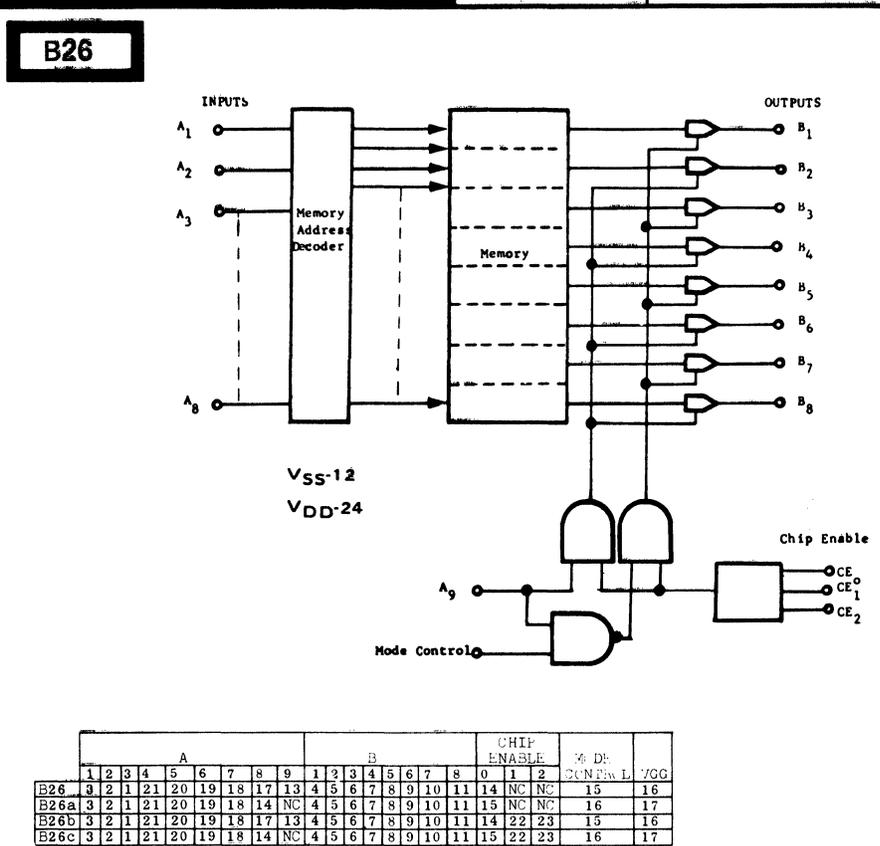
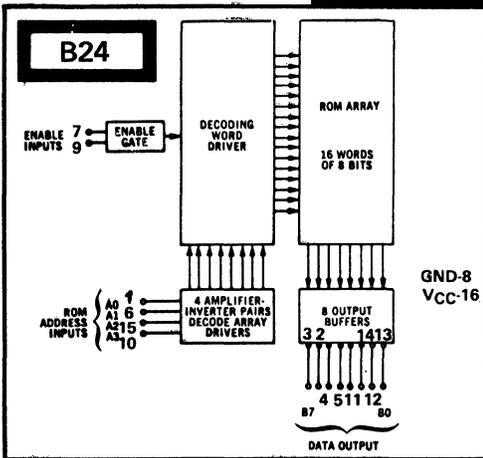
PAIRED DEVICES  
B20 T154DIA/T154DIB

## B21

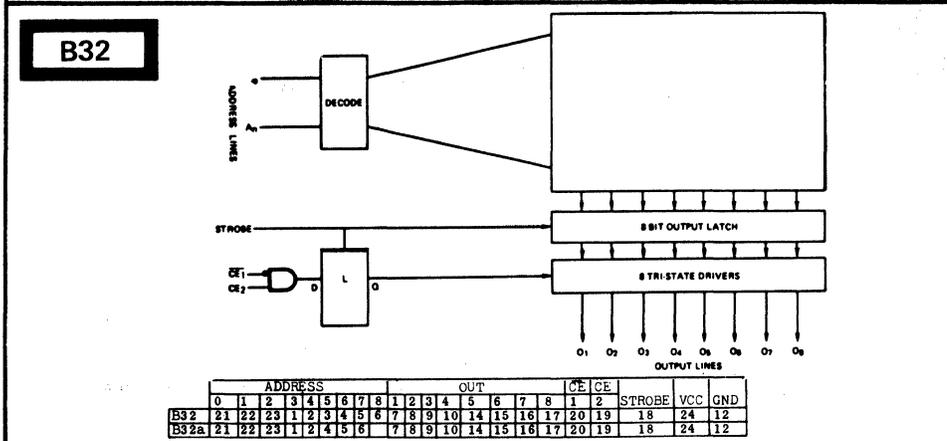
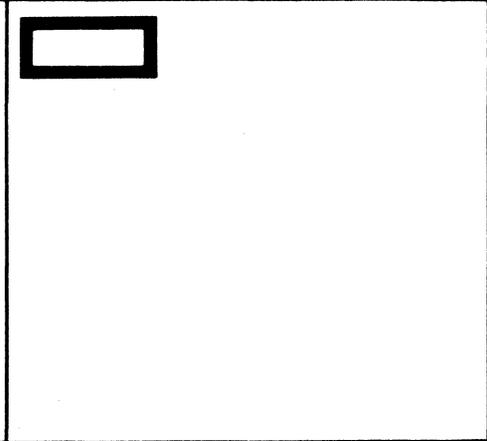
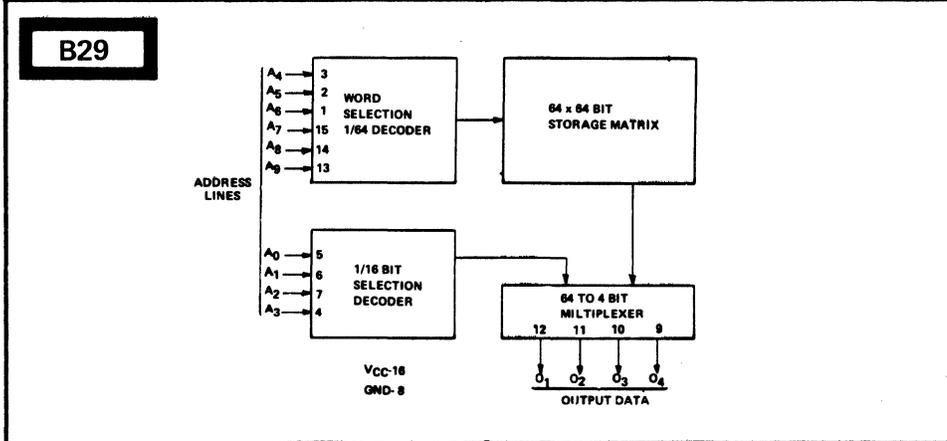
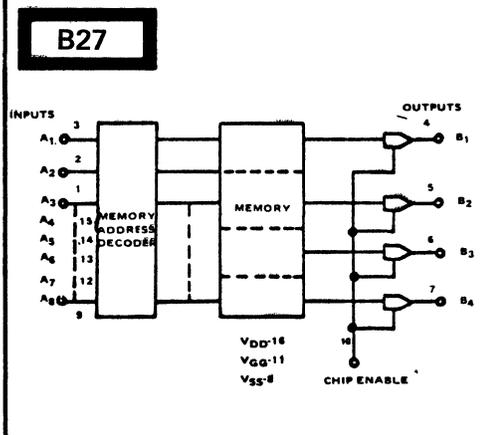


# 22. LOGIC/BLOCK DRAWINGS

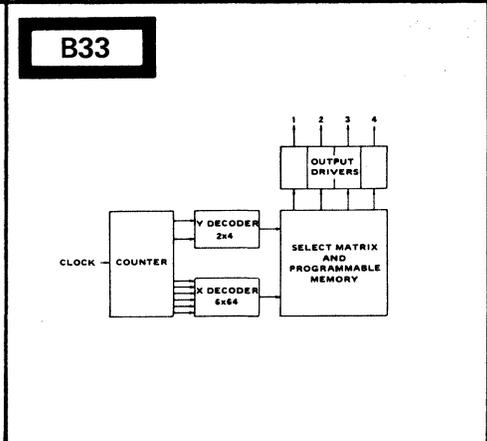
IN DRAWING NUMBER SEQUENCE



	A								B								CHIP ENABLE			* DE		
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	0	1	2	CE0	CE1	VGG
B26	3	2	1	21	20	19	18	17	13	4	5	6	7	8	9	10	11	14	NC	NC	15	16
B26a	3	2	1	21	20	19	18	14	NC	4	5	6	7	8	9	10	11	15	NC	NC	16	17
B26b	3	2	1	21	20	19	18	17	13	4	5	6	7	8	9	10	11	14	22	23	15	16
B26c	3	2	1	21	20	19	18	14	NC	4	5	6	7	8	9	10	11	15	22	23	16	17



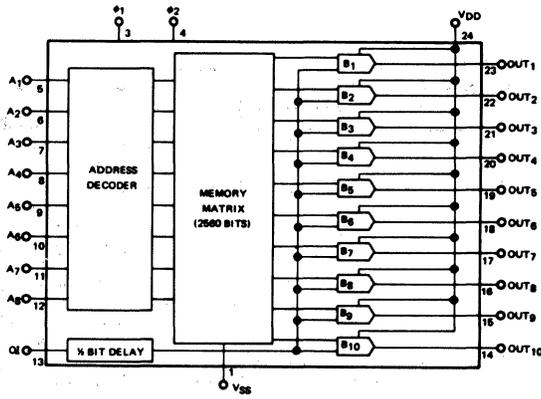
	ADDRESS								OUT								CE		STROBE	VCC	GND	
	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	1	2				
B32	21	22	23	1	2	3	4	5	6	7	8	9	10	14	15	16	17	18	19	18	24	12
B32a	21	22	23	1	2	4	5	6	7	8	9	10	14	13	16	17	20	19	18	24	12	



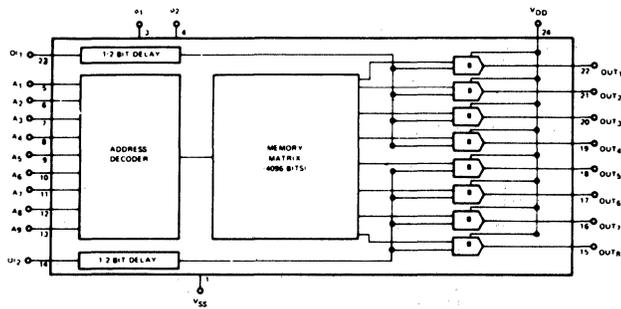
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

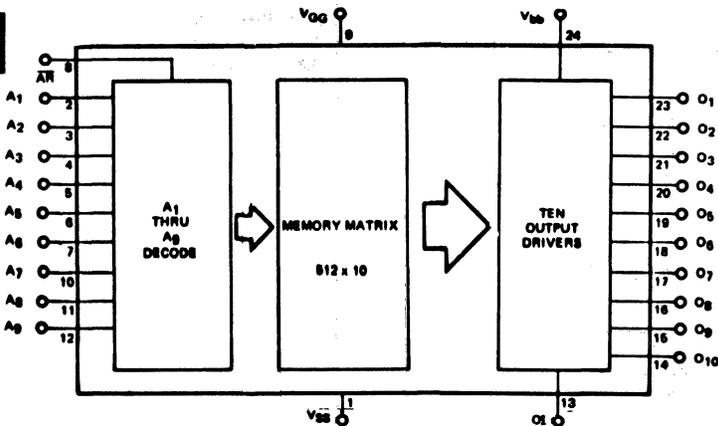
**B35**



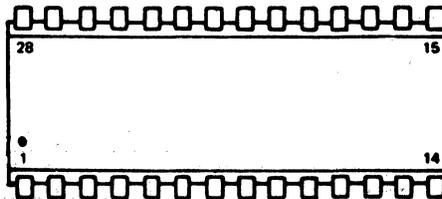
**B36**



**B38**



**B42**

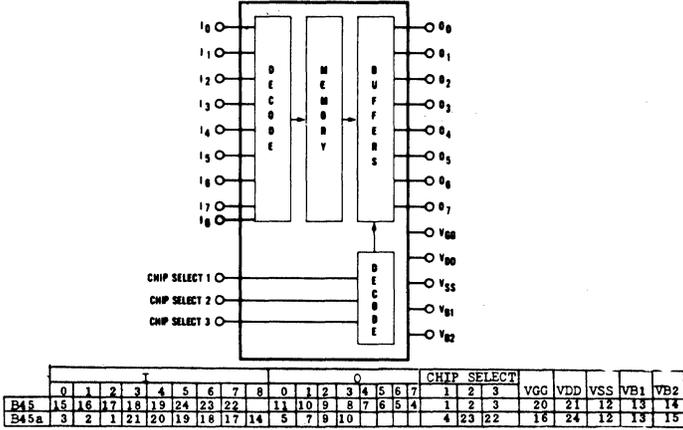


PIN NUMBERS																												
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	
B42	O2	O3	O4	A1	A2	D1	D2	D3	D4	D5	D6	D7	D8	NC	NC	NC	VSS	C4	C2	C3	C1	B4	B3	B2	B1	A4	A3	O1
B42a	D1	C4	VSS	O1	O2	O4	O3	O5	O6	O7	VSS	A2	A1	B1	B2	B3	B4	C1	C3	C2	D8	D7	D6	D5	D4	D3	D2	
B42b	O11	O10	O9	O8	O7	O6	O5	O4	O3	O2	O1	VDD	A13	A10	A9	C7	A5	A5	A4	A3	A2	A1	A7	A3	NC	VSS	NC	C12
B42c	O12	O11	O10	O9	O8	O7	O6	O5	O4	O3	O2	O1	VDD	CS1														
B42d	CS1	CS2	A0	A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	VBB	VSS	VDD	O7	O6	O5	O4	O3	O2	O1	O0	RE	CS4	CS3	VCC
B42e	A7	A6	A5	A4	A3	A2	A1	A0	O1	O2	O3	VSS	VCC	VBB	FG	VCL	O4	O5	O6	O7	O8	NC	VDD	CS	NC	A9	A8	VCC

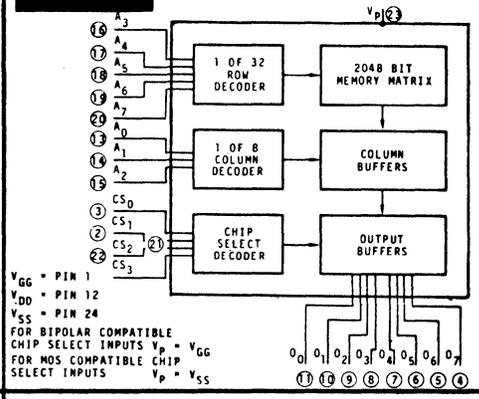
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

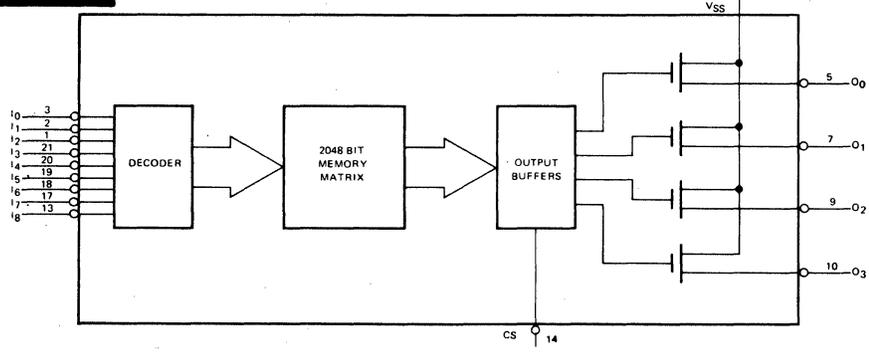
**B45**



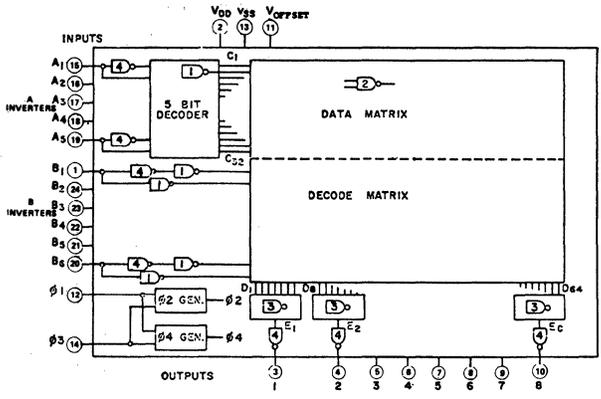
**B46**



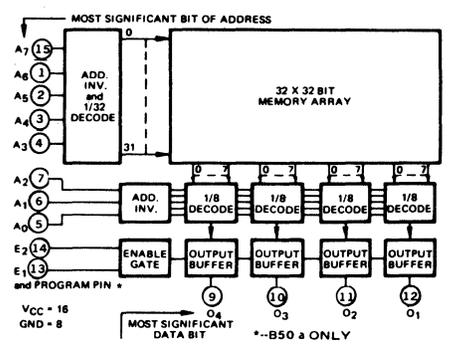
**B47**



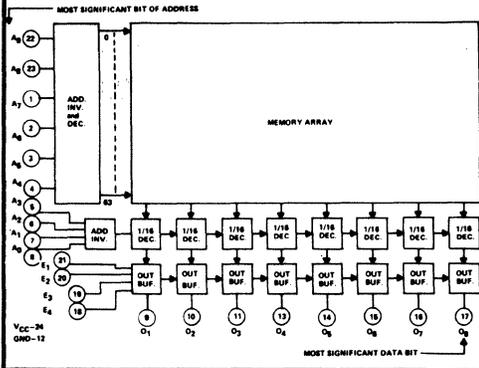
**B49**



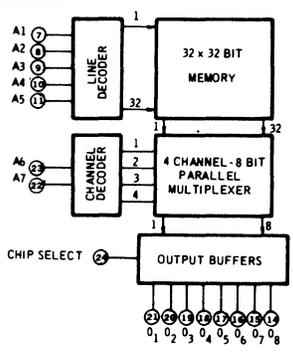
**B50**



**B53**



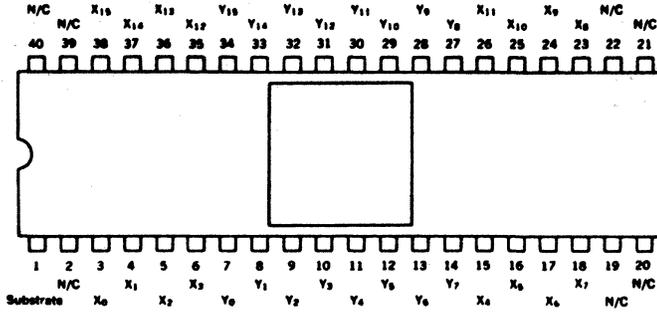
**B57**



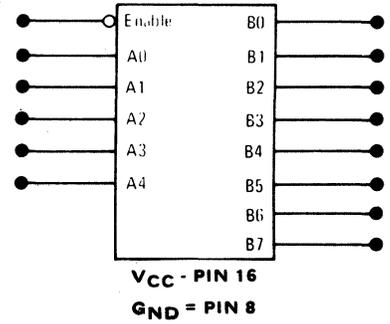
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

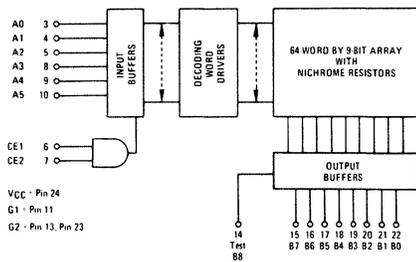
**B59**



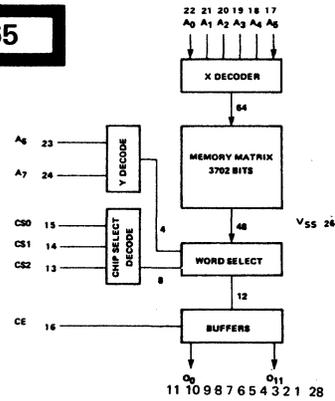
**B60**



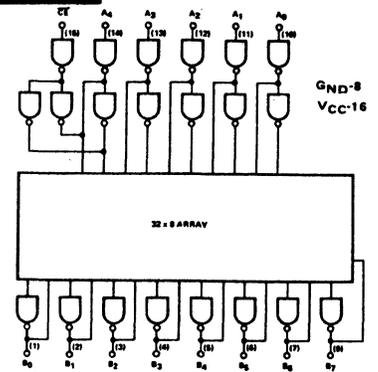
**B61**



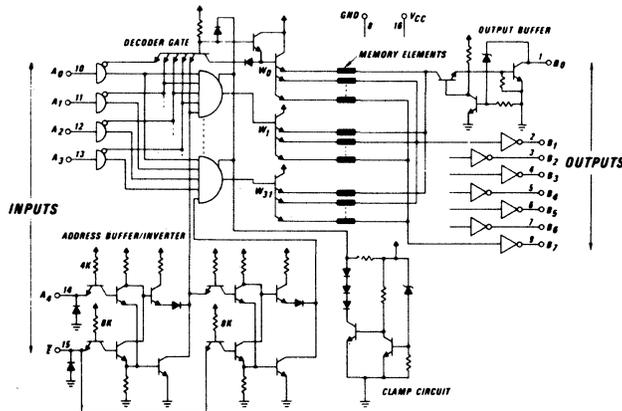
**B65**



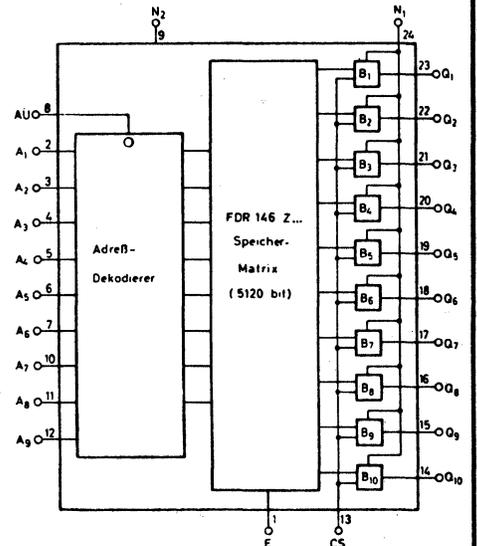
**B67**



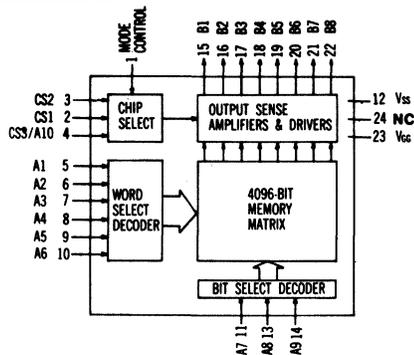
**B70**



**B71**

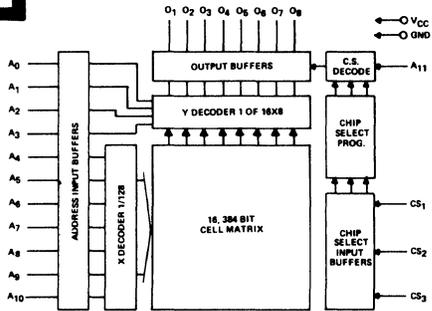


**B73**



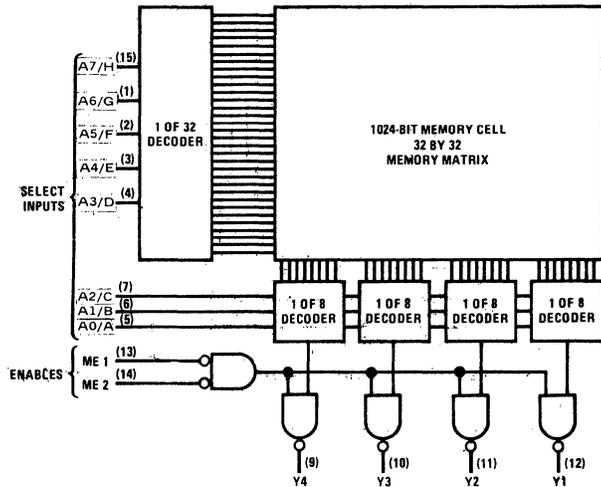
# 22. LOGIC/BLOCK DRAWINGS IN DRAWING NUMBER SEQUENCE

**B78**



	A											O								CS			VCC	GND							
	0	1	2	3	4	5	6	7	8	9	10	11	0	1	2	3	4	5	6	7	8	9	10	11	1	2	3				
B78	1	2	3	4	5	6	7	8	9	10	11	NC	23	22	21	20	19	18	17	16	15	14	13	24	12						
B78a	1	2	3	4	5	6	7	8	9	10	11	16	23	22	21	20	NC	NC	NC	NC	15	14	13	24	12						
B78b	5	6	7	8	9	10	11	1	2	3	4	NC	23	22	21	20	19	18	17	16	15	14	13	24	12						
B78c	8	7	6	5	4	3	2	1	23	22	19	NC	9	10	11	13	14	16	18	17	NC	20	18	21	24	12					

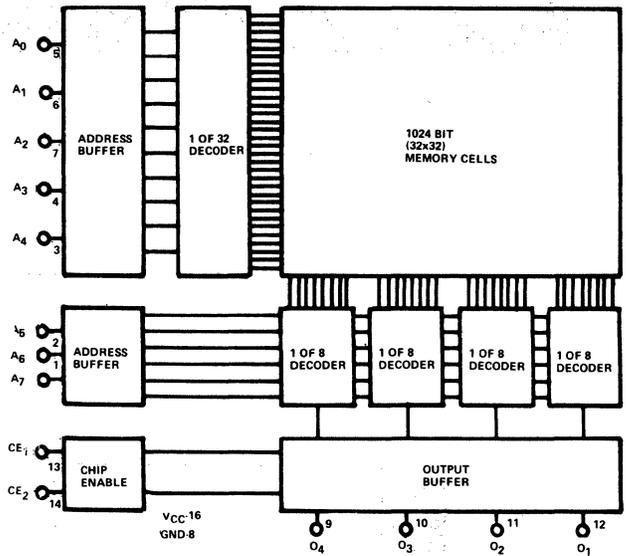
**B80**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B82**

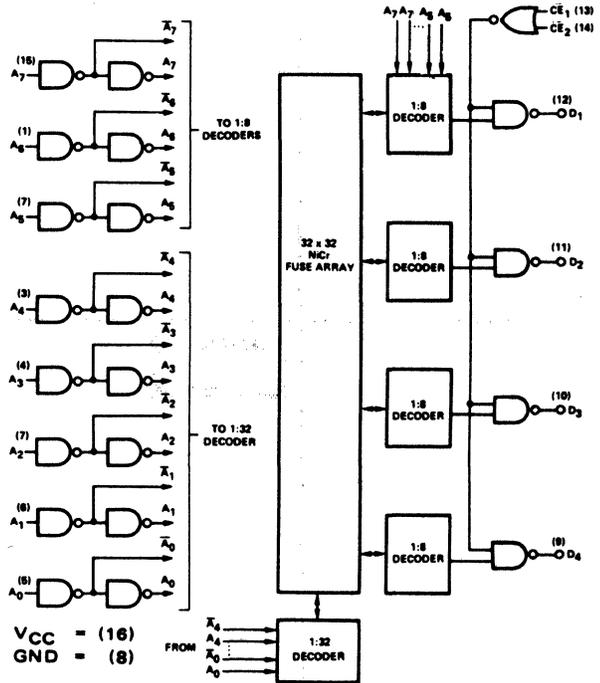


[ ]

[ ]

[ ]

**B87**

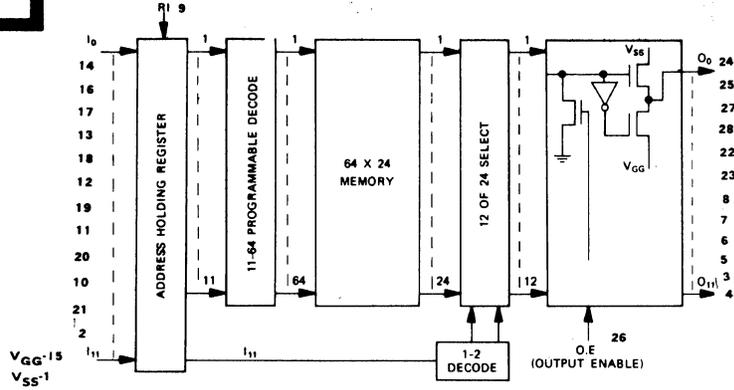


[ ]

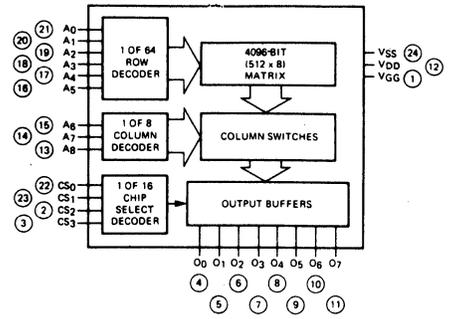
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

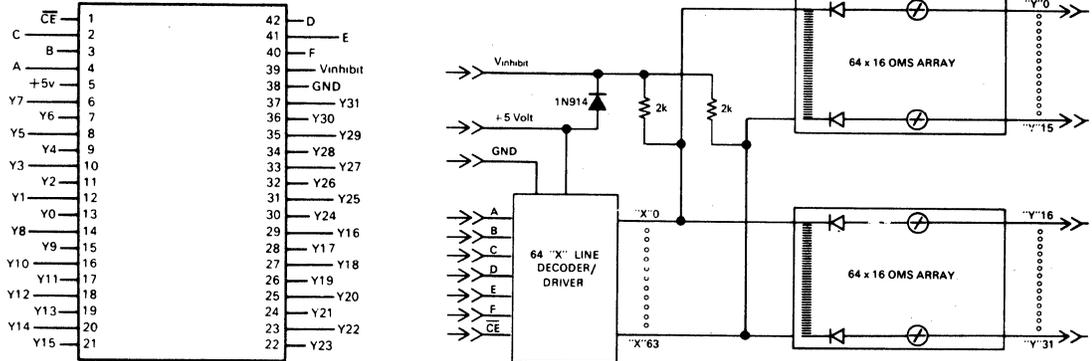
### B89



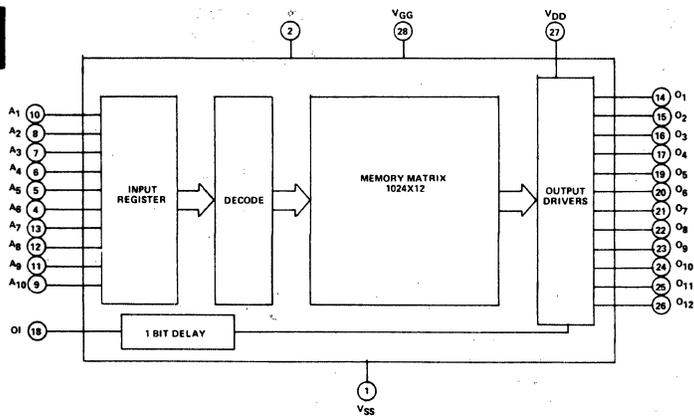
### B90



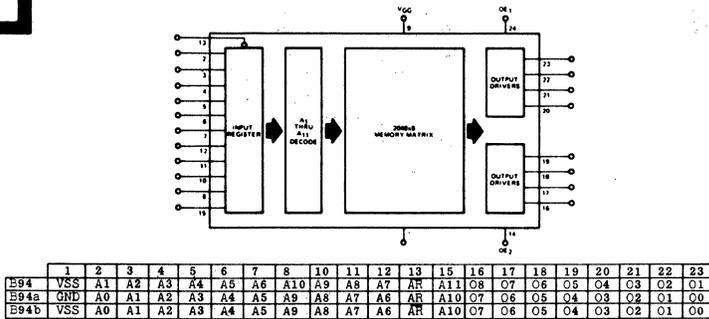
### B91



### B93

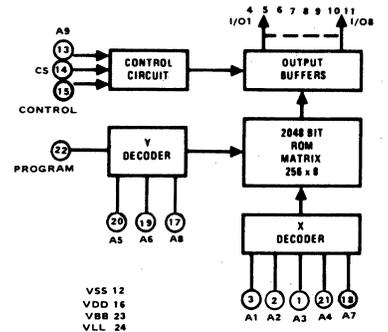


### B94



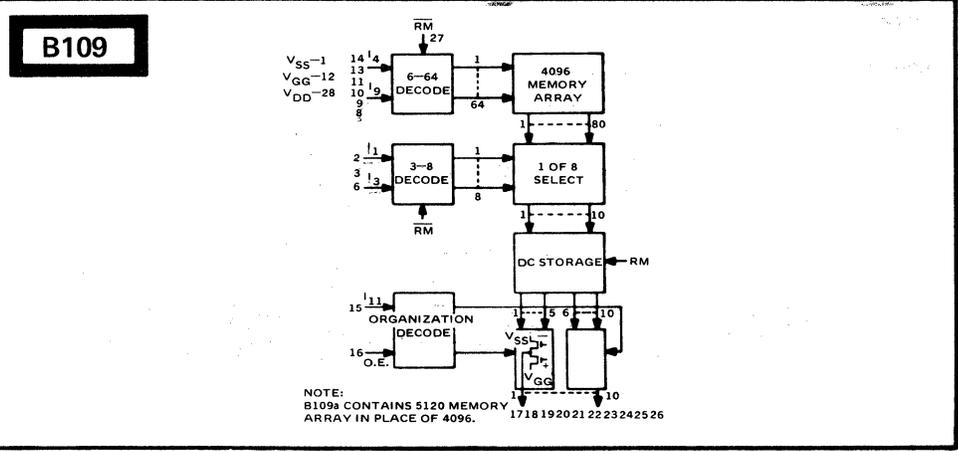
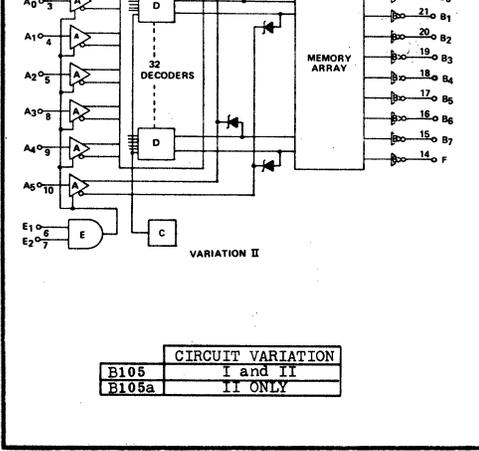
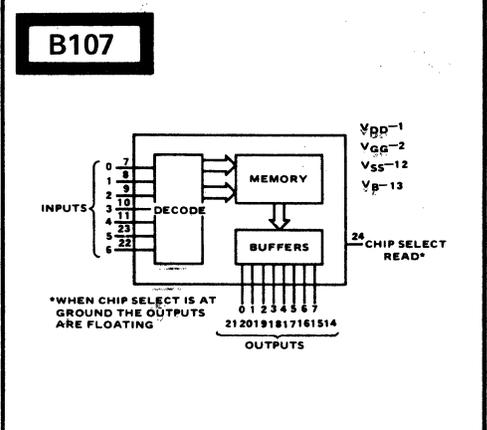
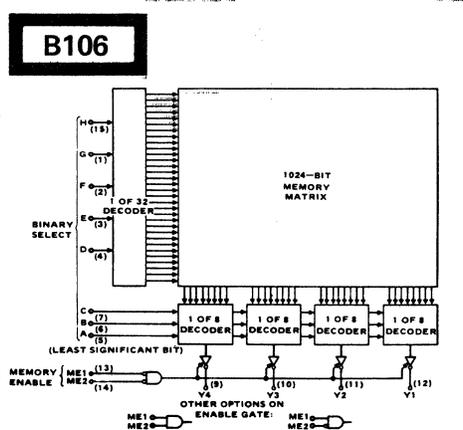
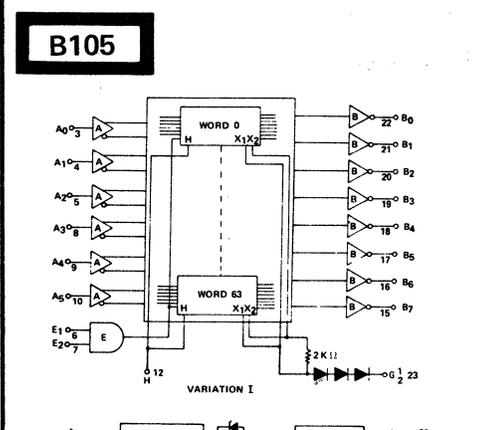
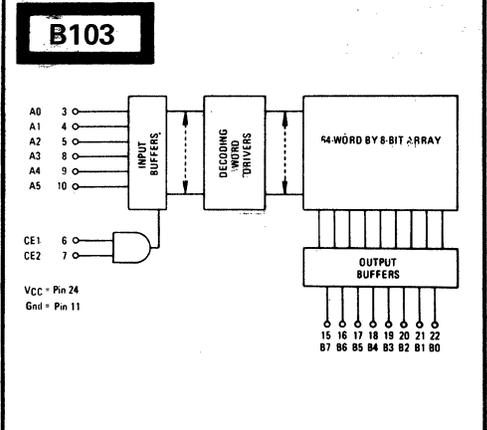
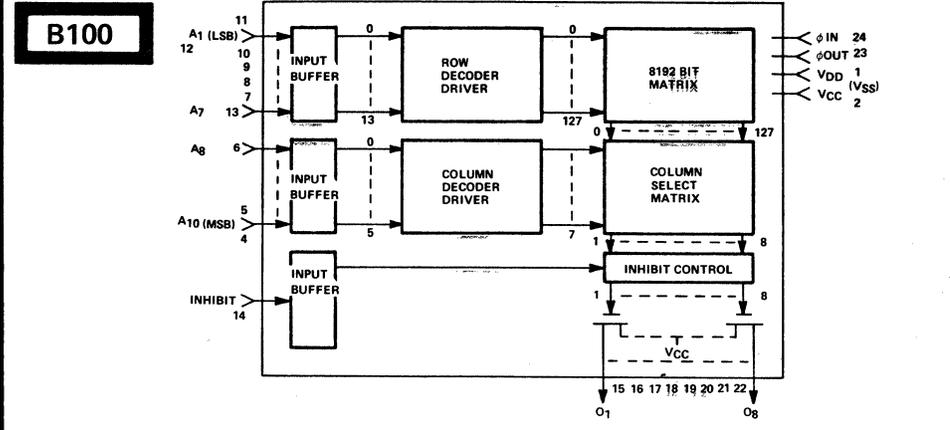
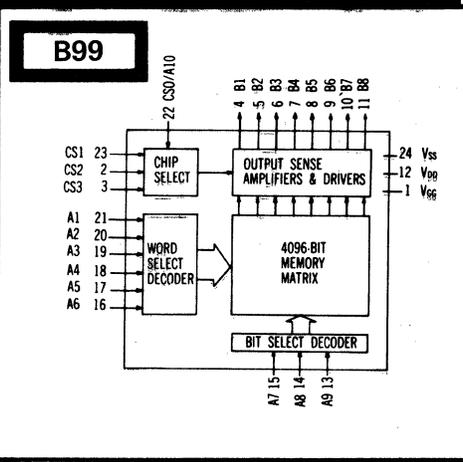
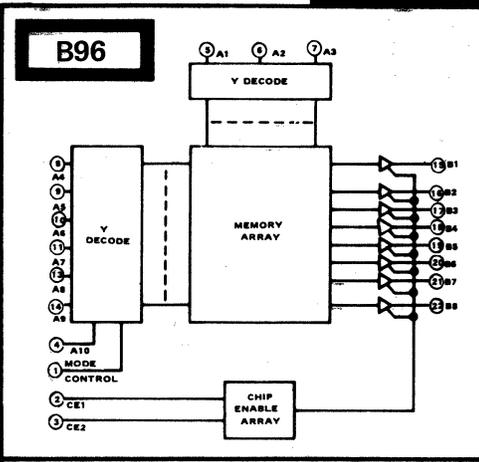
	1	2	3	4	5	6	7	8	10	11	12	13	15	16	17	18	19	20	21	22	23
B94	VSS	A1	A2	A3	A4	A5	A6	A10	A9	A8	A7	A6	A11	O8	O7	O6	O5	O4	O3	O2	O1
B94a	GND	A0	A1	A2	A3	A4	A5	A9	A8	A7	A6	A6	A10	O7	O6	O5	O4	O3	O2	O1	O0
B94b	VSS	A0	A1	A2	A3	A4	A5	A9	A8	A7	A6	A6	A10	O7	O6	O5	O4	O3	O2	O1	O0

### B95



# 22. LOGIC/BLOCK DRAWINGS

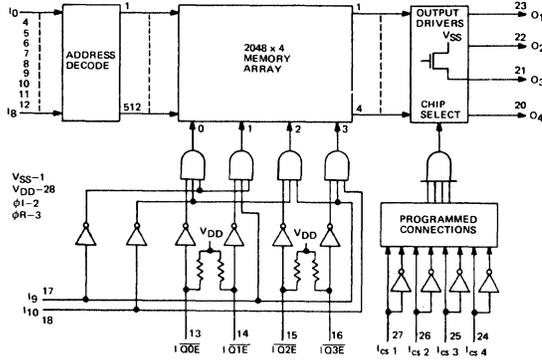
IN DRAWING NUMBER  
SEQUENCE



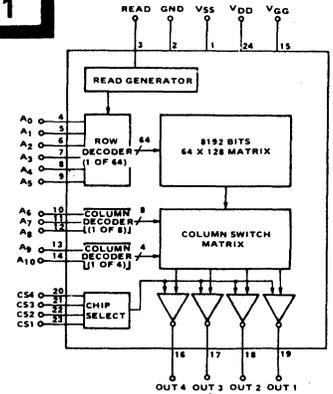
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

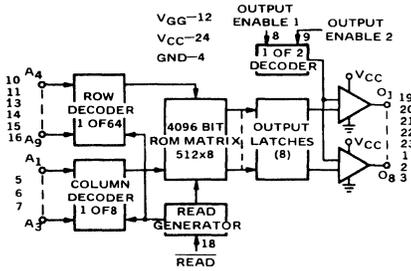
**B110**



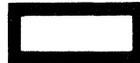
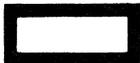
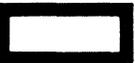
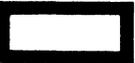
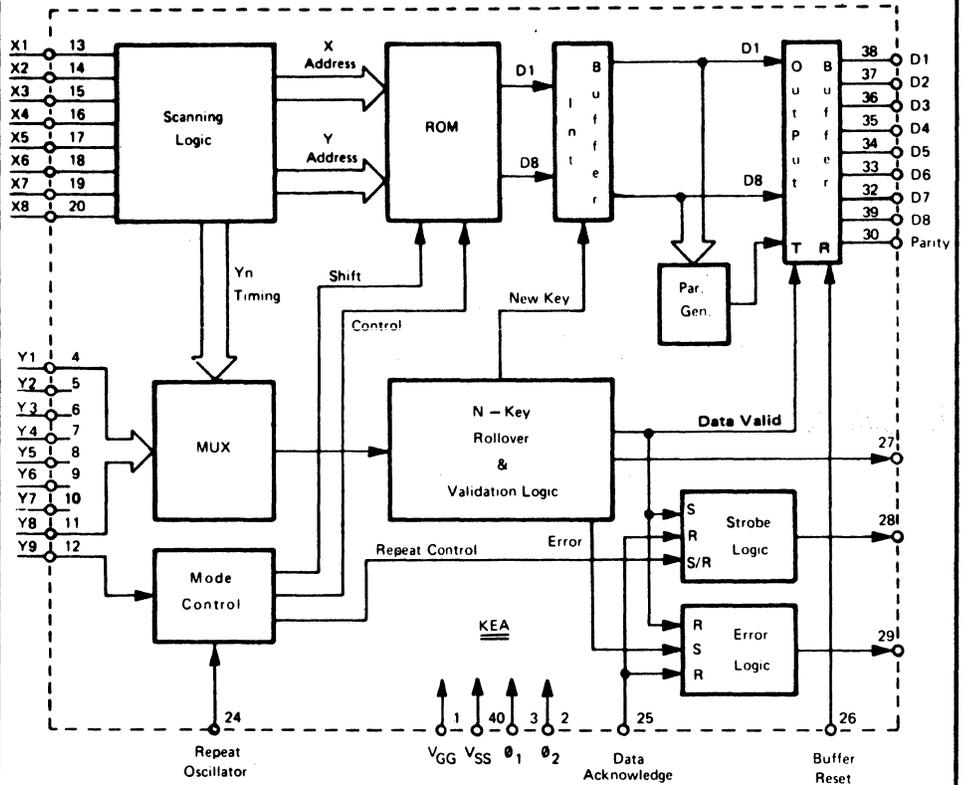
**B111**



**B112**

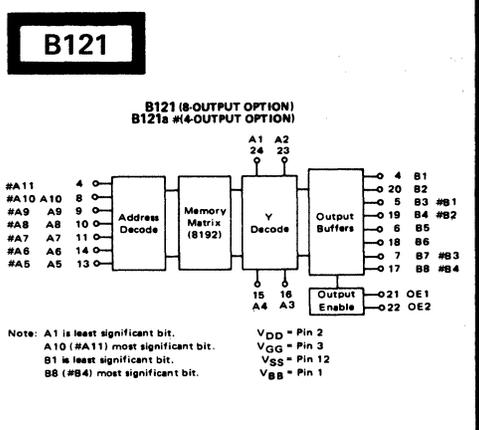
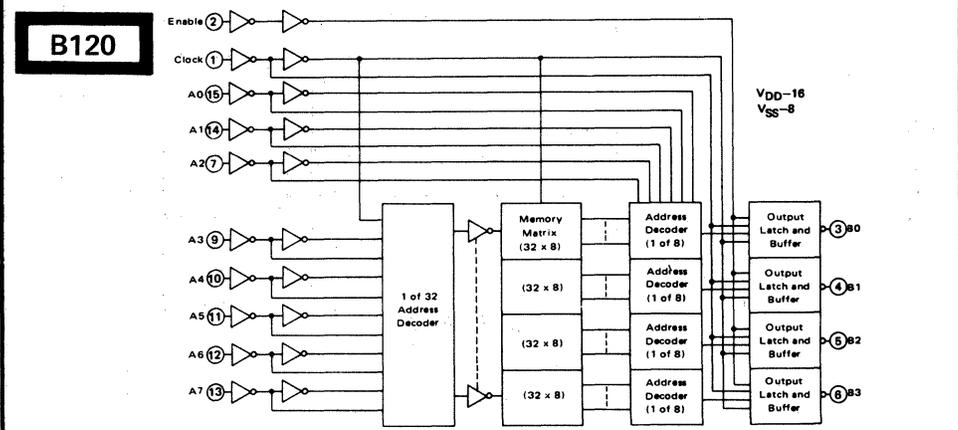
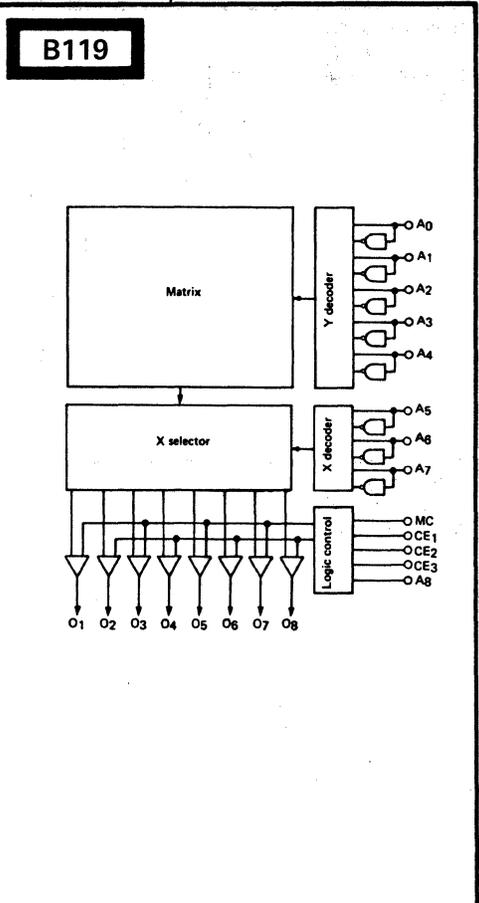
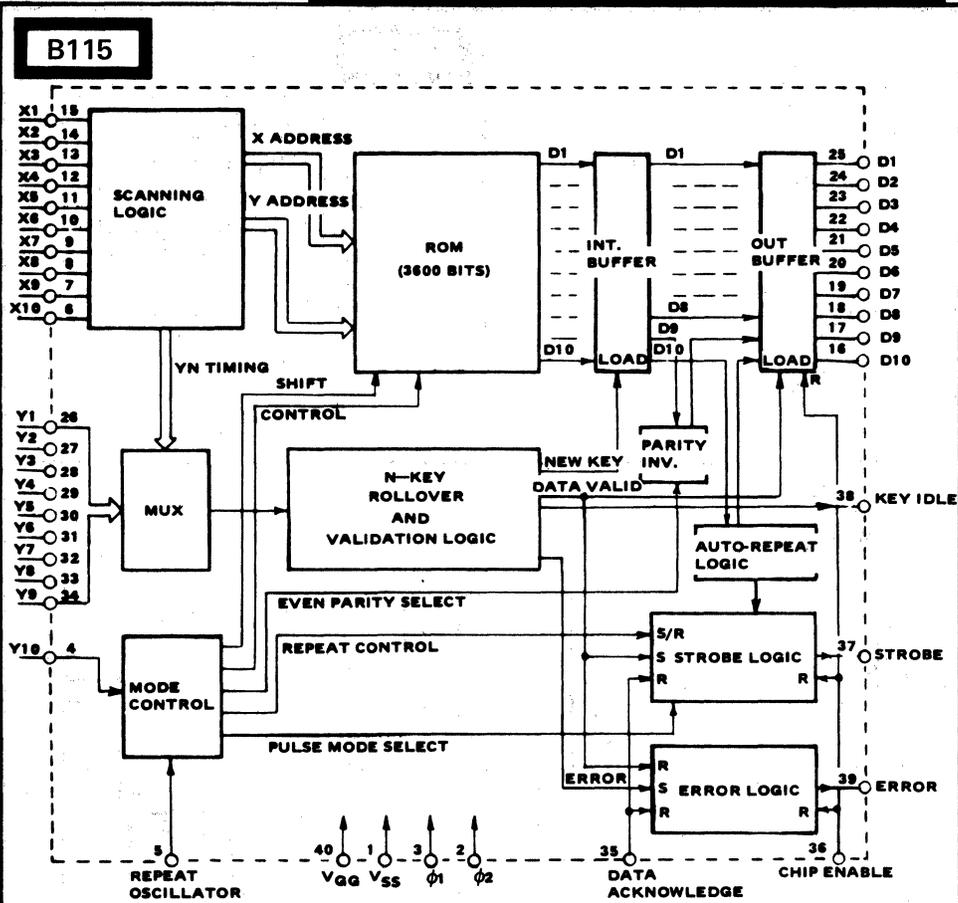


**B113**



# 22. LOGIC/BLOCK DRAWINGS

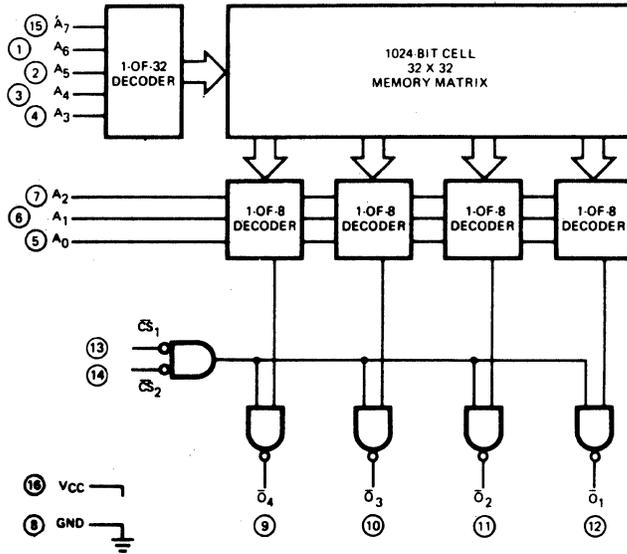
IN DRAWING NUMBER SEQUENCE



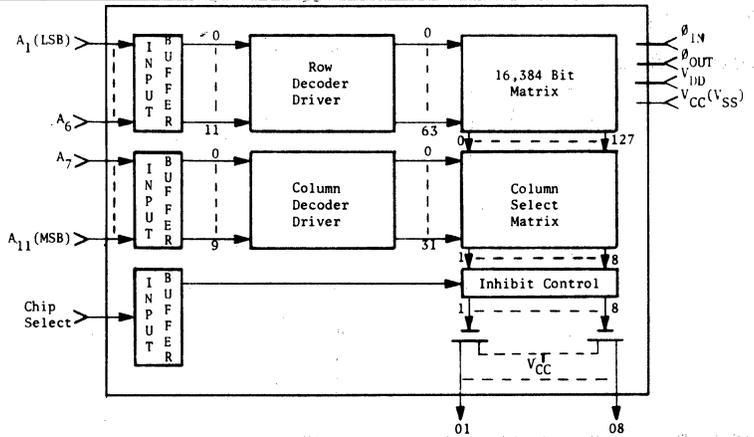
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**B123**



**B124**



**B126**

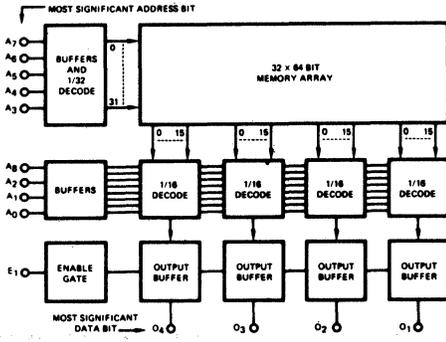


	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	REMARKS
B126a	A6	A5	A4	A3	A0	A1	A2	GND	O4	O3	O2	O1	E1	A8	A7	VCC	ENABLE-E1 LOW
B126b	A6	A5	A4	A3	A0	A1	A2	GND	O5	O4	O3	O2	O1	E1	A7	VCC	ENABLE-E1 LOW
B126c	A6	A5	A4	A3	A0	A1	A2	GND	O4	O3	O2	O1	E1	E2	A7	VCC	ENABLE-E1 LOW and E2 LOW
B126d	O1	O2	O3	O4	O5	O6	O7	GND	O8	A0	A1	A2	A3	A4	E1	VCC	ENABLE-E1 LOW
B126e	A6	A5	A4	A3	A0	A1	A2	GND	O4	O3	O2	O1	CS	A8	A7	VCC	SELECT CS LOW
B126f	A6	A5	A4	A3	A0	A1	A2	GND	O4	O3	O2	O1	A9	A8	A7	VCC	NO SELECT INPUTS
B126g	A6	A5	A4	A3	A0	A1	A2	GND	B4	B3	B2	B1	C11	C12	A7	VCC	
B126h	VCC	A1	A2	A0	A6	A5	A7	VEE	A3	A4	CS	Q2	CE	Q1	Q0	VCC	
B126i	A0	A1	A2	A3	CS0	CS1	CS2	VEE	A4	A5	A6	A7	D	ME	Q	VCC	
B126k	A6	A5	A4	A3	A0	A1	A2	GND	O4	O3	O2	O1	CS1	CS2	A7	VCC	

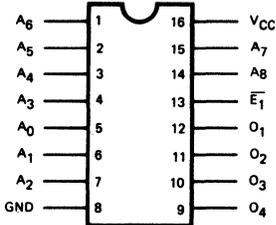
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

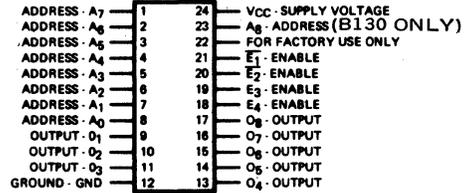
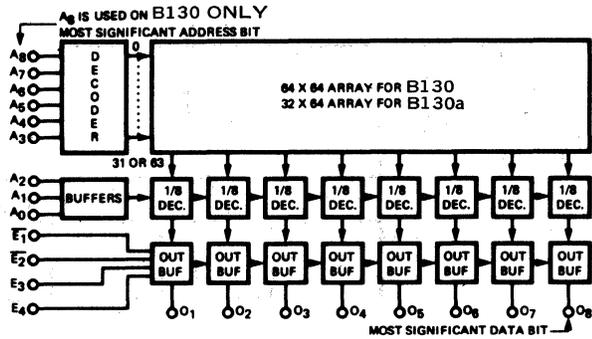
**B129**



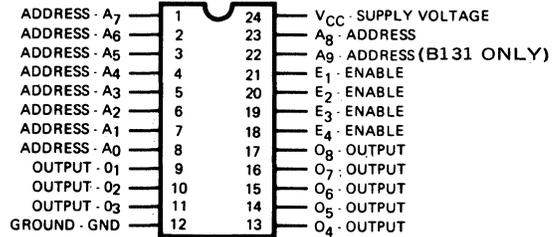
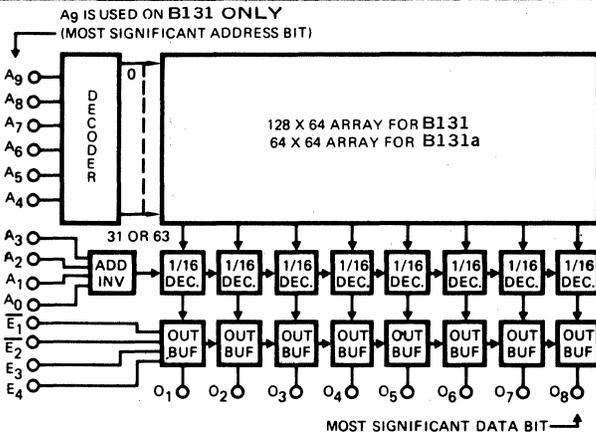
V<sub>CC</sub> = 16  
GND = 8



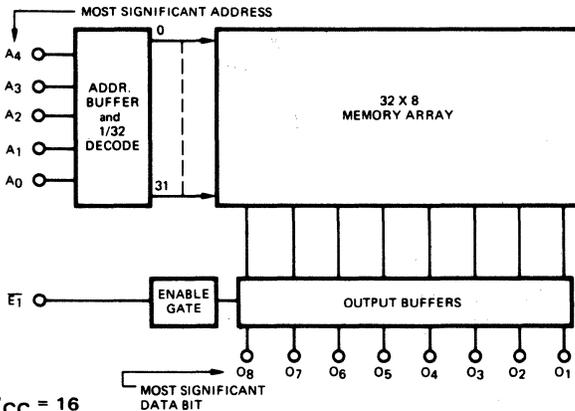
**B130**



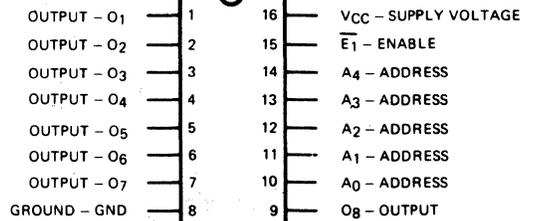
**B131**



**B132**



V<sub>CC</sub> = 16  
GND = 8

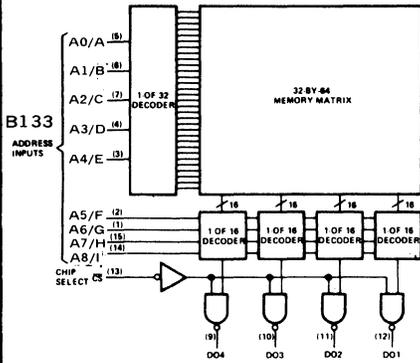


LOW = ENABLE

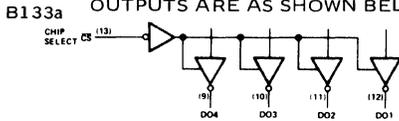
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

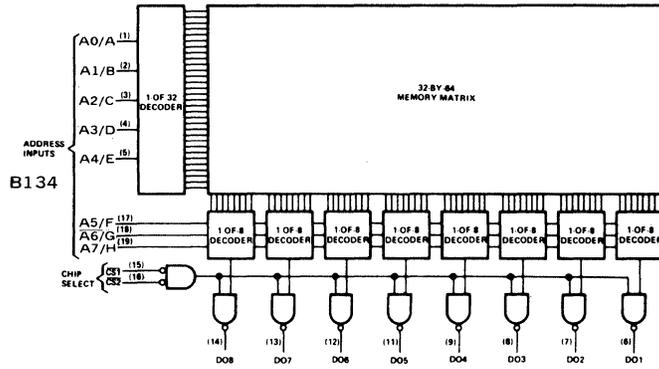
**B133**



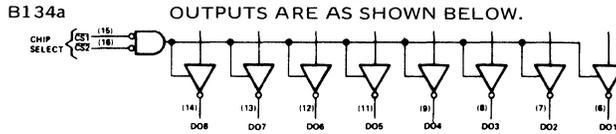
SAME AS ABOVE EXCEPT OUTPUTS ARE AS SHOWN BELOW.



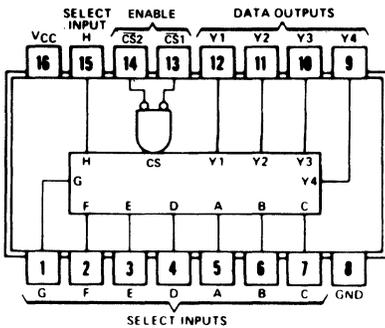
**B134**



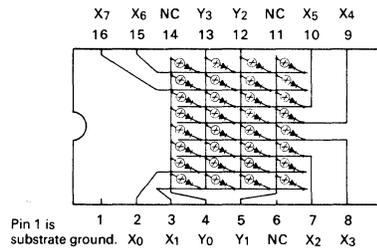
SAME AS ABOVE EXCEPT OUTPUTS ARE AS SHOWN BELOW.



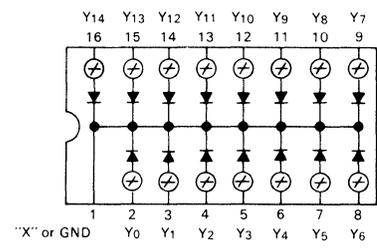
**B135**



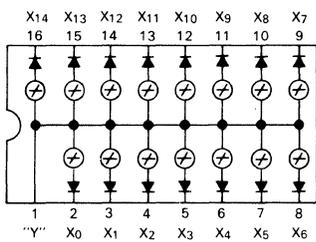
**B137**



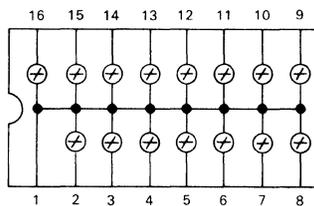
**B138**



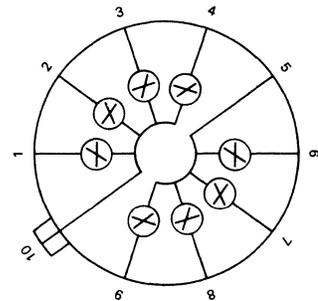
**B139**



**B140**



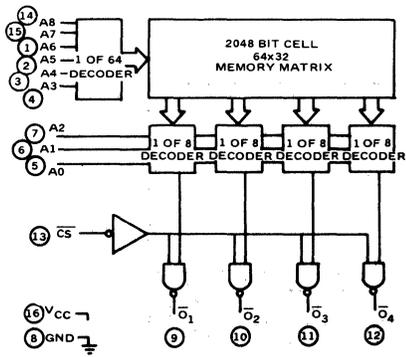
**B141**



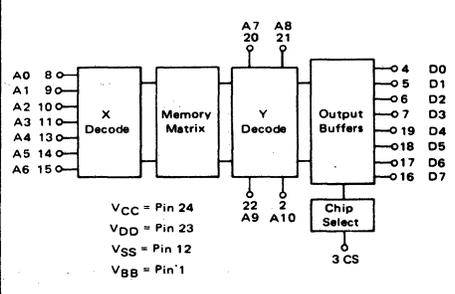
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

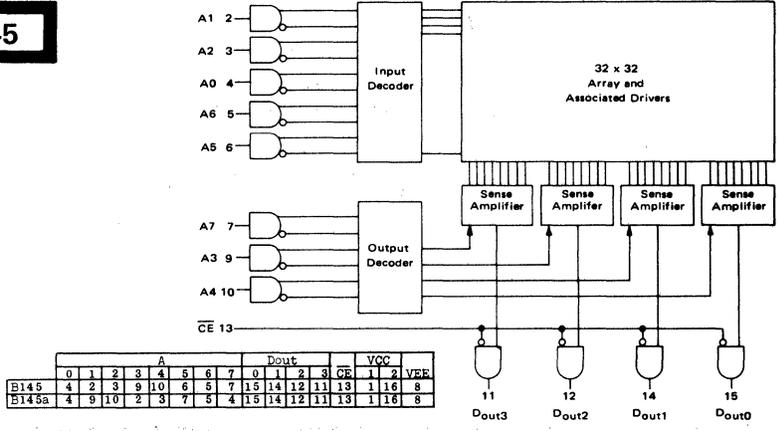
**B143**



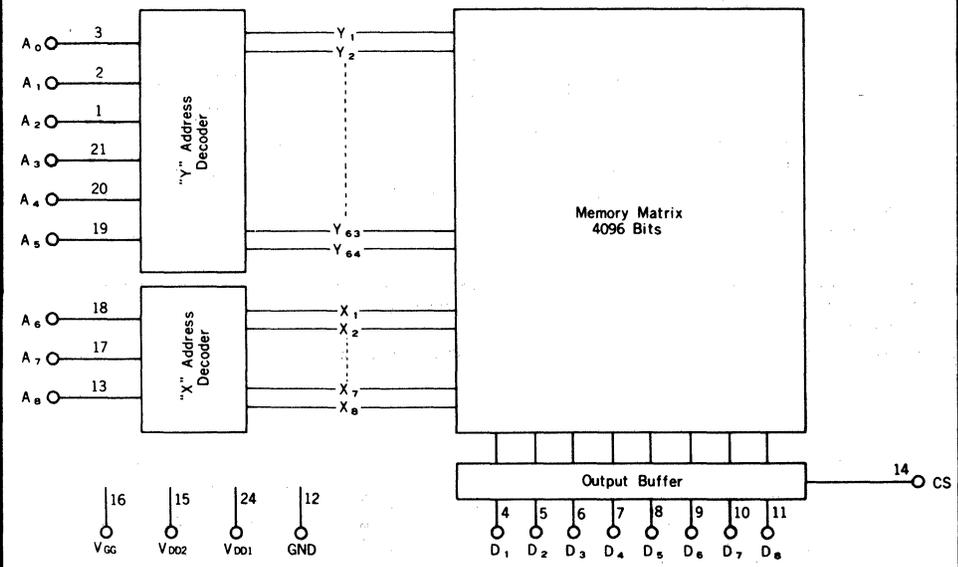
**B144**



**B145**



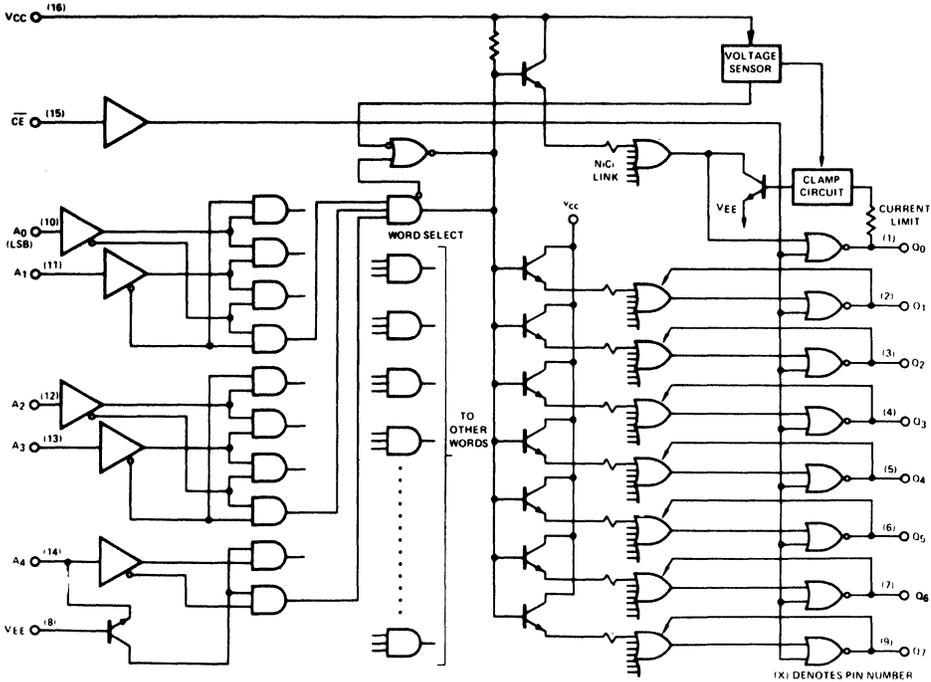
**B148**



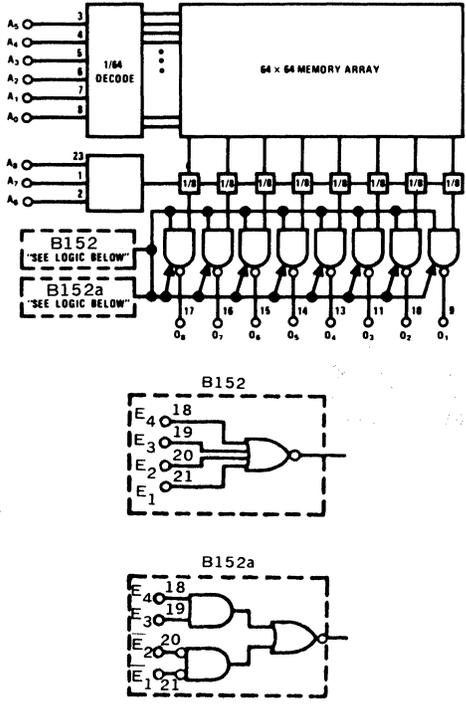
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

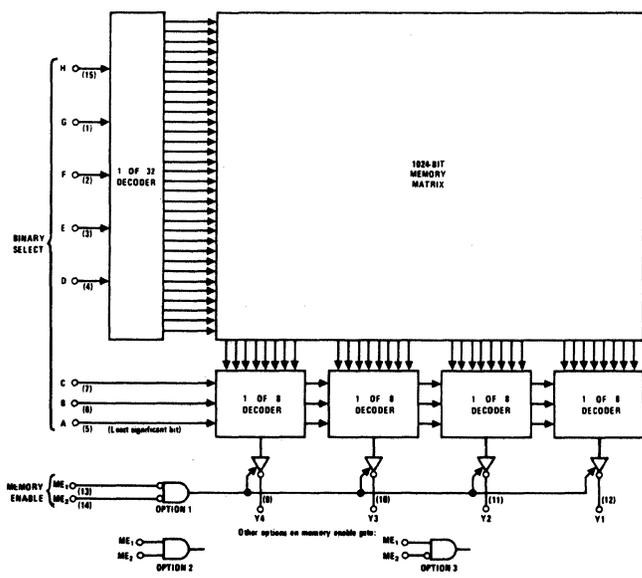
**B149**



**B152**



**B153**



**B154**



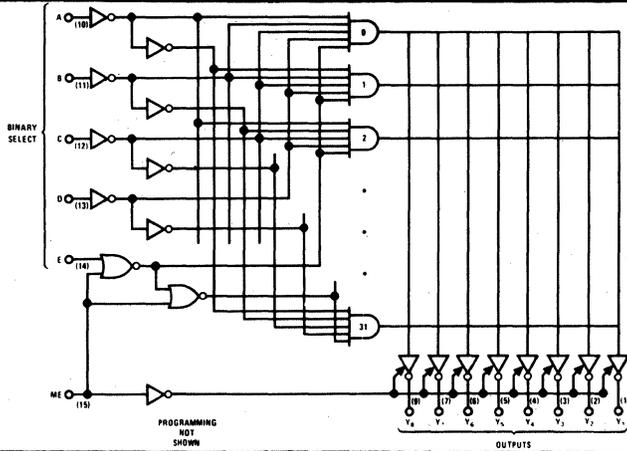
**B155**



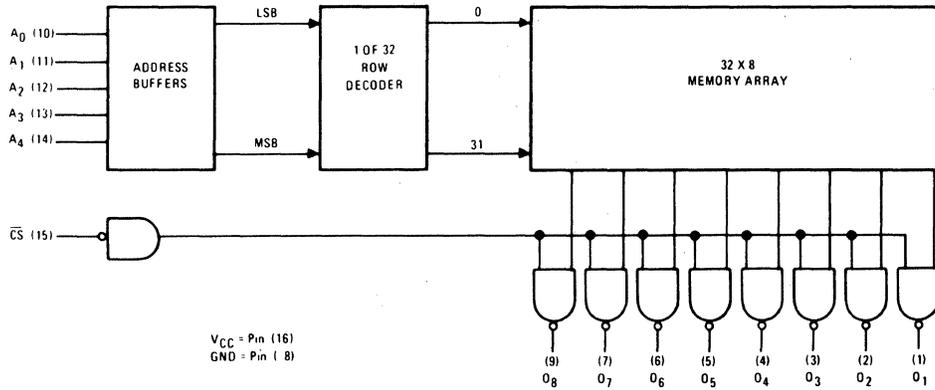
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

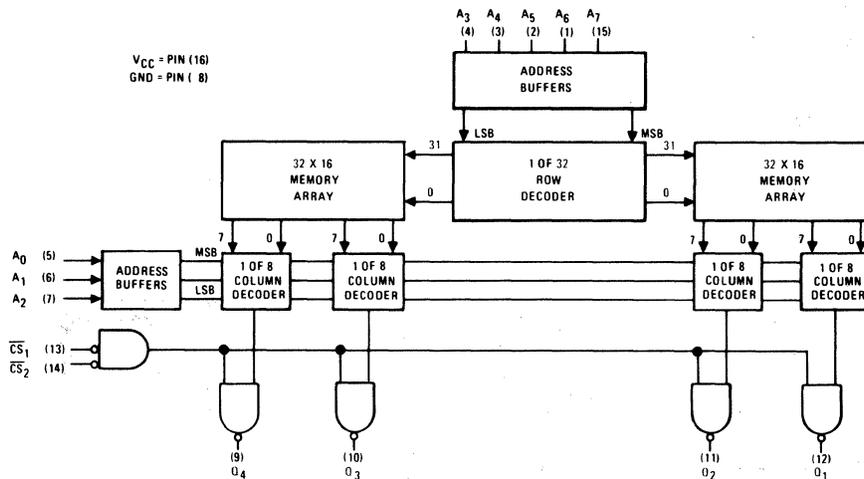
**B154**



**B156**



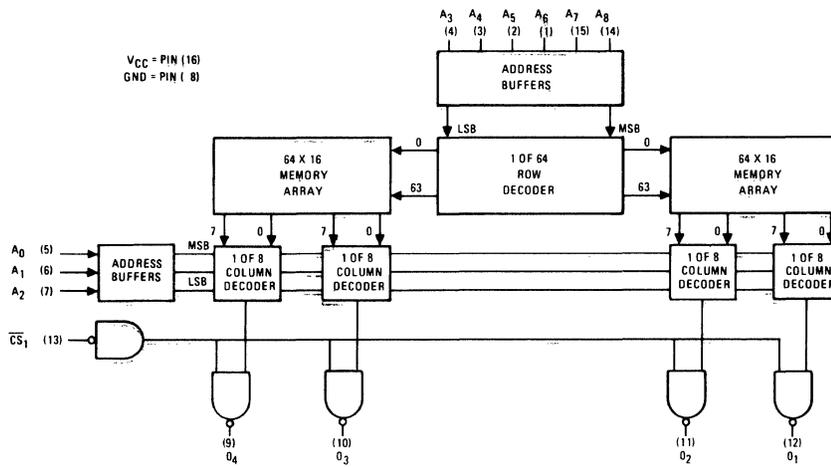
**B157**



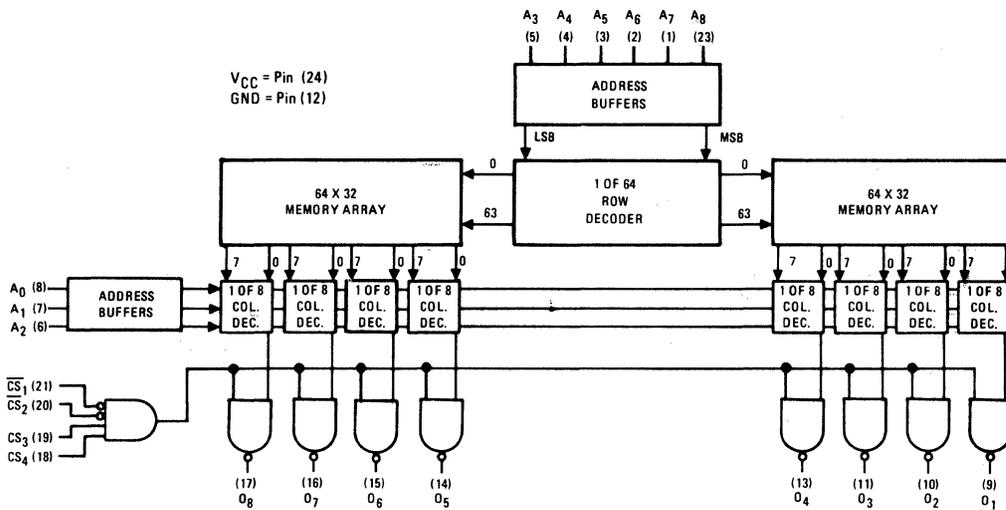
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B158**



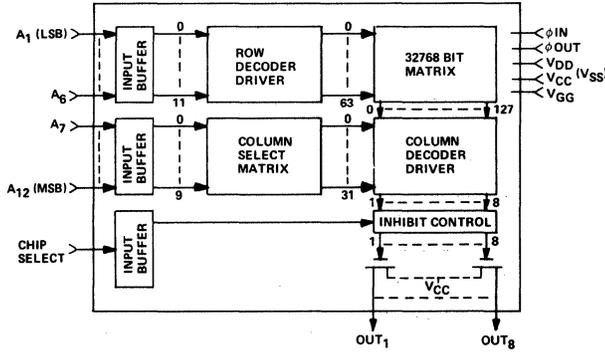
**B159**



# 22. LOGIC/BLOCK DRAWINGS

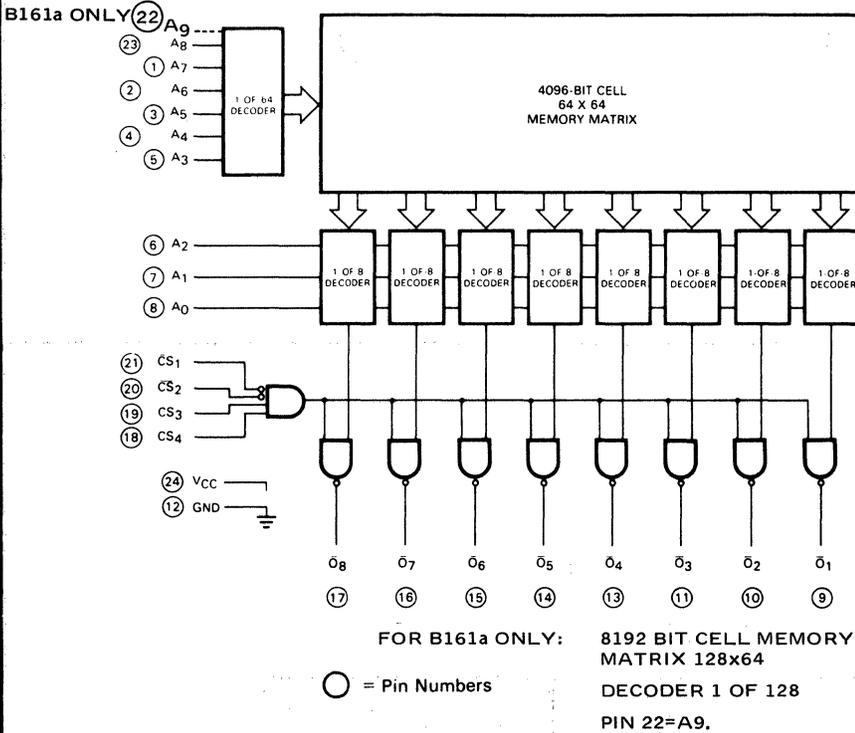
IN DRAWING NUMBER SEQUENCE

## B160

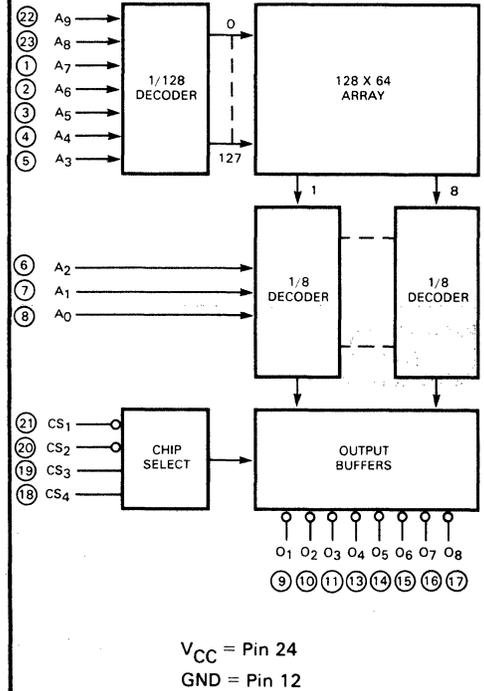


P/N	FUNCTION	P/N	FUNCTION
1	VDD	15	A8
2	A12	16	A9
3	A10	17	A7
4	A1	18	OUT1
5	A2	19	OUT2
6	A3	20	OUT3
7	A4	21	OUT4
8	A5	22	OUT5
9	A6	23	OUT6
10	VSS	24	OUT7
11	QIN	25	OUT8
12	QOUT	26	CHIP SELECT
13	VGG	27	NC
14	A11	28	NC(VSS)

## B161



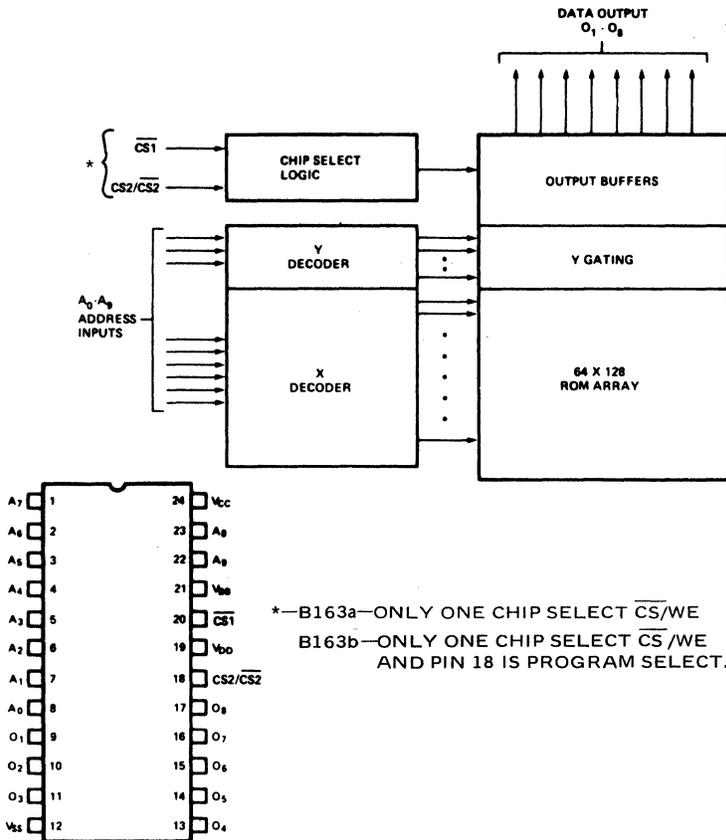
## B162



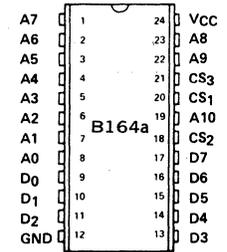
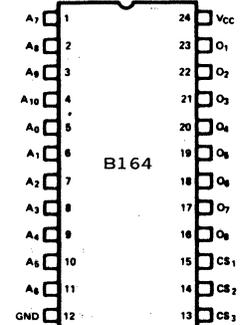
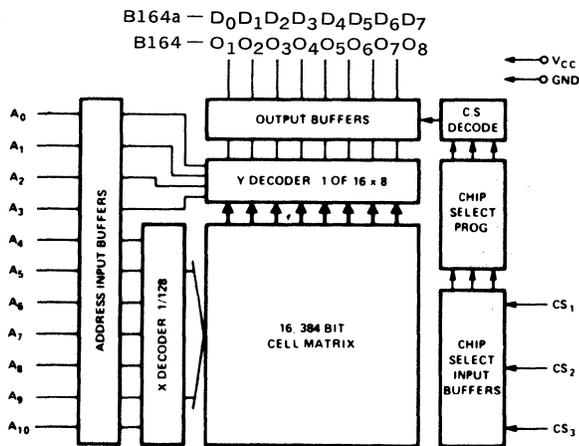
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B163**



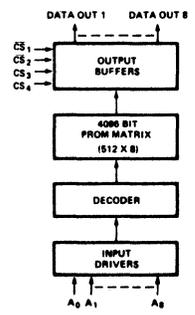
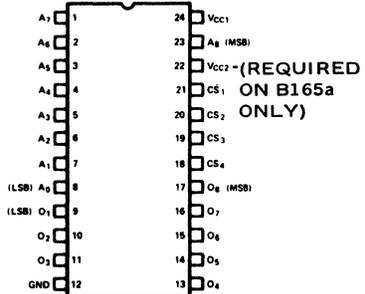
**B164**



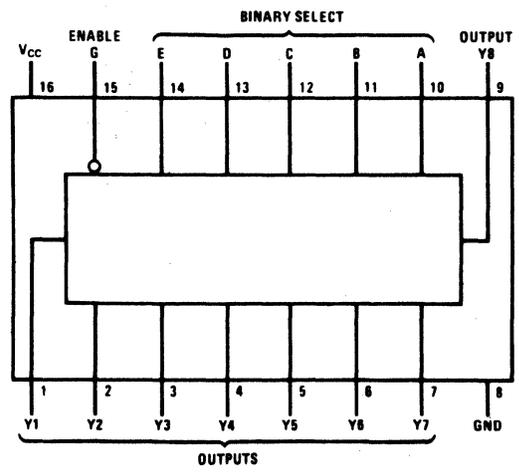
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

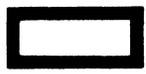
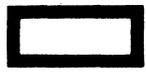
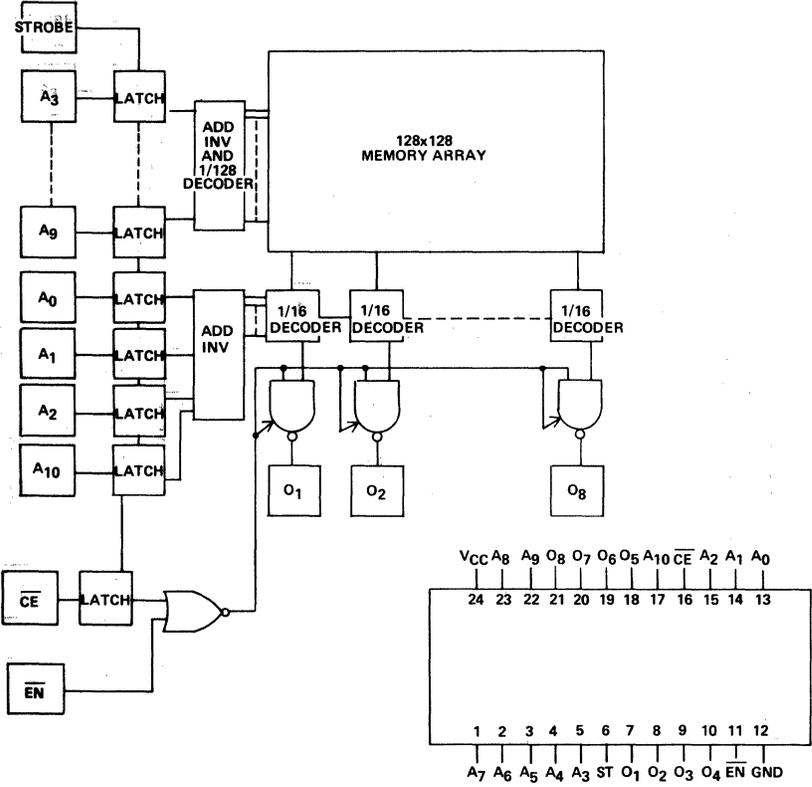
**B165**



**B168**



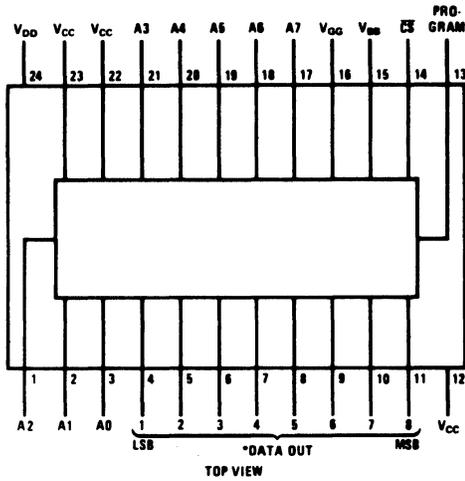
**B169**



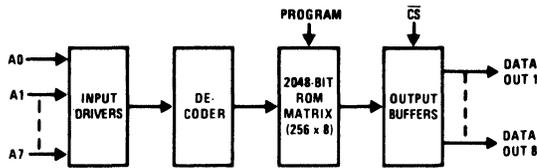
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

B170

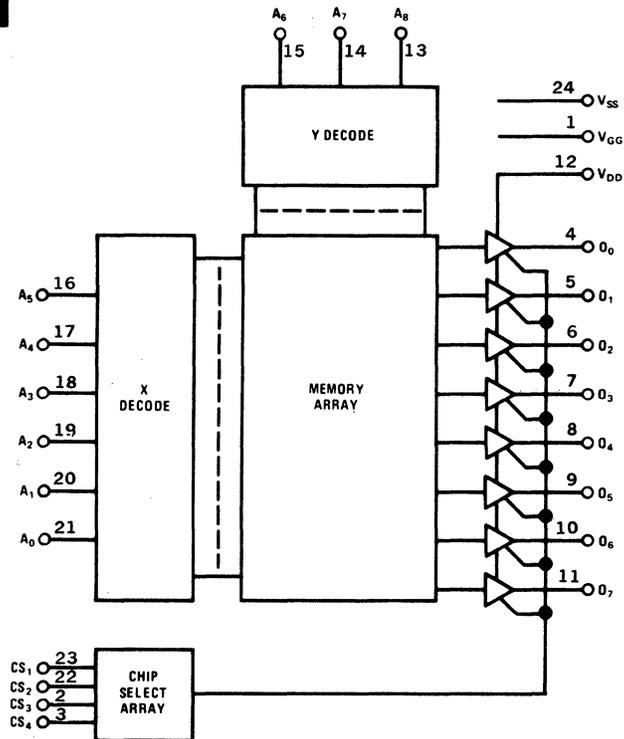


\*This pin is the data input lead during programming.



Note: In the read mode a logic "1" at the address inputs and data outputs is a high and logic "0" is a low.

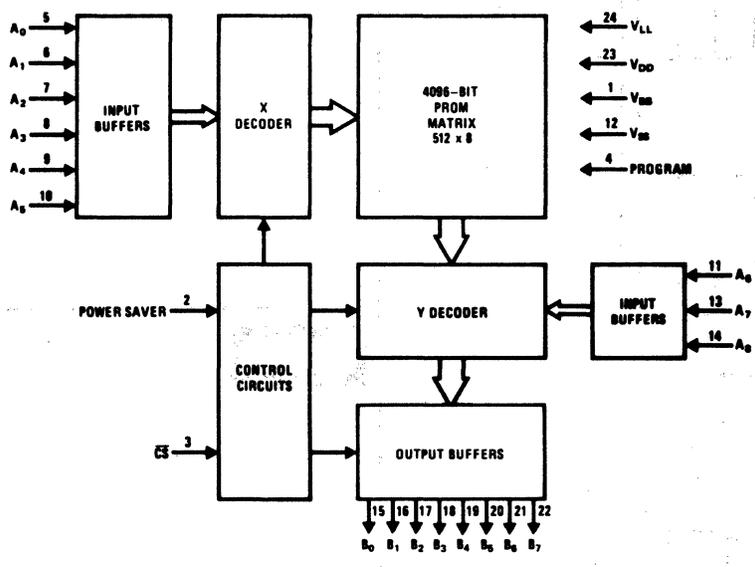
B171



# 22. LOGIC/BLOCK DRAWINGS

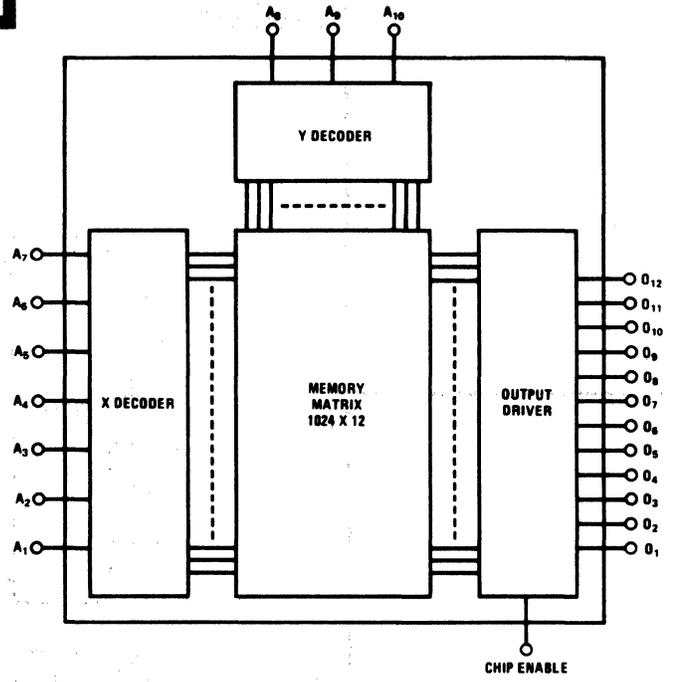
IN DRAWING NUMBER SEQUENCE

**B172**



[ ]

**B174**

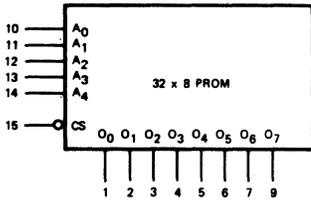
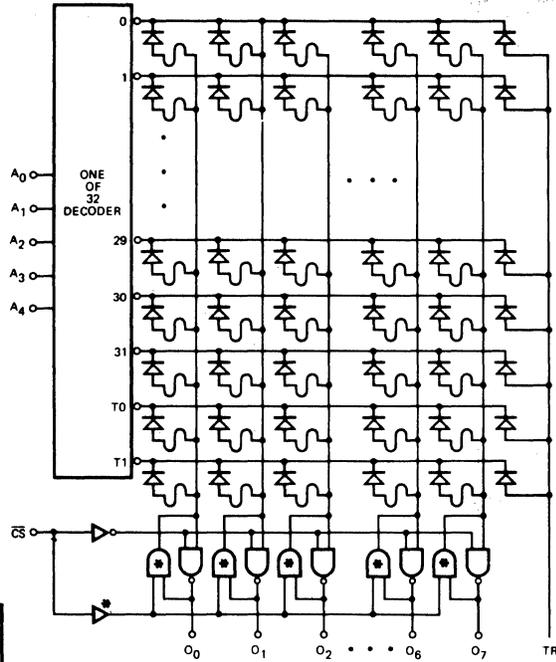


	A										O										CE	VSS	VDD	VGG		
	1	2	3	4	5	6	7	8	9	10	1	2	3	4	5	6	7	8	9	10	11	12	18	1	27	
B174	10	8	7	6	5	4	13	12	11	9	14	15	16	17	19	20	21	22	23	24	25	26	18	1	27	
B174a	10	8	7	6	5	4	13	12	11	9	14	15	16	17	19	20	21	22	23	24	25	26	18	1	27	28

# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

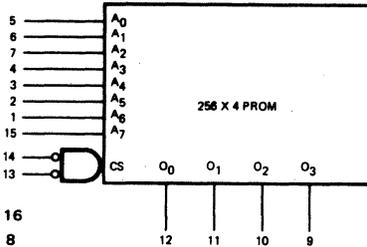
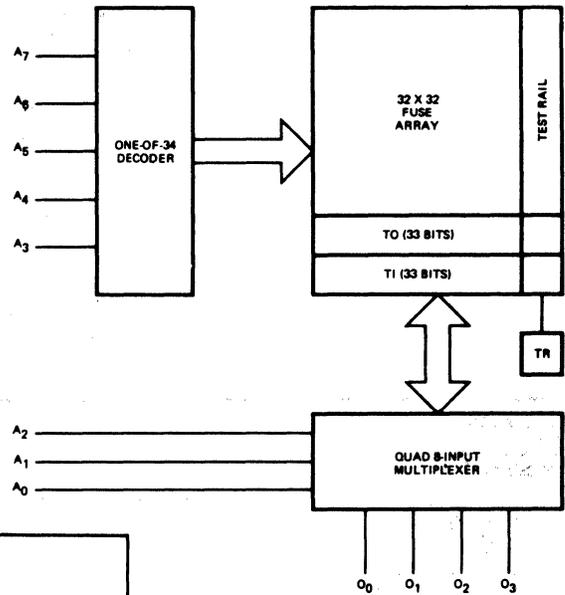
B175



VCC = Pin 16  
GND = Pin 8

\* = High Voltage Gate for Programming  
T0 = Test Word  
T1 = Test Word  
TR = Test Rail

B176



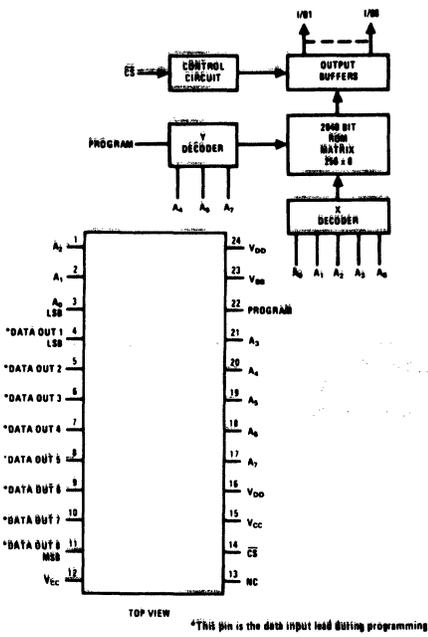
VCC = Pin 16  
GND = Pin 8

T0 = Test Word  
T1 = Test Word  
TR = Test Rail

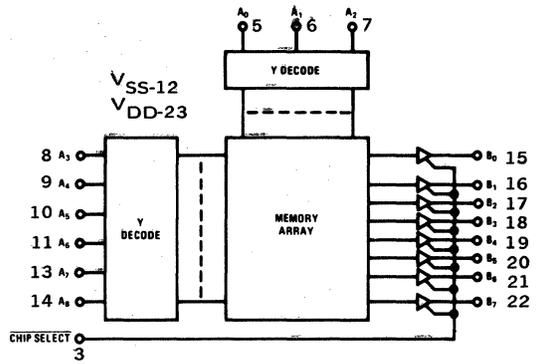
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

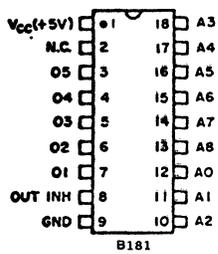
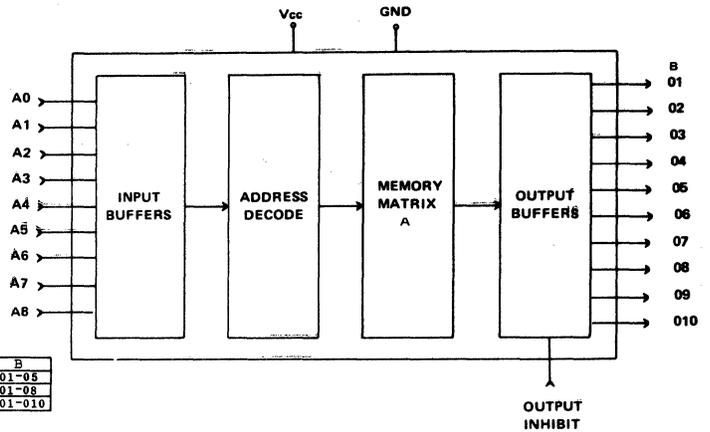
**B177**



**B178**



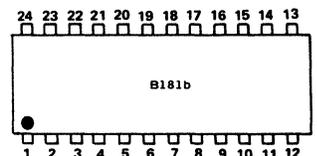
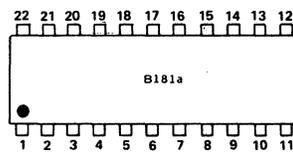
**B181**



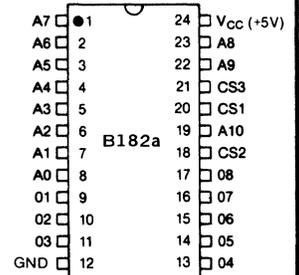
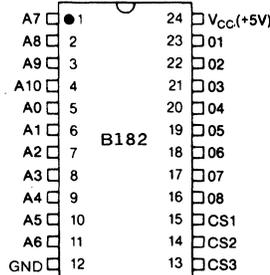
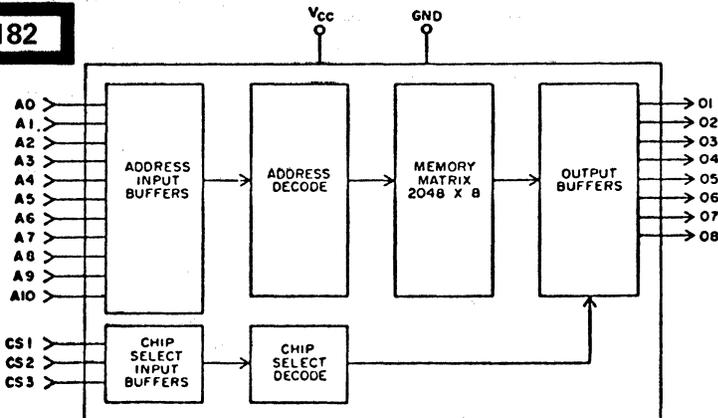
PIN	FUNCTION	PIN	FUNCTION
1	GND	12	Output Inhibit
2	A8	13	N.C.
3	A7	14	N.C.
4	A6	15	Out 8
5	A5	16	Out 7
6	A4	17	Out 6
7	A3	18	Out 5
8	Vcc	19	Out 4
9	A2	20	Out 3
10	A1	21	Out 2
11	A0	22	Out 1

PIN	FUNCTION	PIN	FUNCTION
1	GND	13	Output Inhibit
2	A8	14	Out 10
3	A7	15	Out 9
4	A6	16	Out 8
5	A5	17	Out 7
6	A4	18	Out 6
7	A3	19	Out 5
8	N.C.	20	Out 4
9	Vcc	21	Out 3
10	A2	22	Out 2
11	A1	23	Out 1
12	A0	24	N.C.

A0 - A8 ADDRESS INPUTS  
 O1 - O8 DATA OUTPUTS  
 OUT INH OUTPUT INHIBIT  
 N.C. NO CONNECTION



**B182**

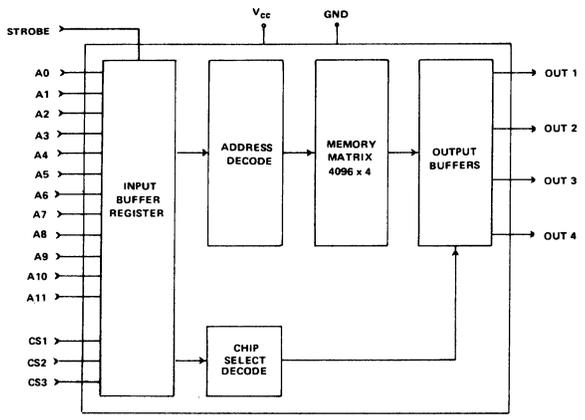


# 22. LOGIC/BLOCK DRAWINGS

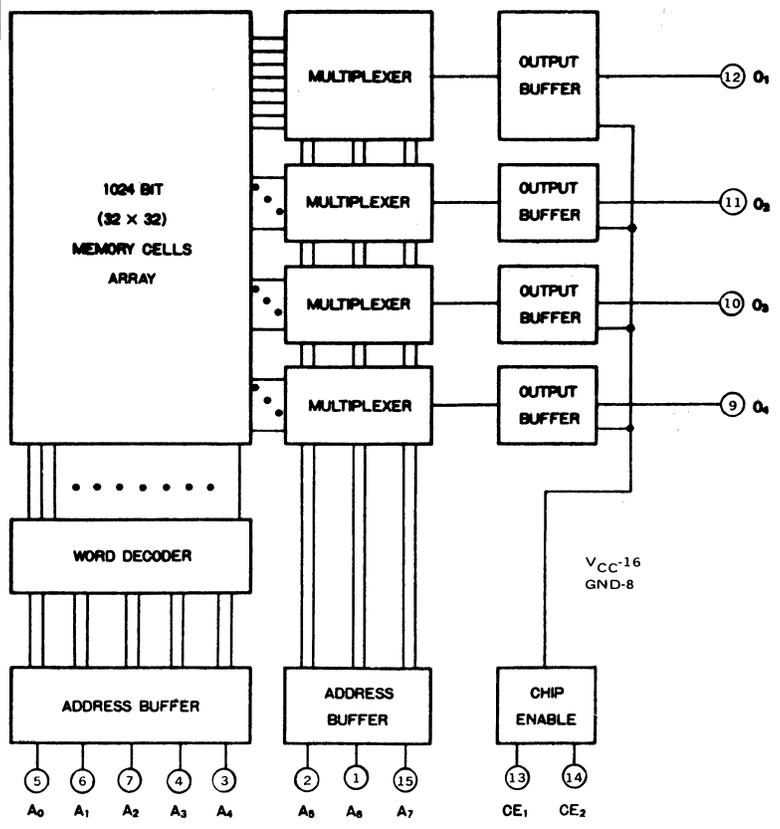
IN DRAWING NUMBER SEQUENCE

**B183**

PIN	FUNCTION	PIN	FUNCTION
1	V <sub>cc</sub>	13	A <sub>9</sub>
2	Address/CS Latch	14	A <sub>10</sub>
3	N.C.	15	N.C.
4	A <sub>0</sub>	16	Out 4
5	A <sub>1</sub>	17	Out 3
6	A <sub>2</sub>	18	Out 2
7	A <sub>3</sub>	19	Out 1
8	A <sub>4</sub>	20	A <sub>11</sub>
9	A <sub>5</sub>	21	CS <sub>3</sub>
10	A <sub>6</sub>	22	CS <sub>2</sub>
11	A <sub>7</sub>	23	CS <sub>1</sub>
12	A <sub>8</sub>	24	GND



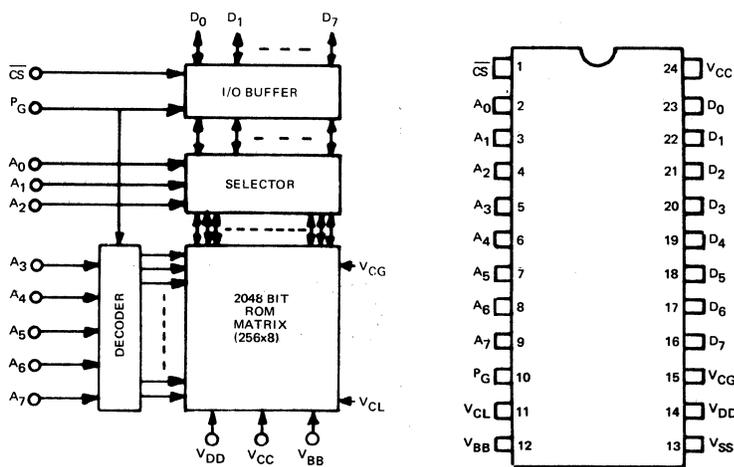
**B184**



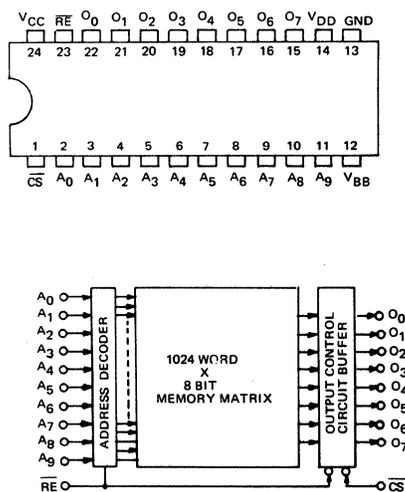
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

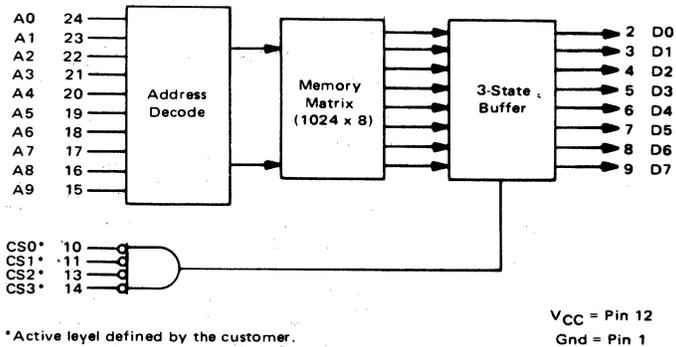
**B185**



**B186**



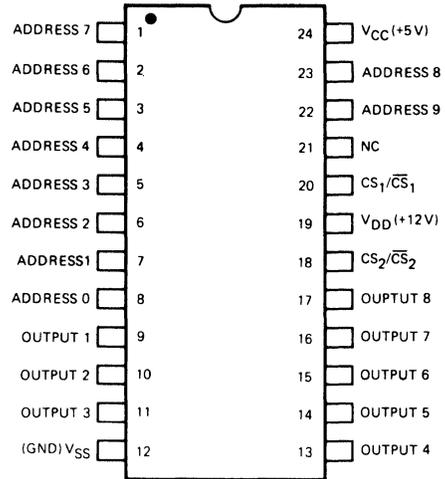
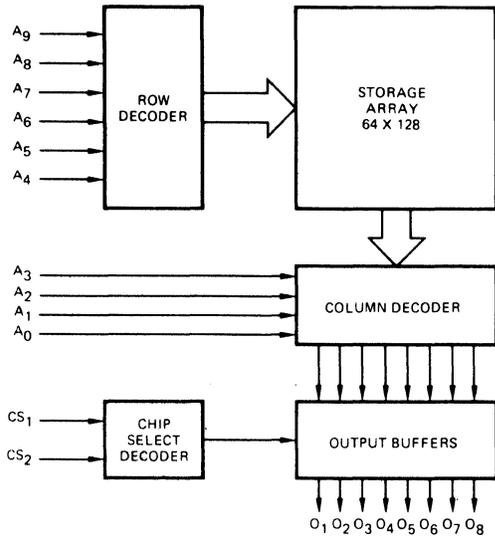
**B187**



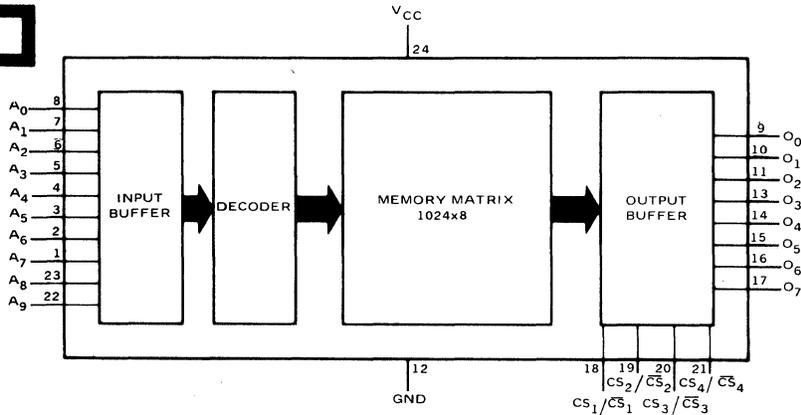
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

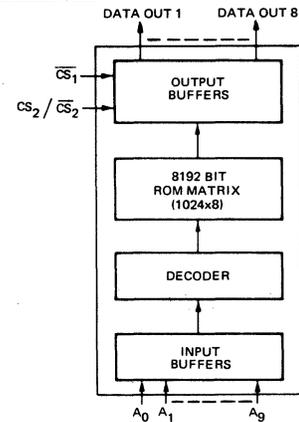
**B188**



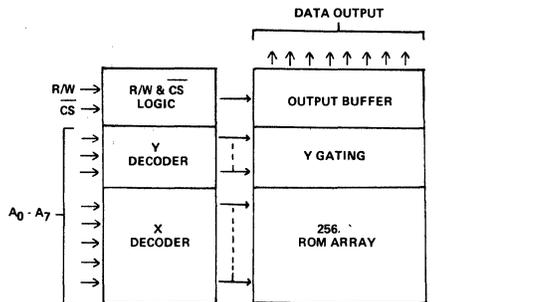
**B189**



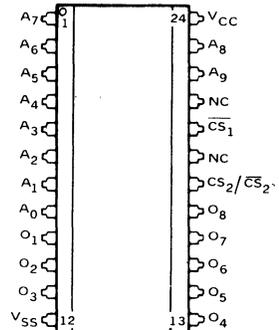
**B190**



**B191**



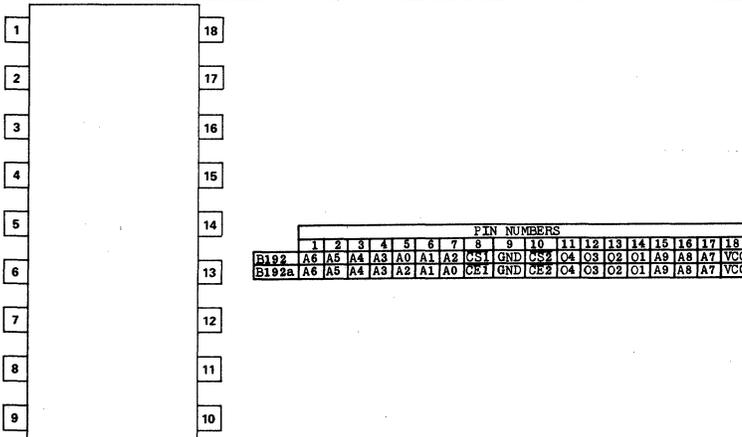
DATA OUTPUT		A																VDD	VCC	VGG	VBB	NC		
1	2	3	4	5	6	7	8	CS	RW	PR	0	1	2	3	4	5	6	7	24	12	16	15	23	
B191	4	5	6	7	8	9	10	11	14	22	13	3	2	1	21	20	19	18	17	24	12	16	15	23



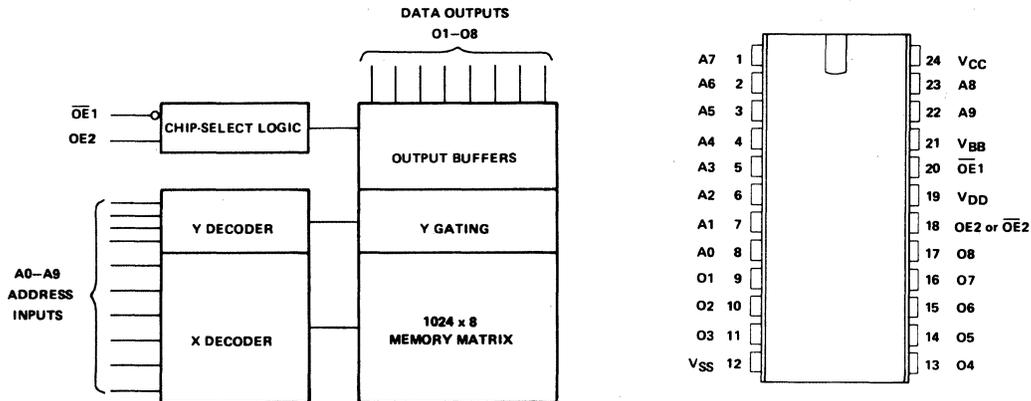
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

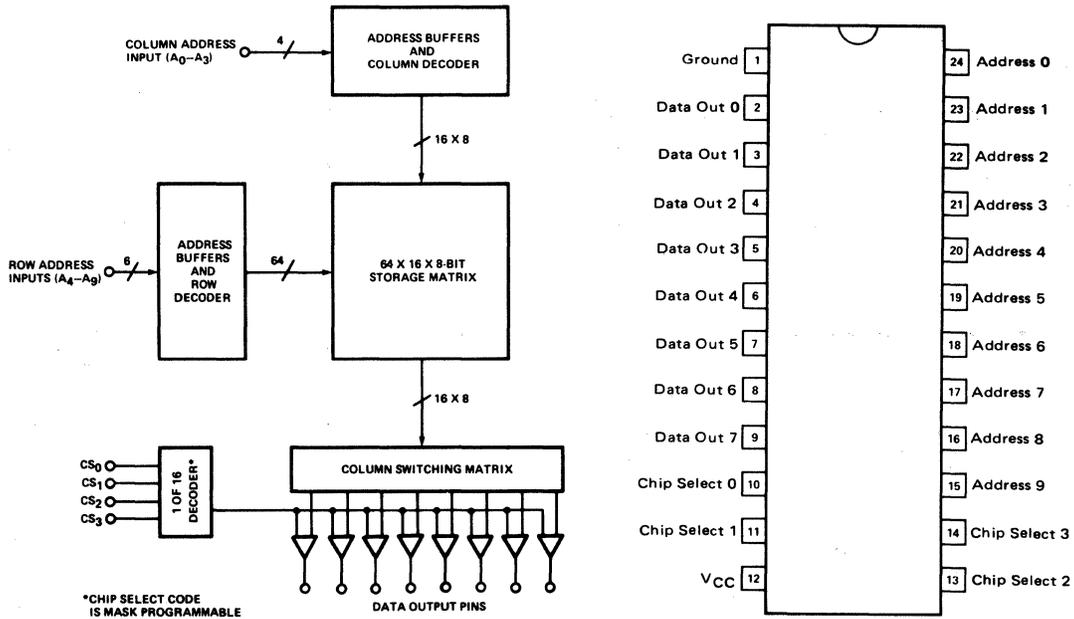
**B192**



**B194**



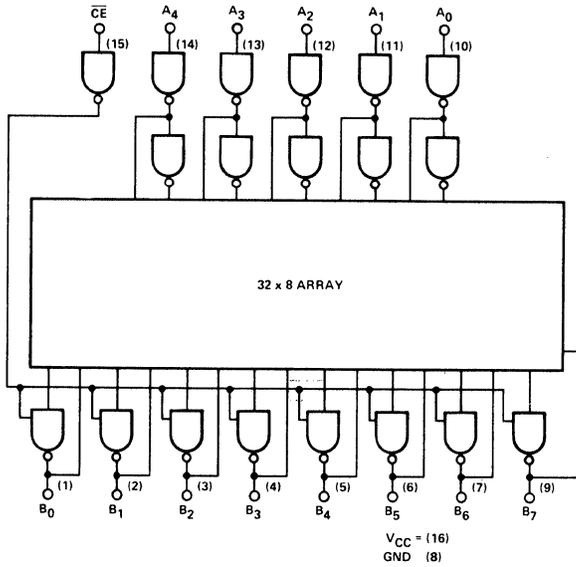
**B195**



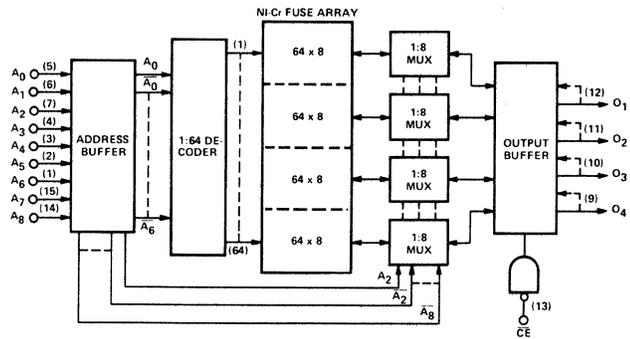
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

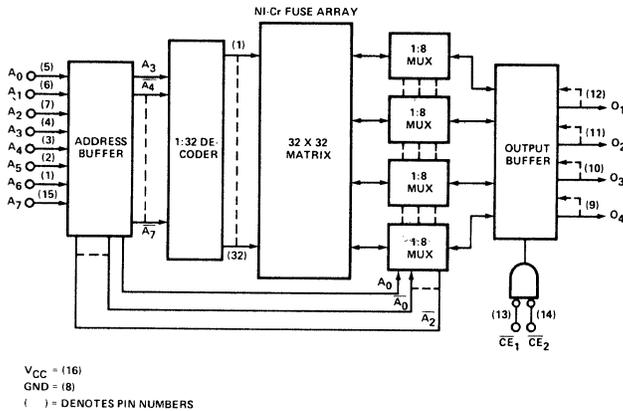
**B196**



**B197**



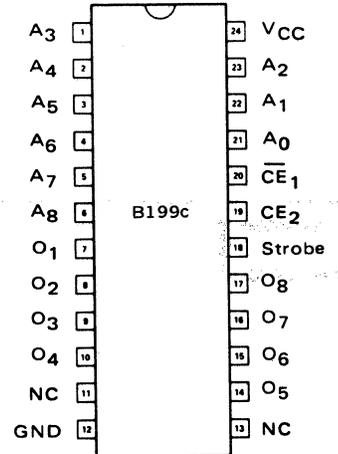
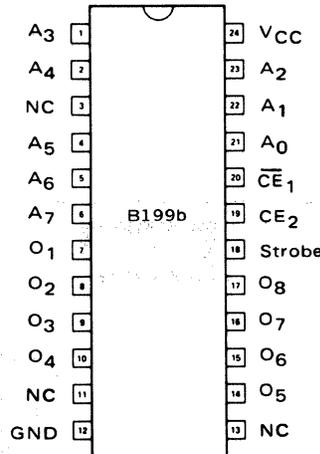
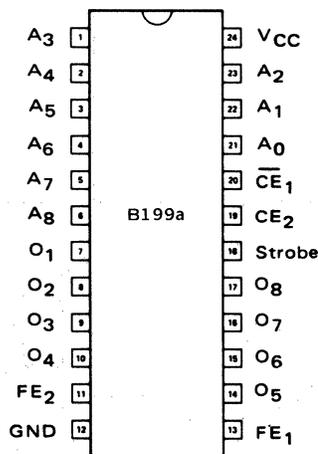
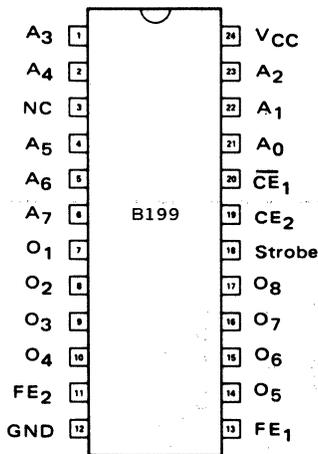
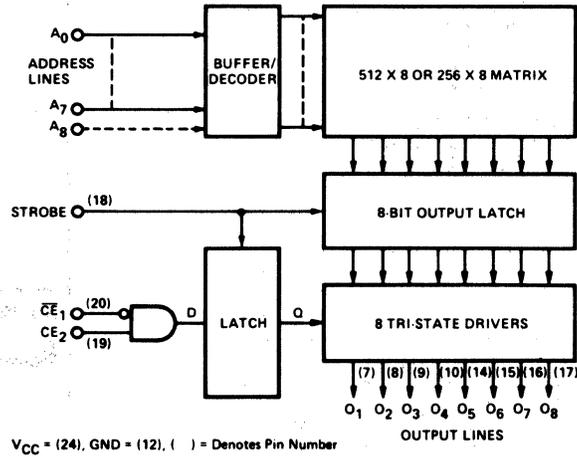
**B198**



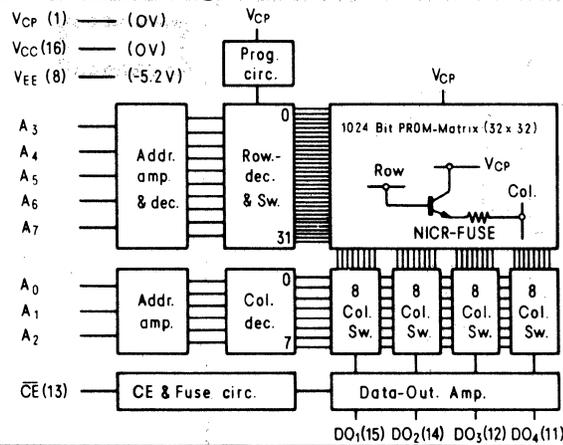
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B199**



**B200**



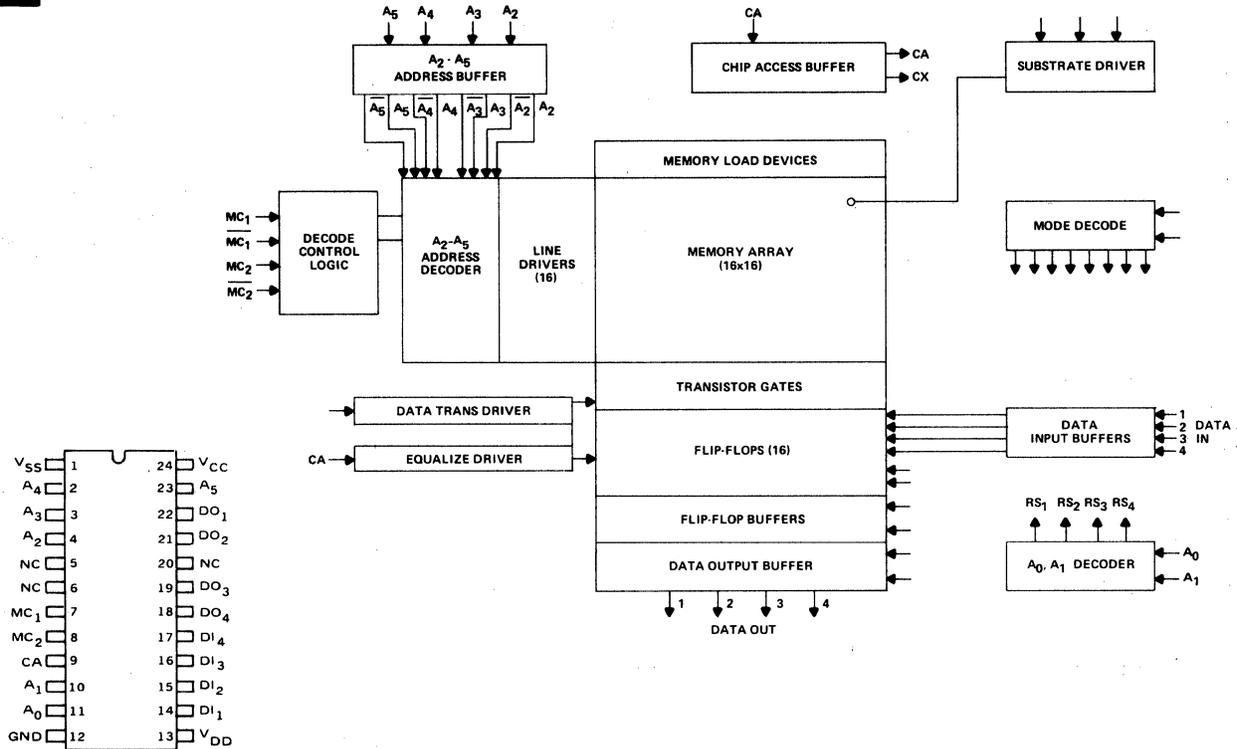
	A0	A1	A2	A3	A4	A5	A6	A7
B200	4	2	3	9	10	6	5	7
B200a	6	7	9	2	3	4	5	10

D.A.T.A.

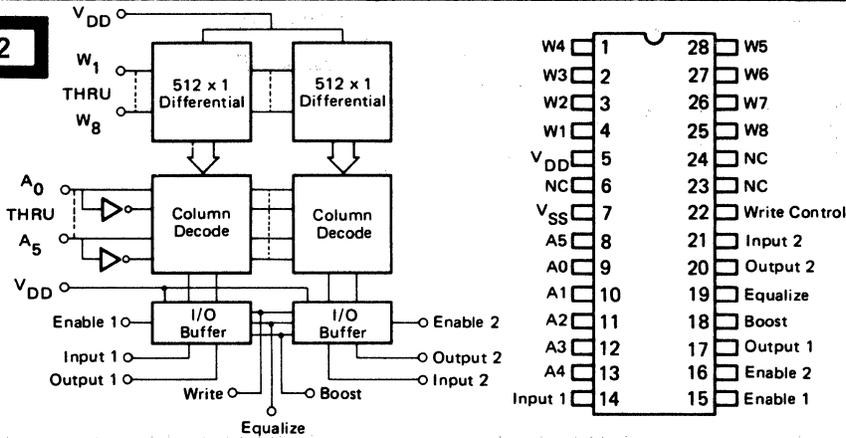
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

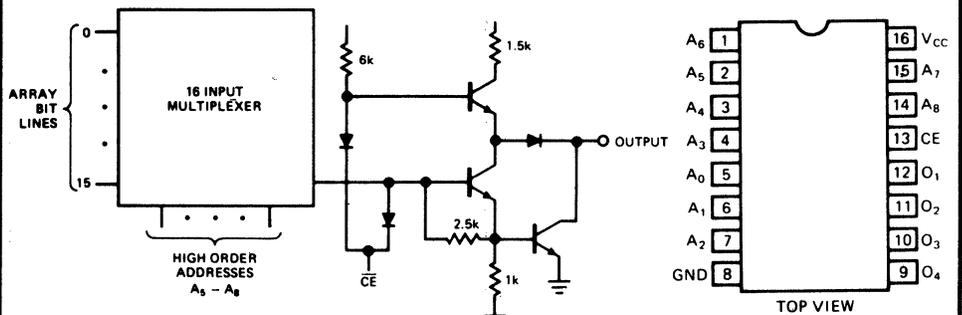
**B201**



**B202**



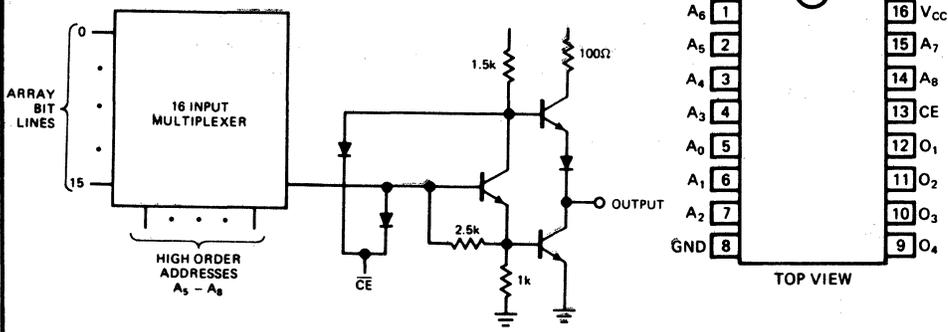
**B203**



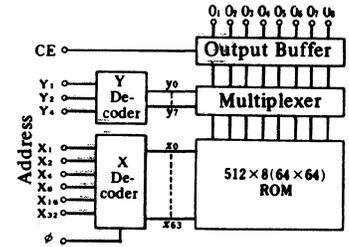
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

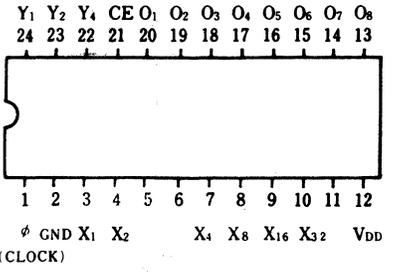
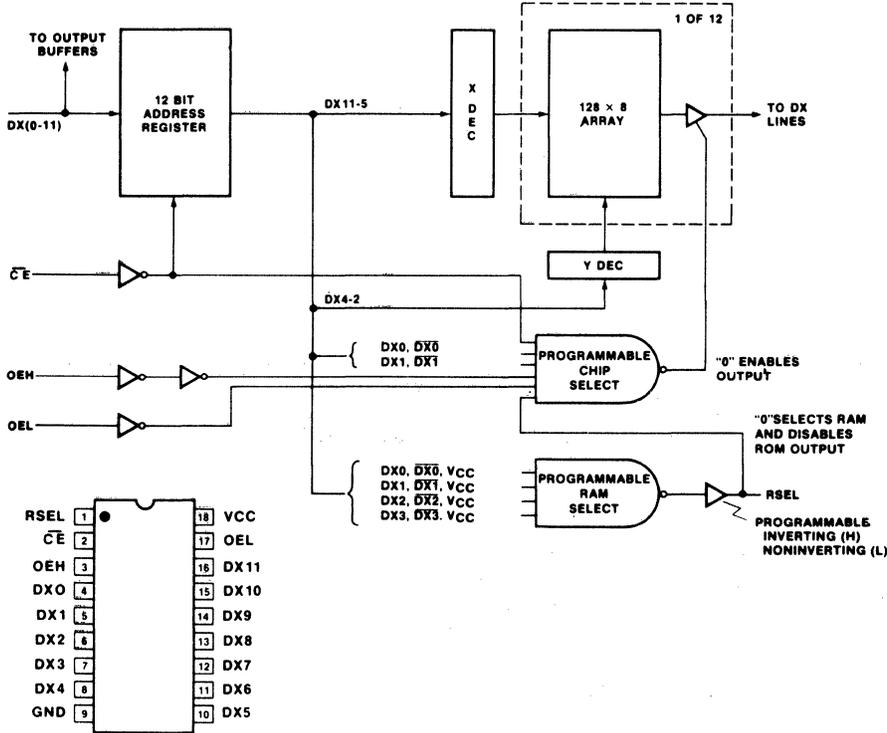
**B204**



**B205**



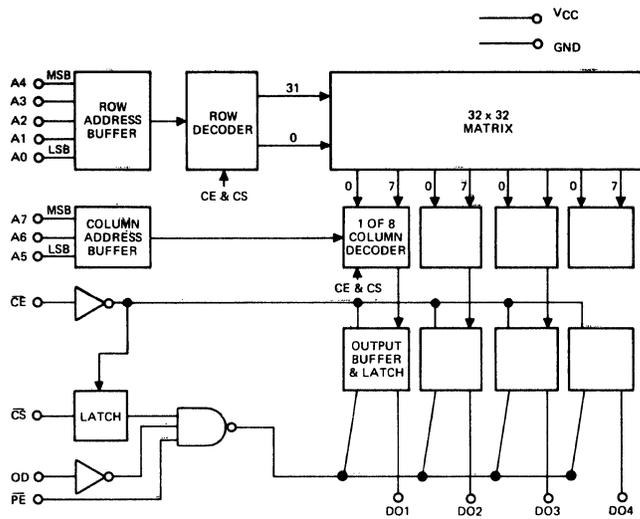
**B206**



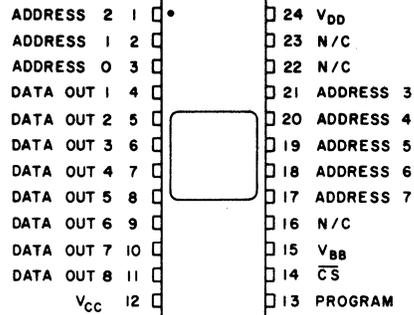
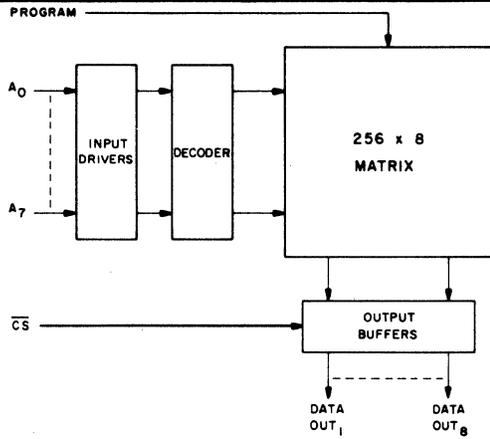
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

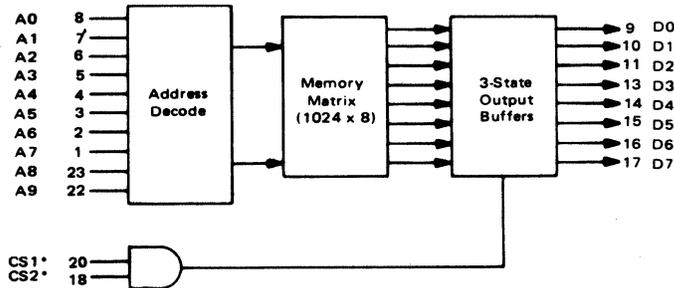
**B207**



**B208**



**B209**



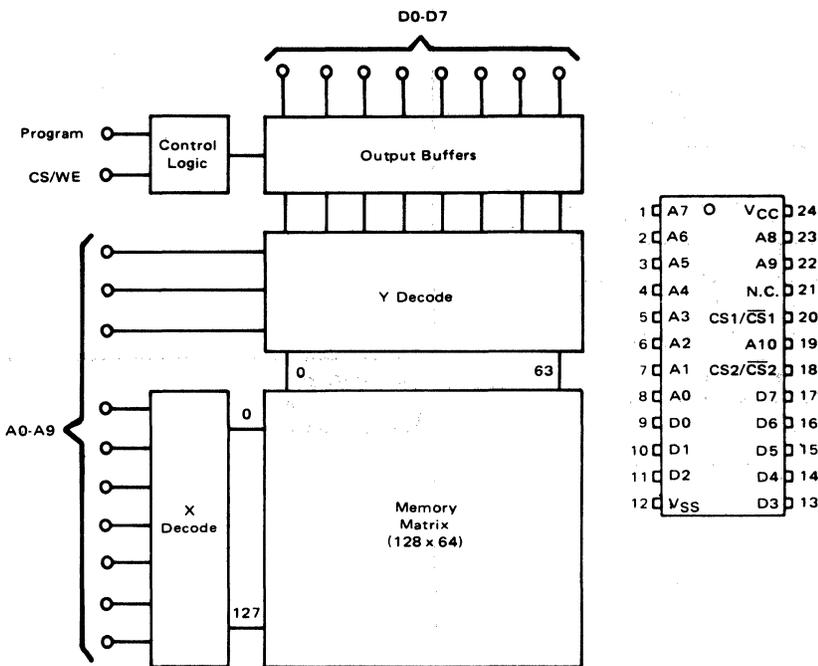
\* Active level defined by the customer.

V<sub>CC</sub> = Pin 24  
Gnd = Pin 12

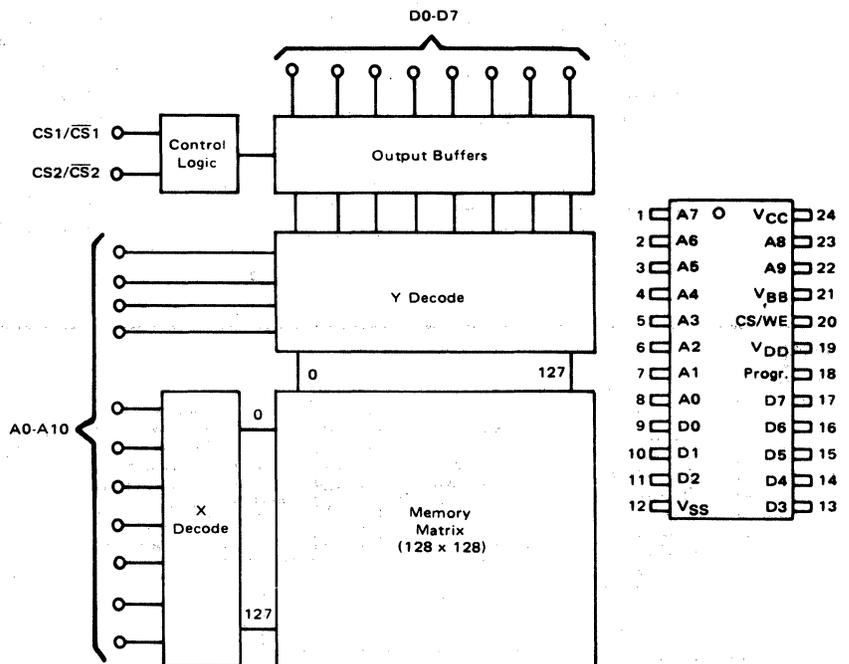
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B210**



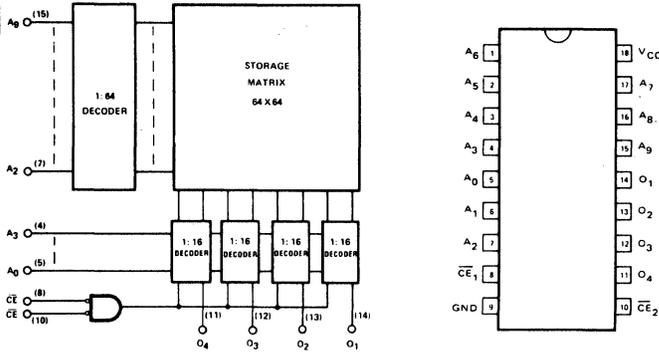
**B211**



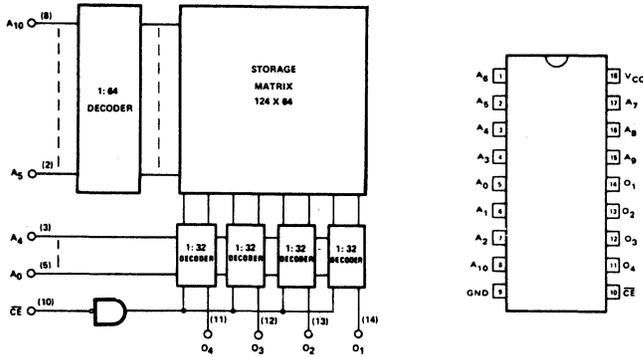
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

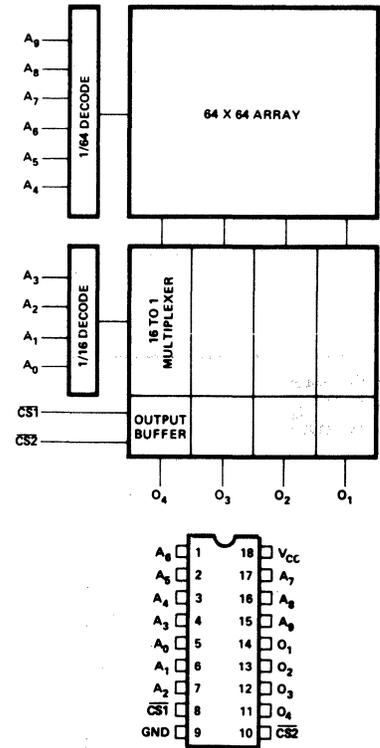
**B212**



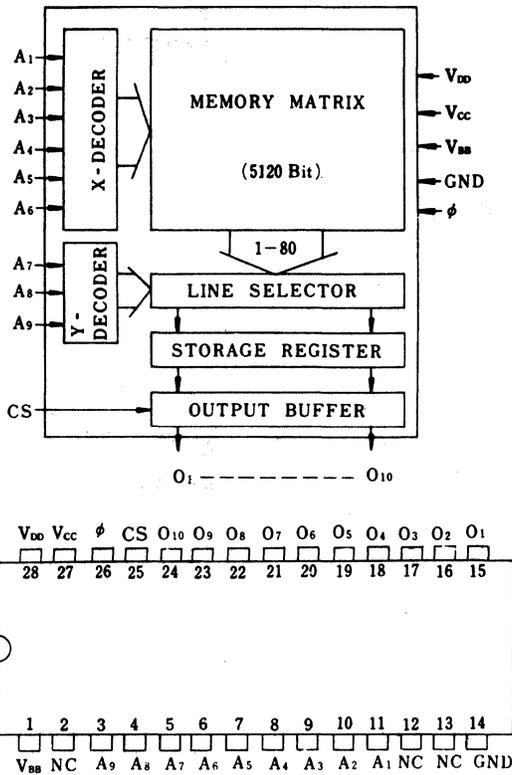
**B213**



**B214**



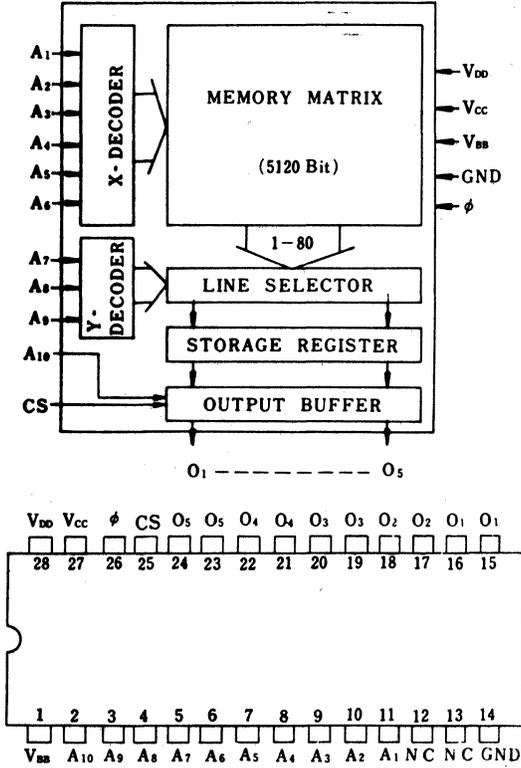
**B215**



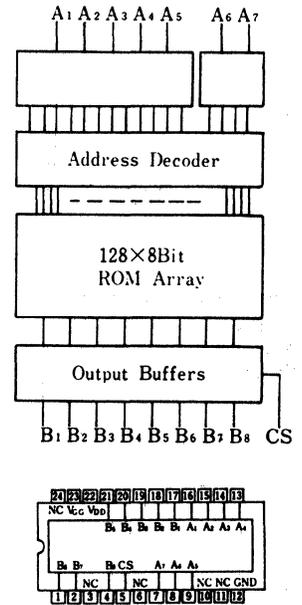
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

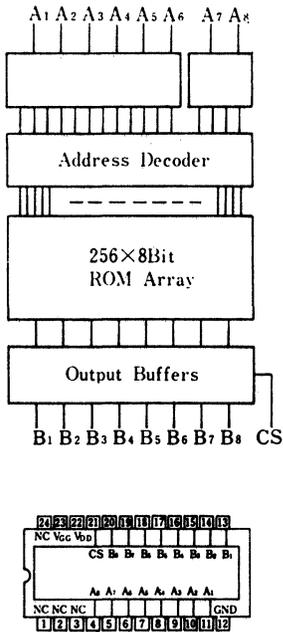
**B216**



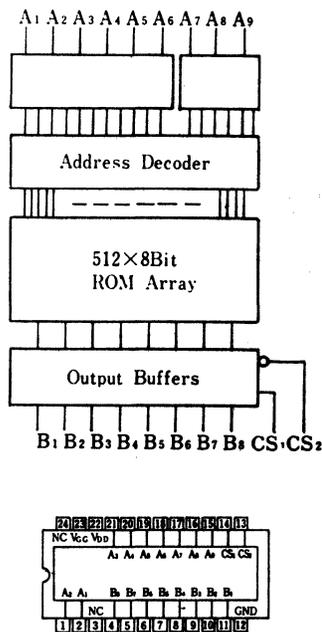
**B217**



**B218**



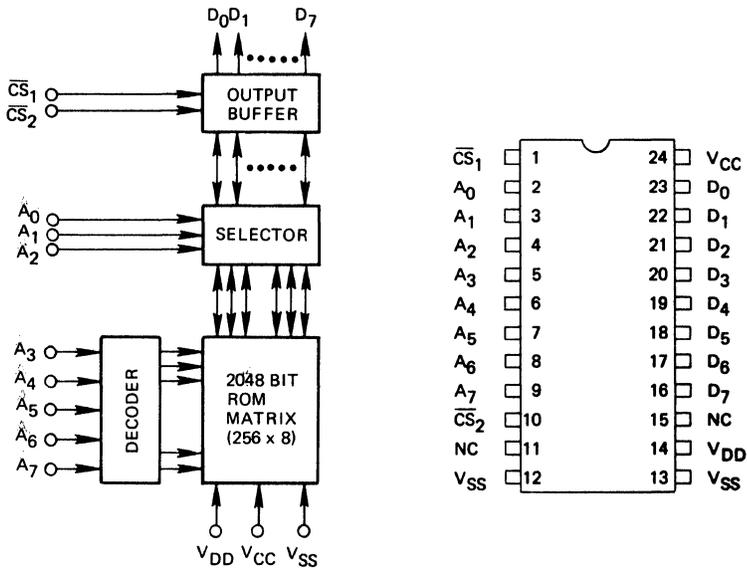
**B219**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**B220**

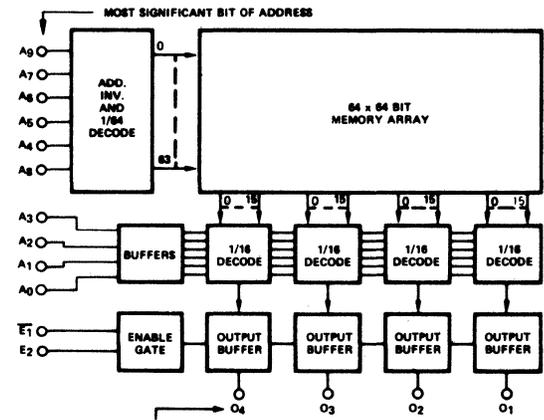


[ ]

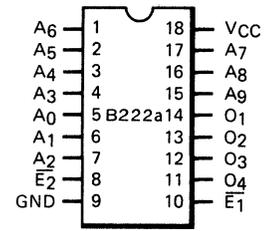
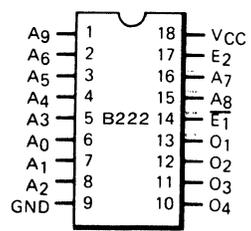
[ ]

[ ]

**B222**



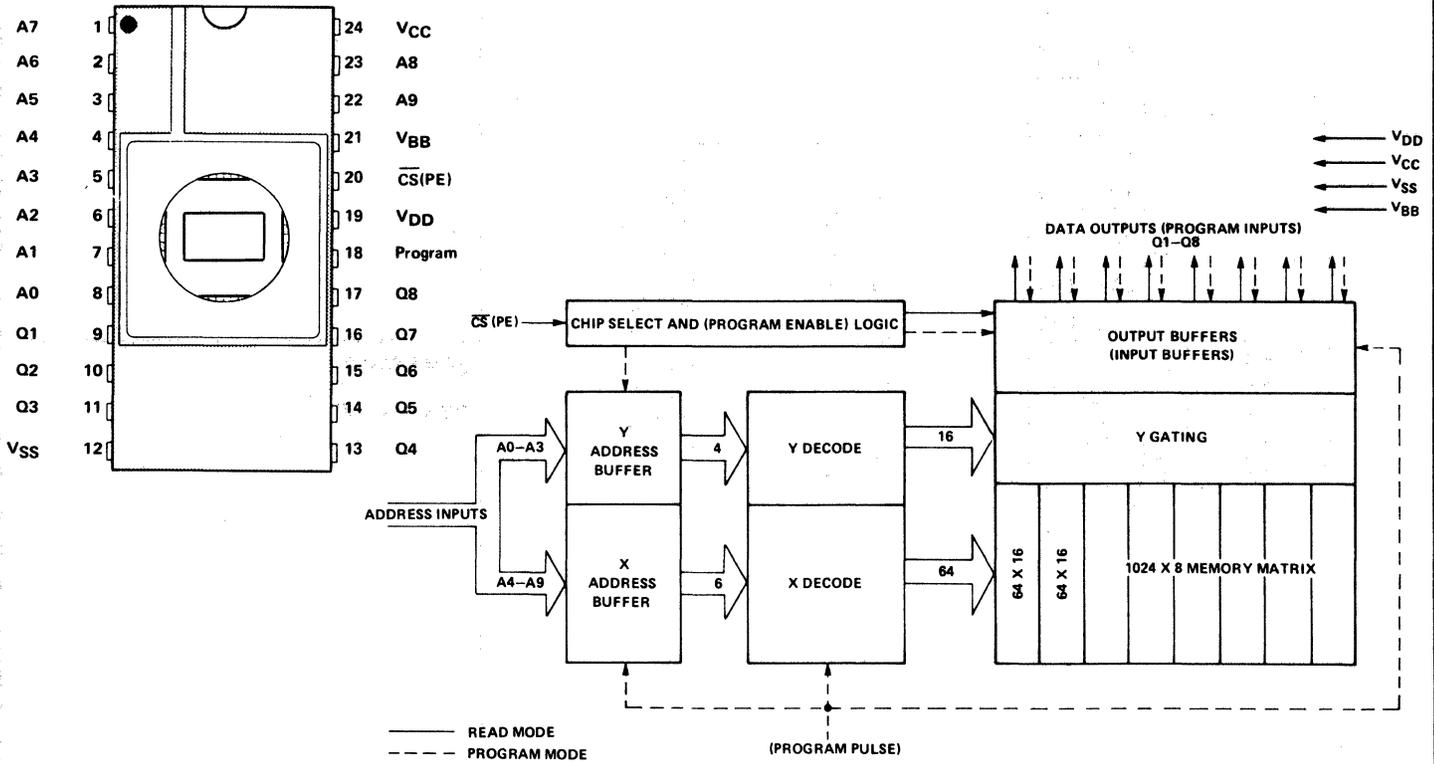
VCC = 18  
GND = 9



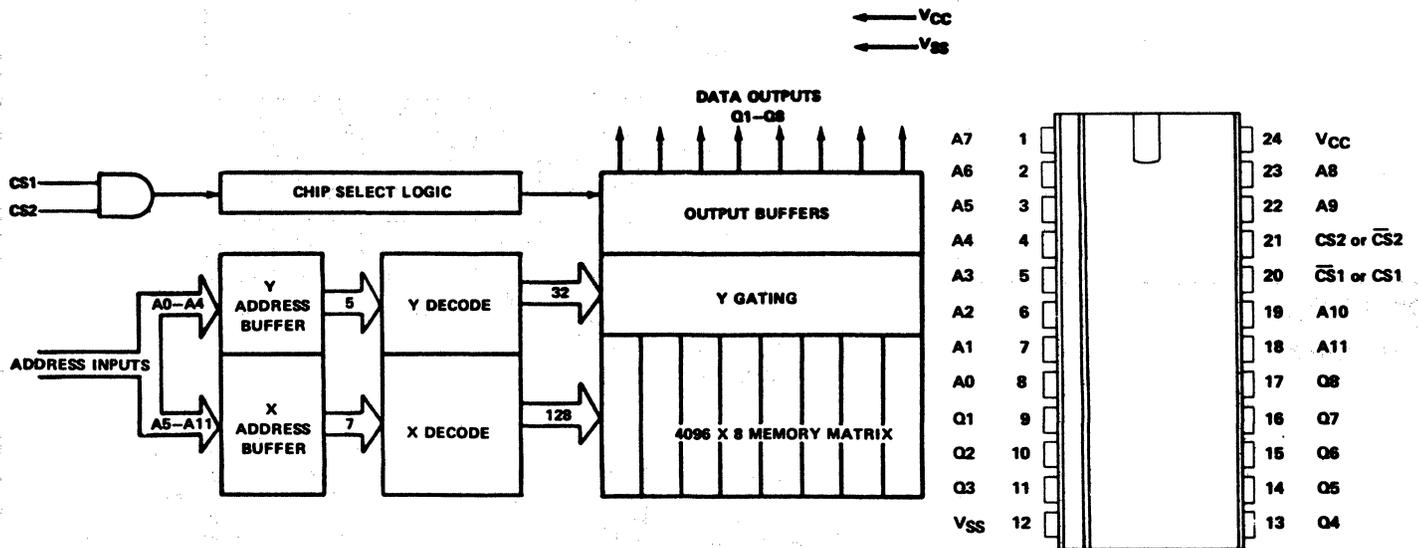
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**B225**



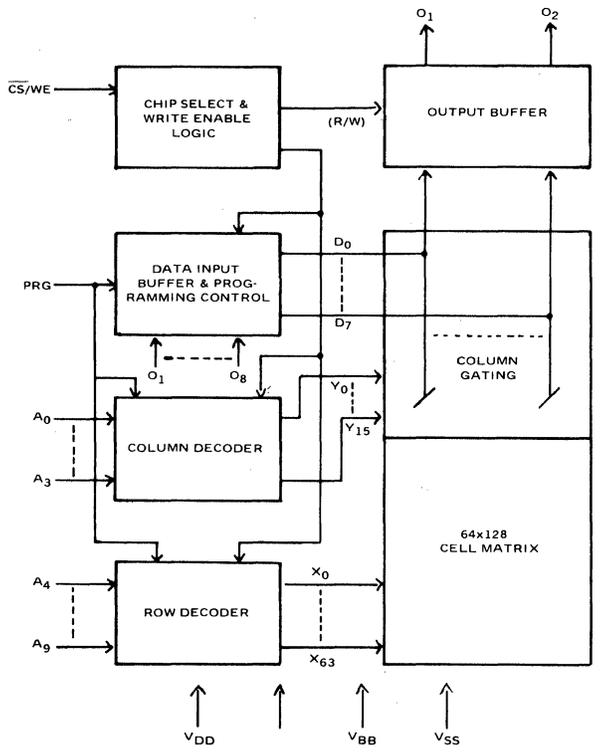
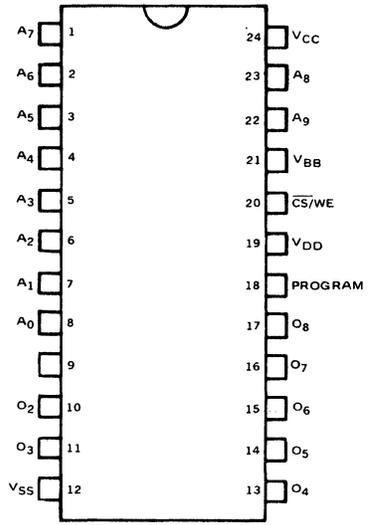
**B226**



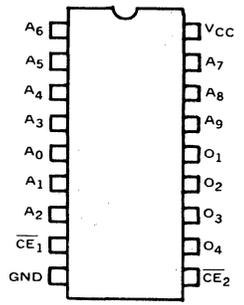
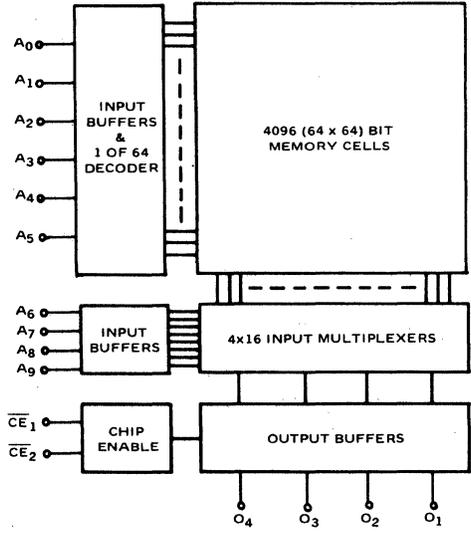
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**B227**



**B228**



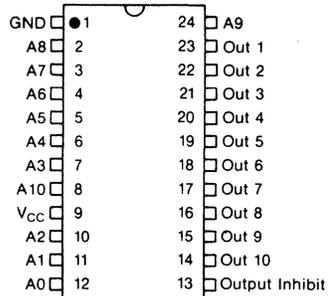
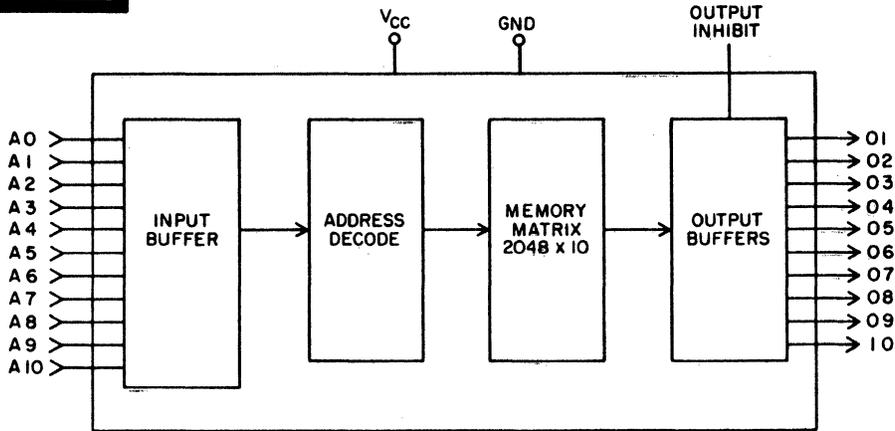
[Empty box]

[Empty box]

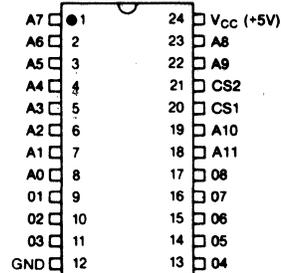
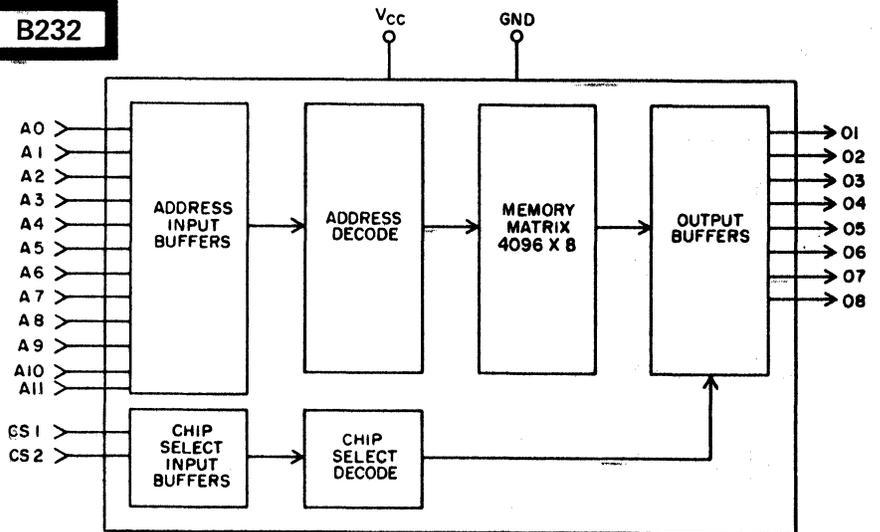
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**B231**



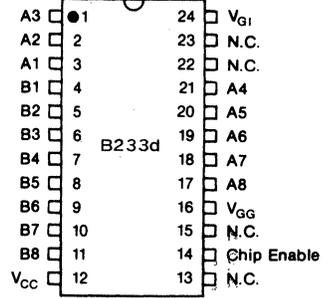
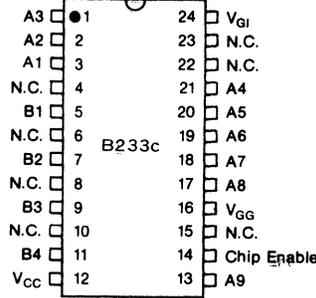
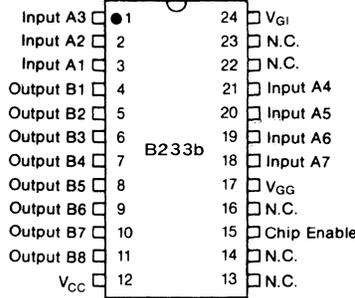
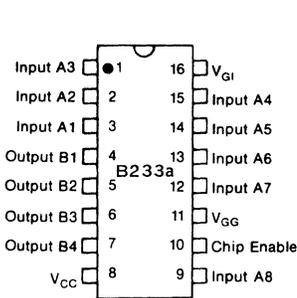
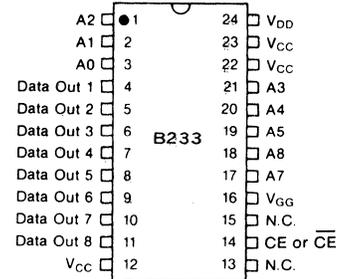
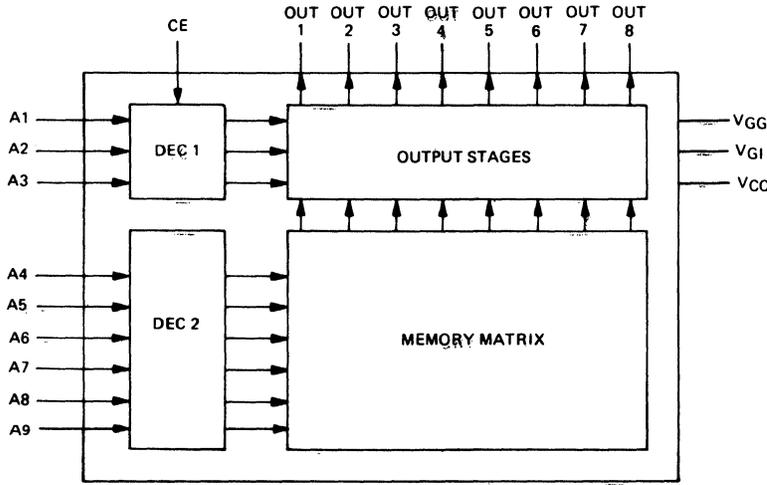
**B232**



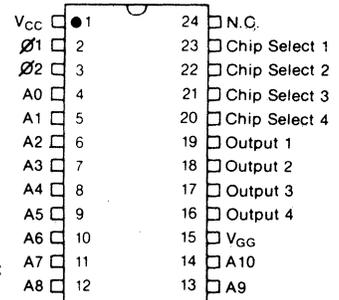
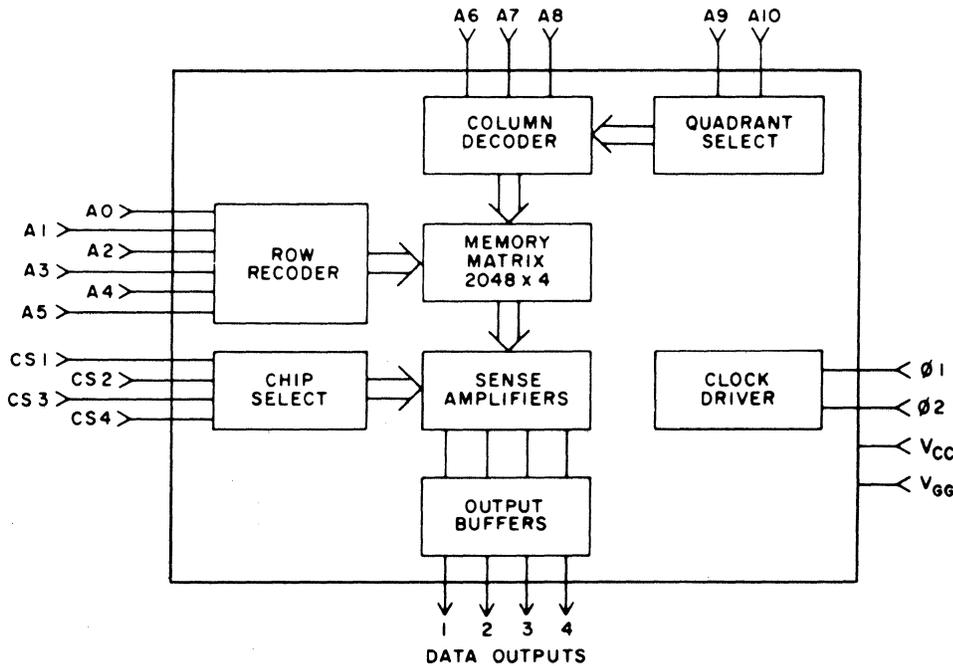
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

## B233



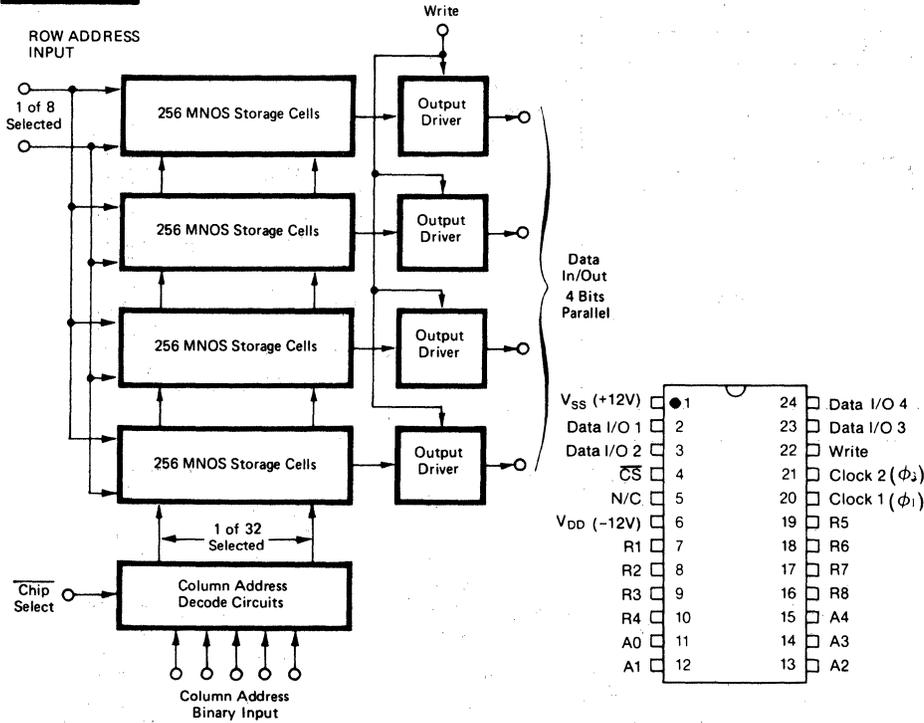
## B234



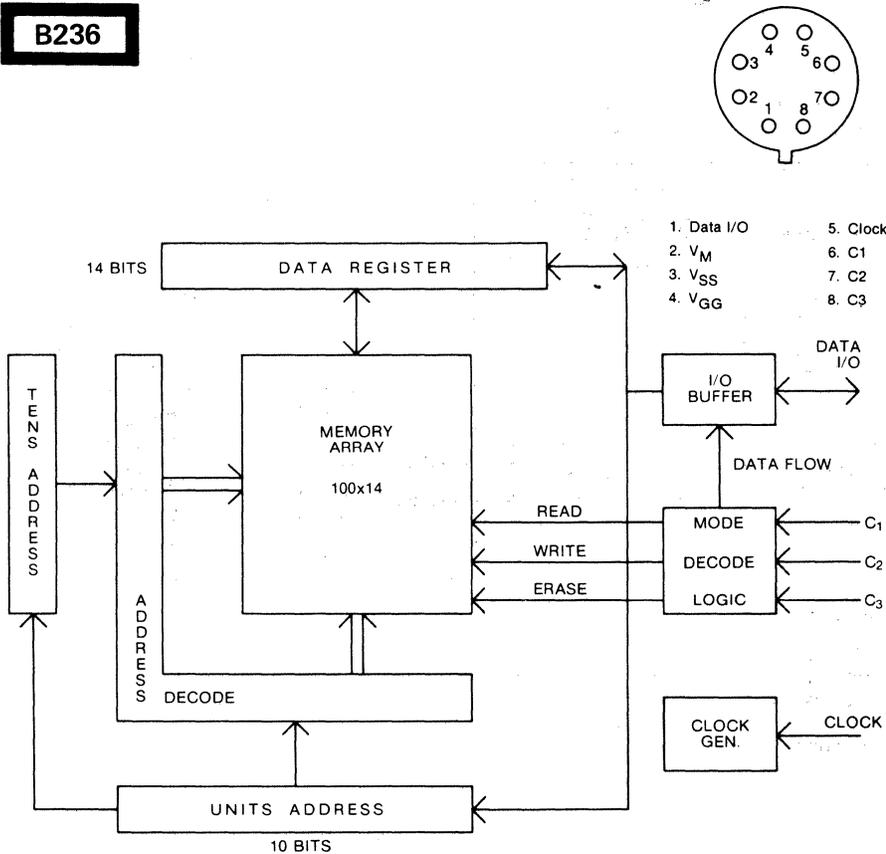
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

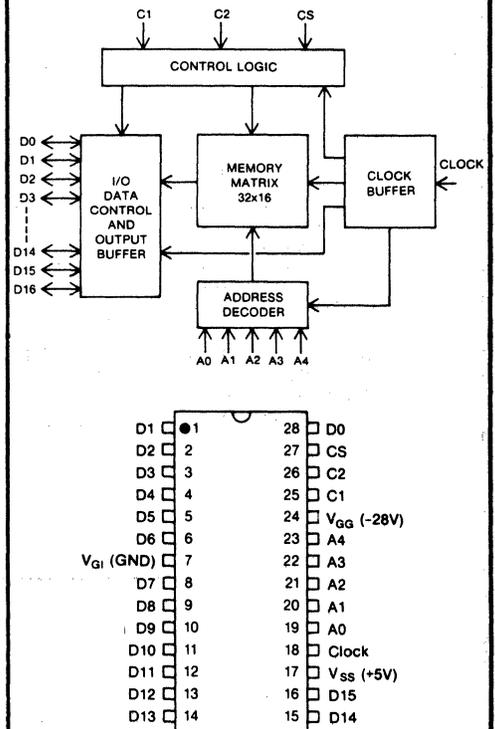
**B235**



**B236**



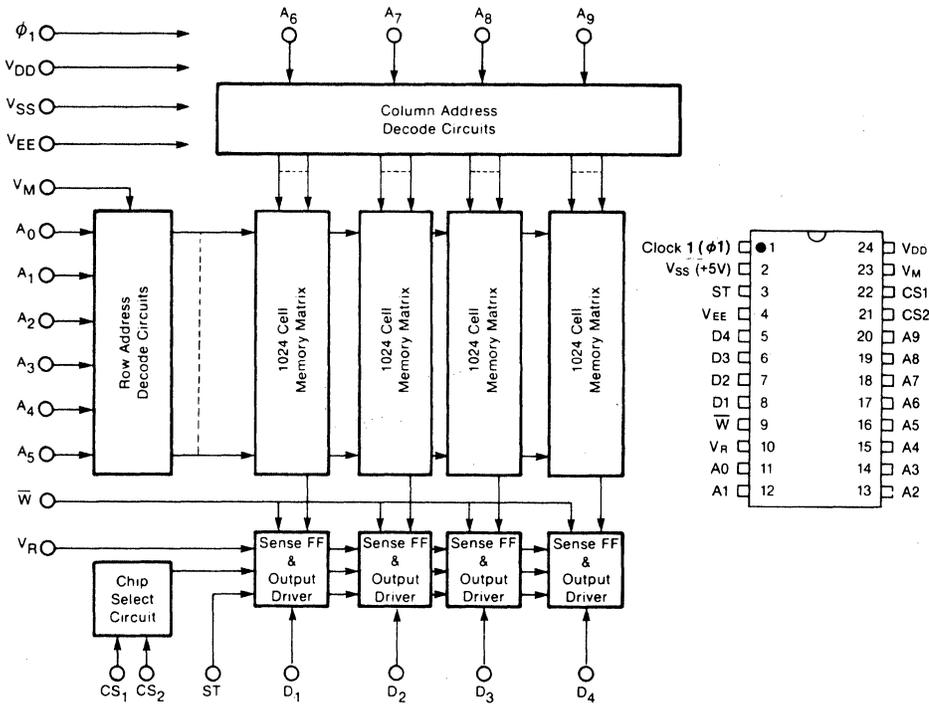
**B237**



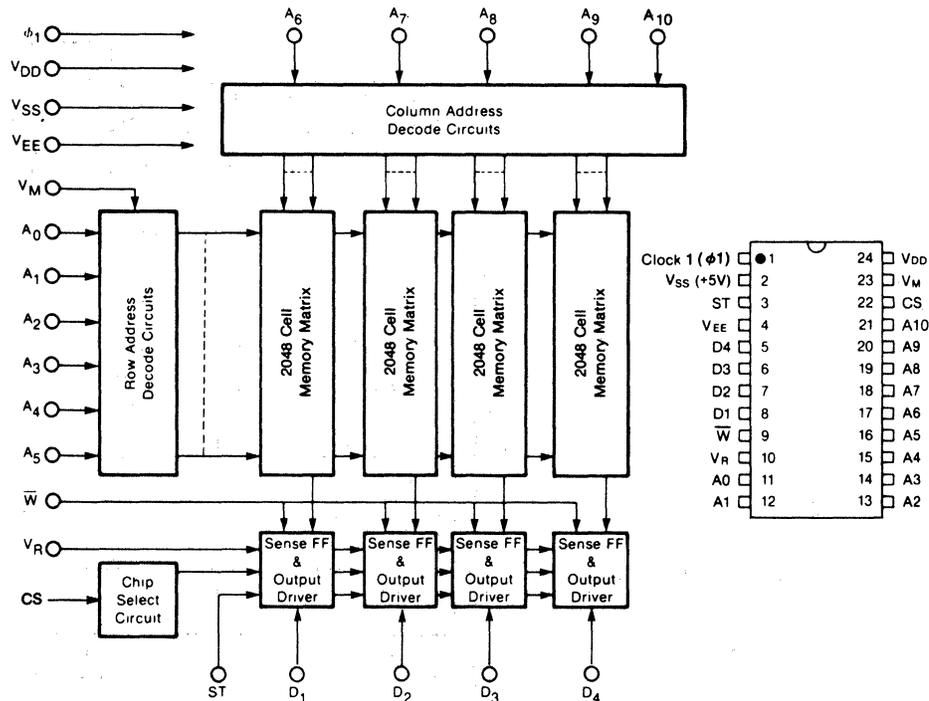
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

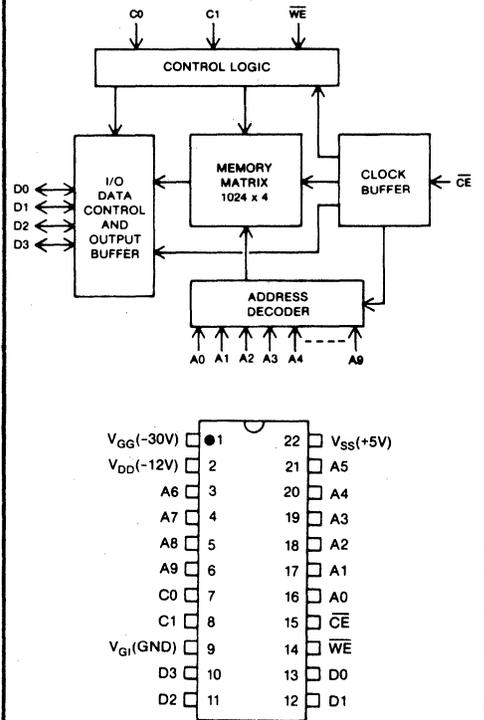
**B238**



**B239**



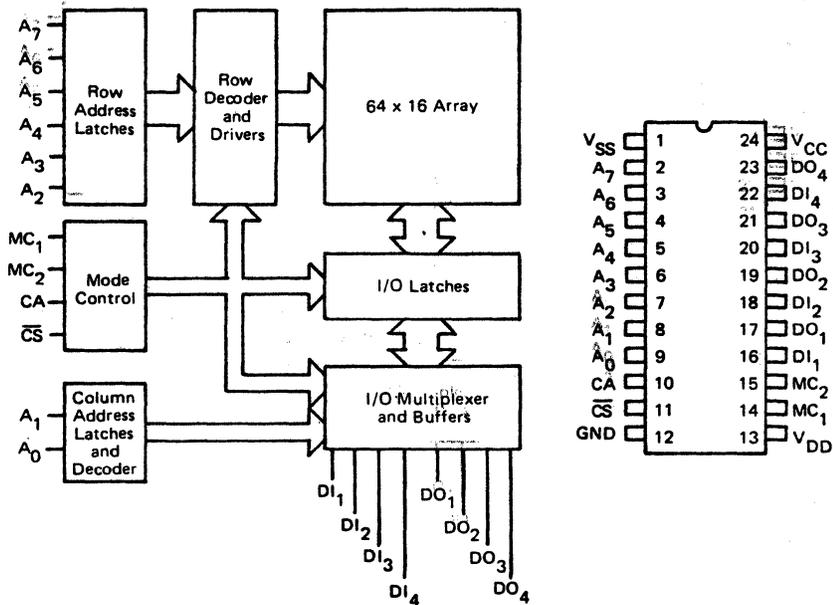
**B240**



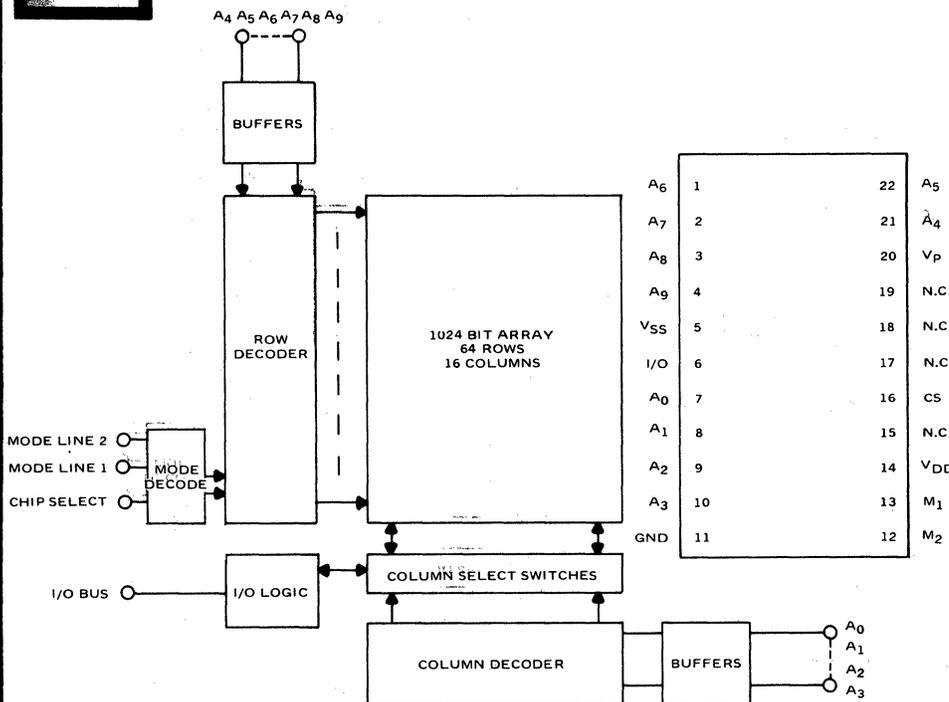
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

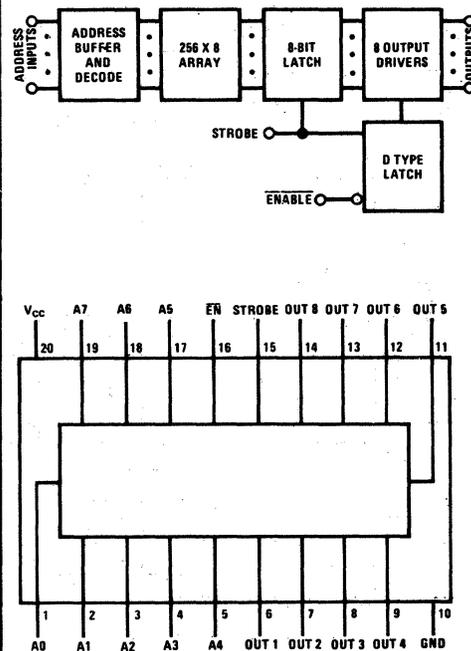
**B241**



**B242**



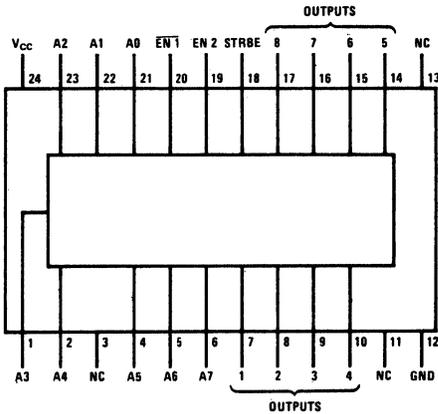
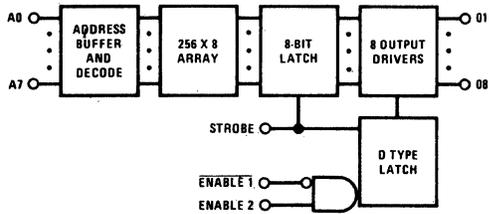
**B243**



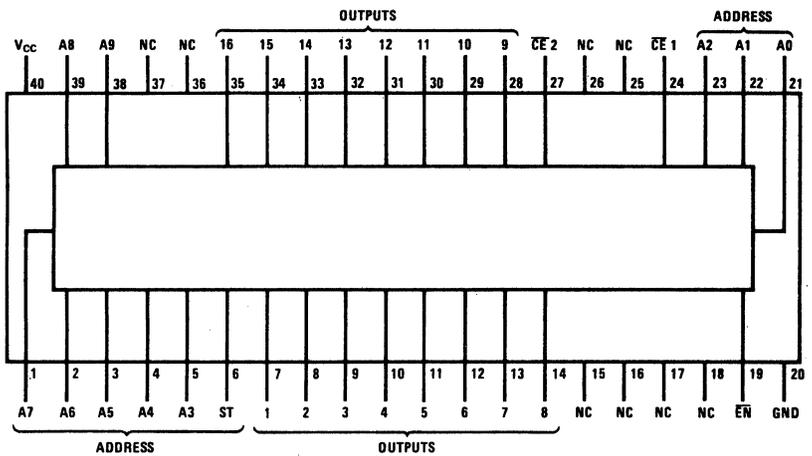
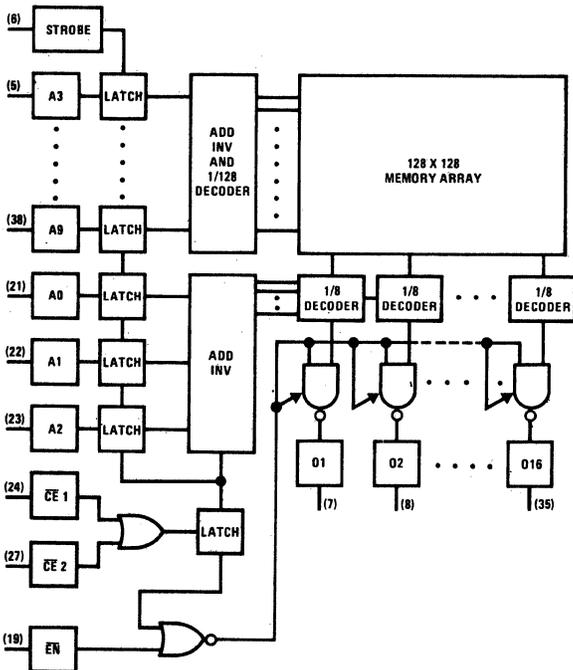
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

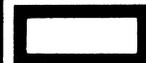
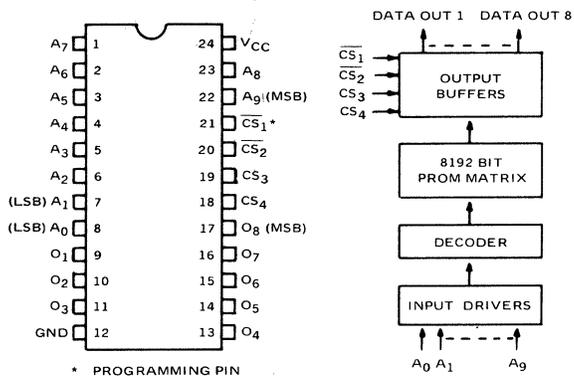
**B244**



**B245**



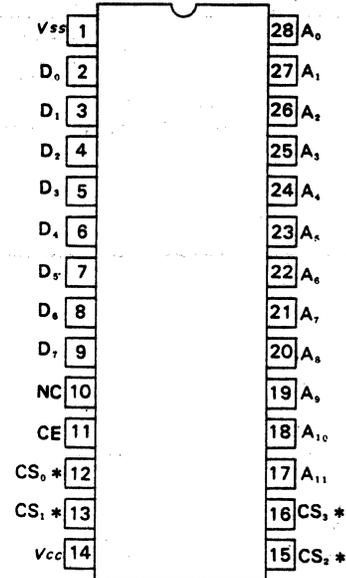
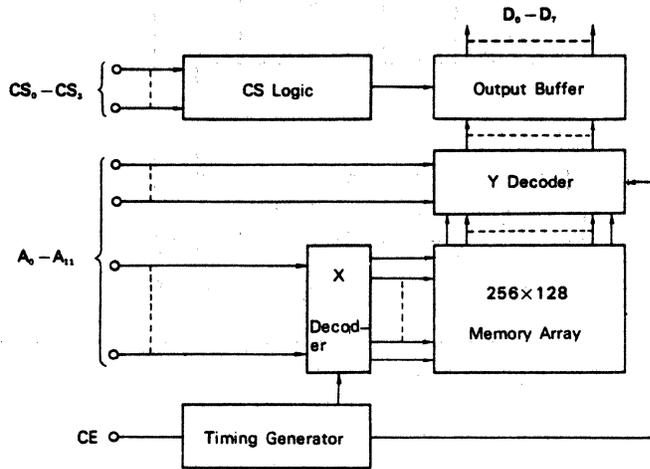
**B246**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

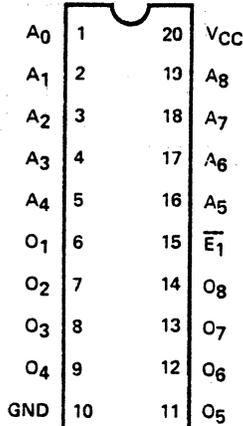
**B247**



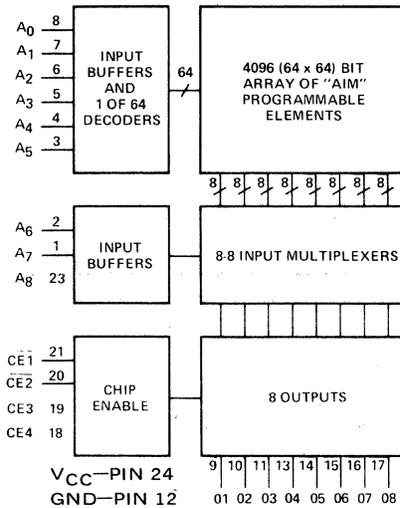
(Top View)

\* Mask Programmable

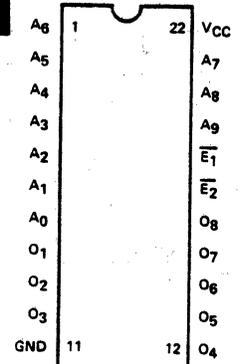
**B249**



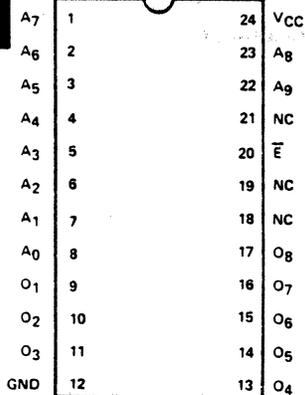
**B250**



**B251**



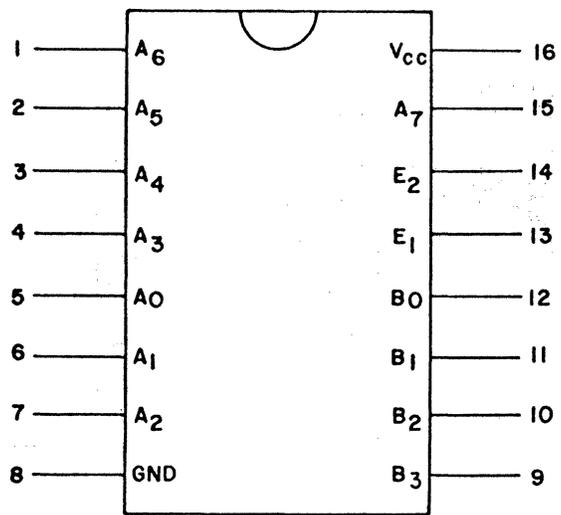
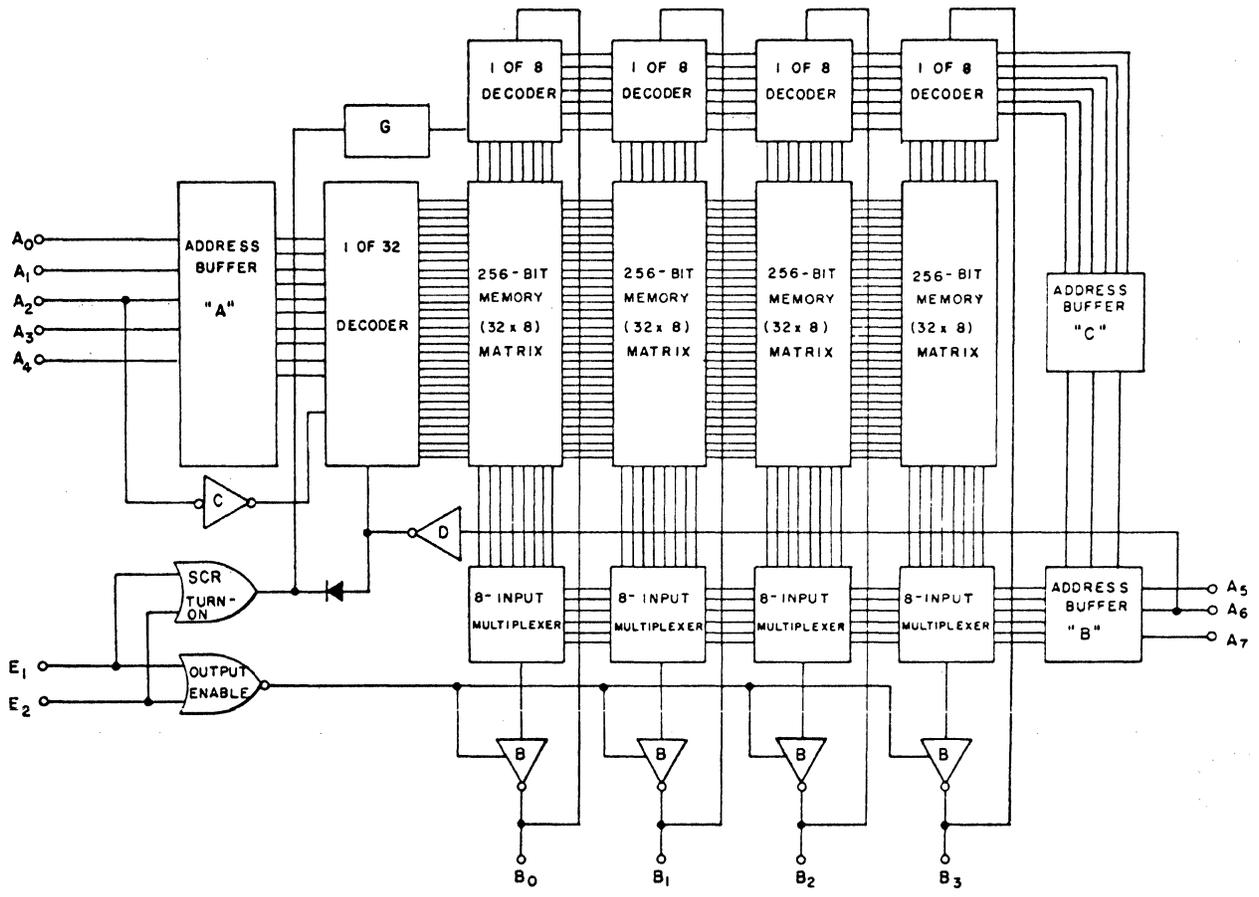
**B252**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

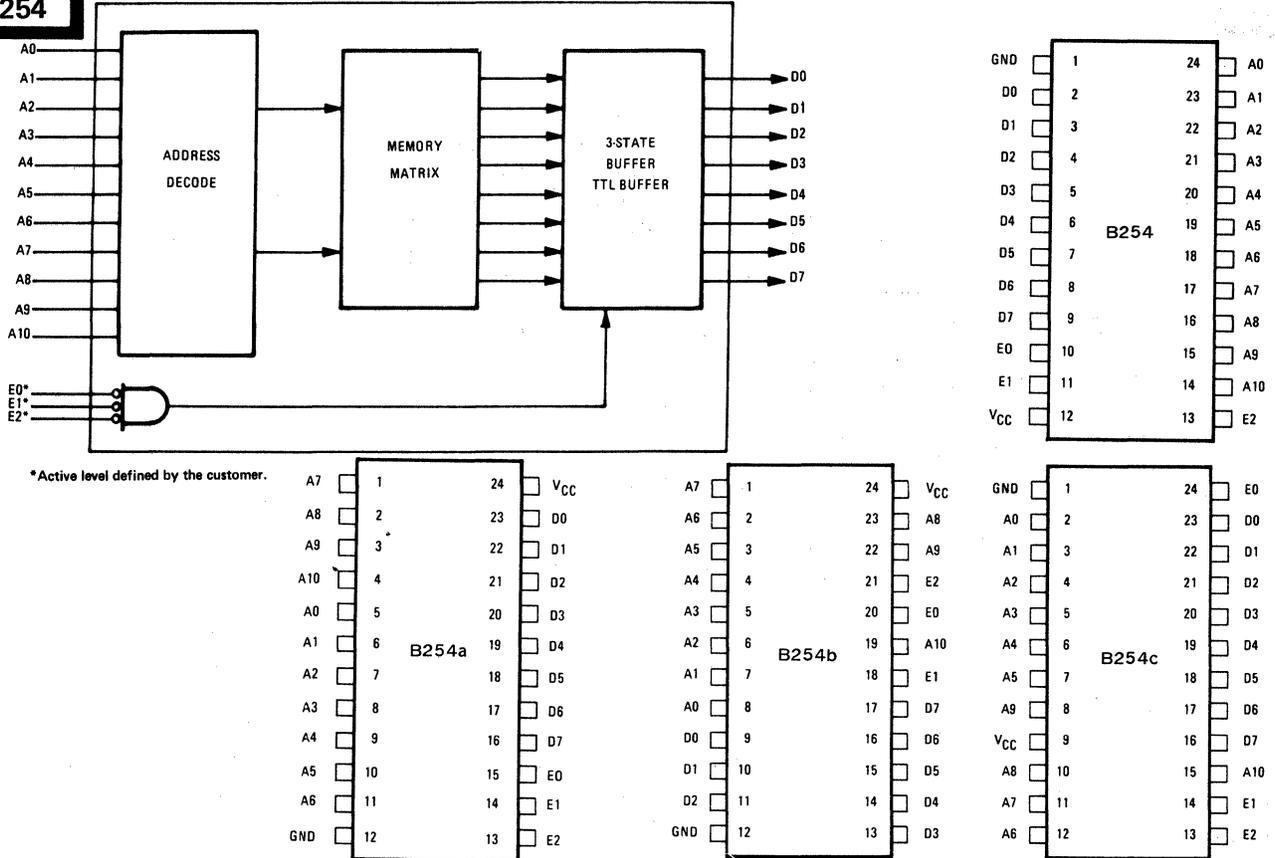
**B253**



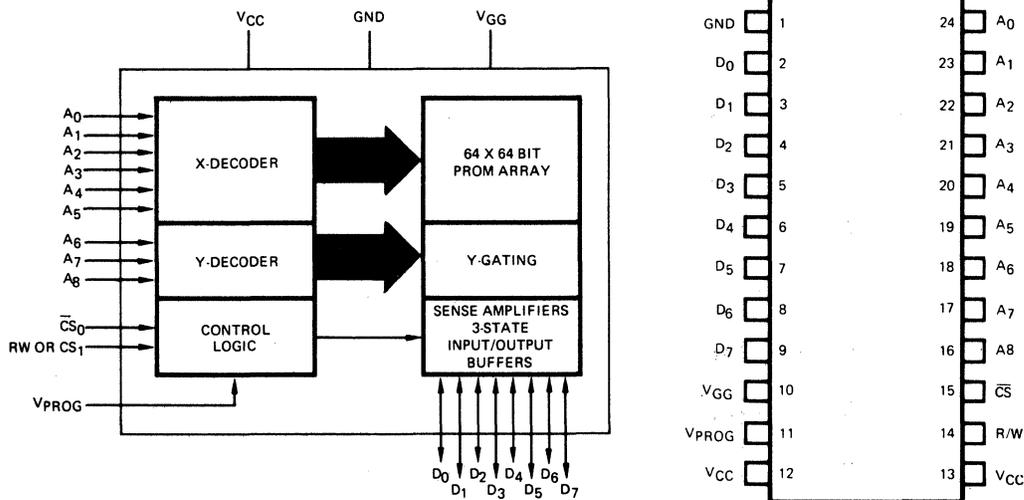
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

## B254



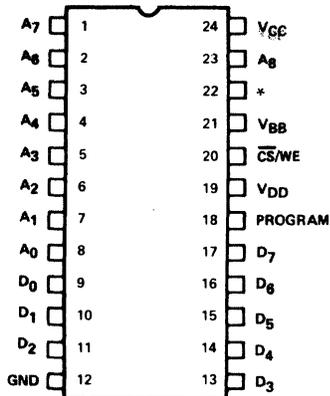
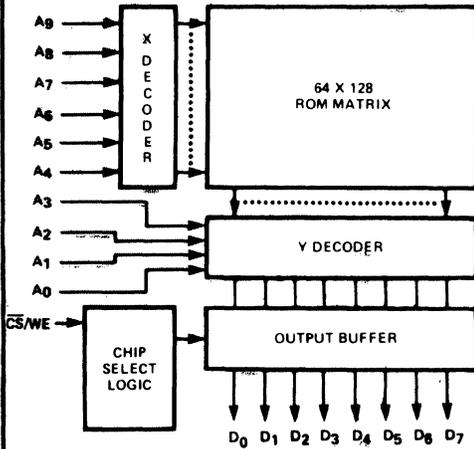
## B255



# 22. LOGIC/BLOCK DRAWINGS

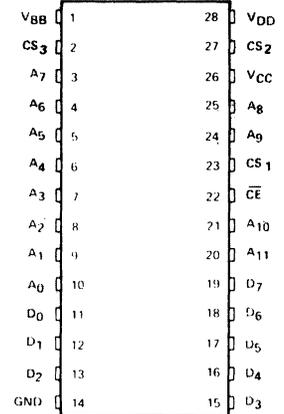
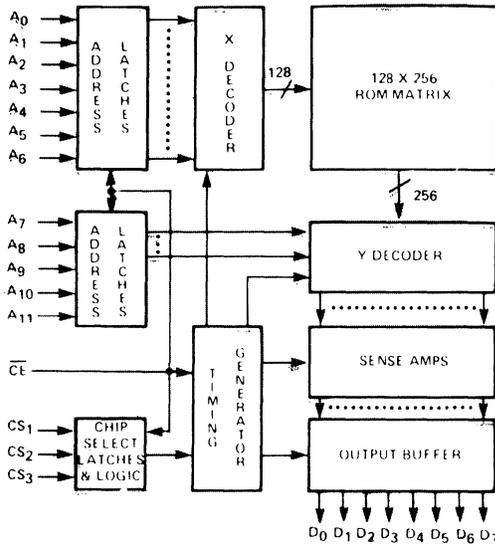
IN DRAWING NUMBER SEQUENCE

**B256**

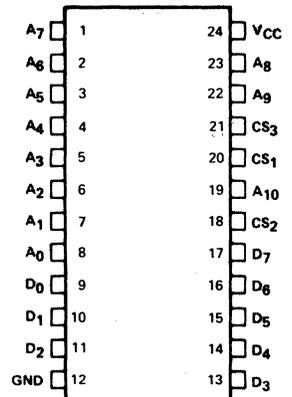
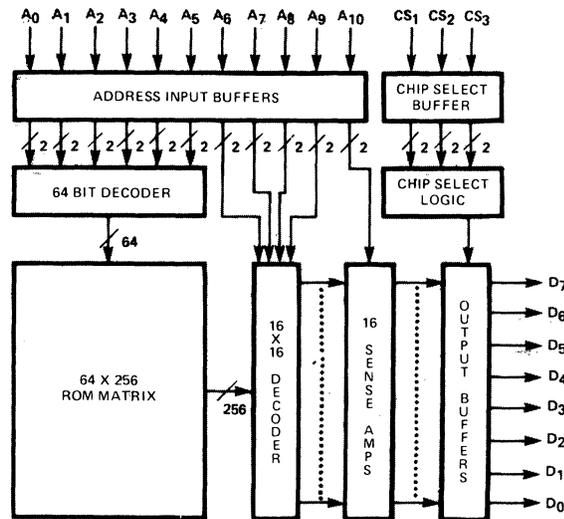


\* B256: PIN 22=V<sub>SS</sub>  
B256a: PIN22=A9

**B257**



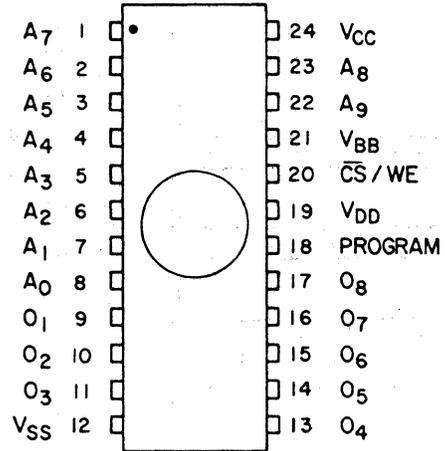
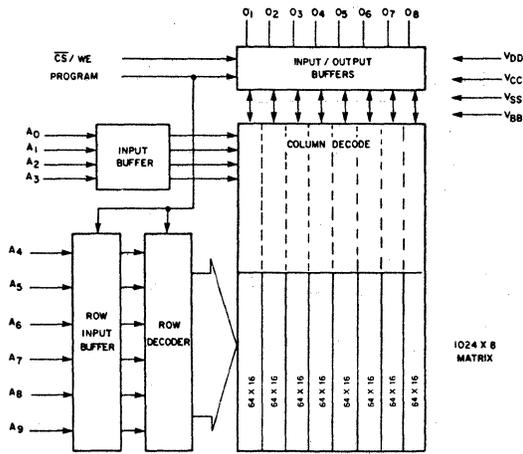
**B258**



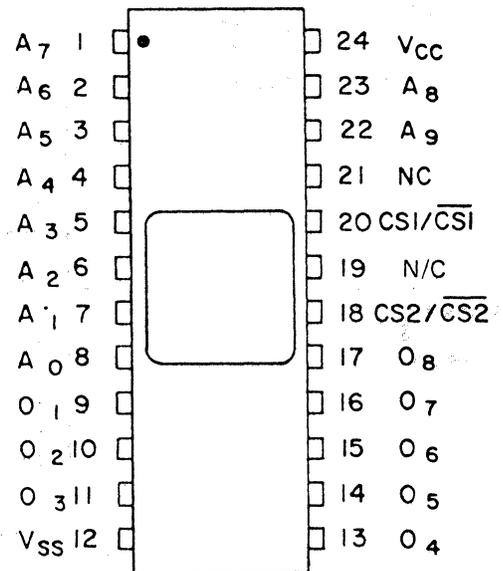
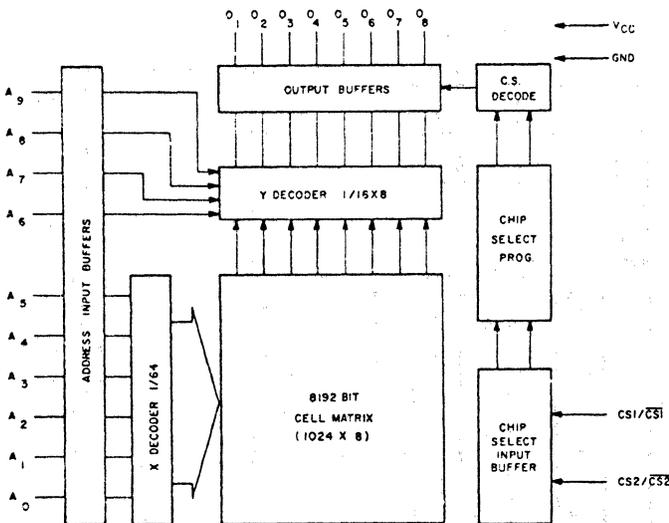
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B259**



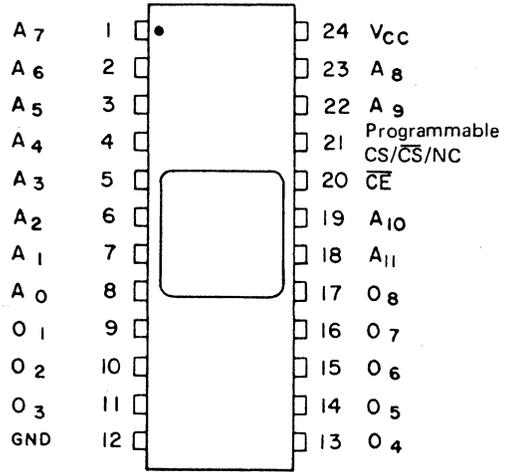
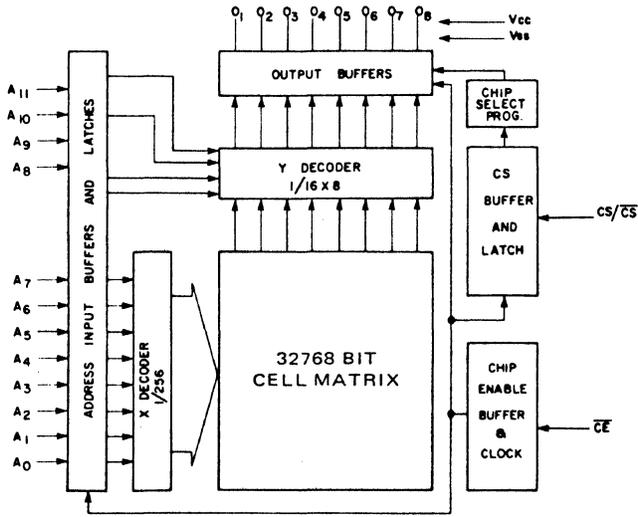
**B260**



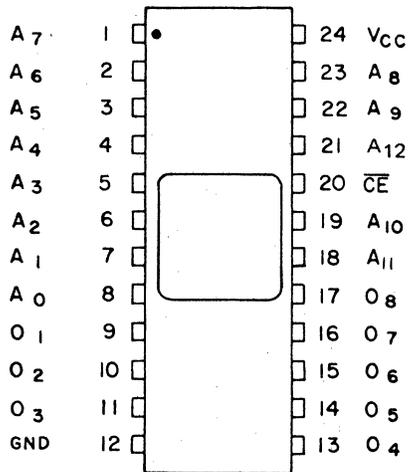
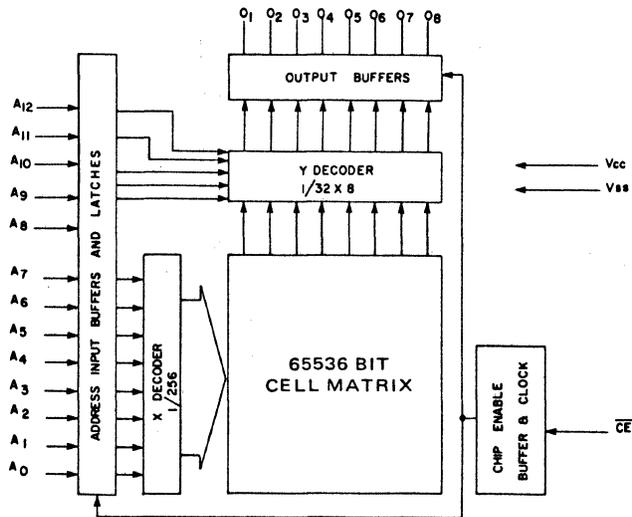
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**B261**



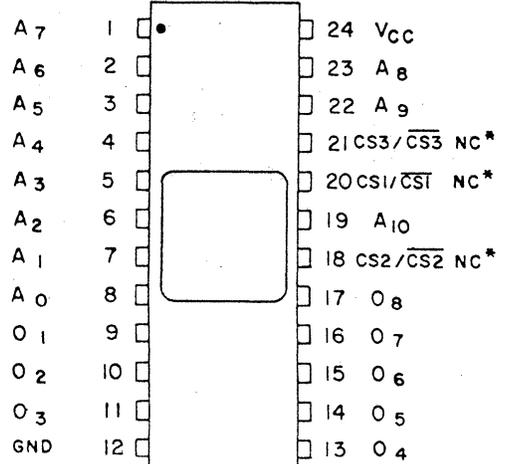
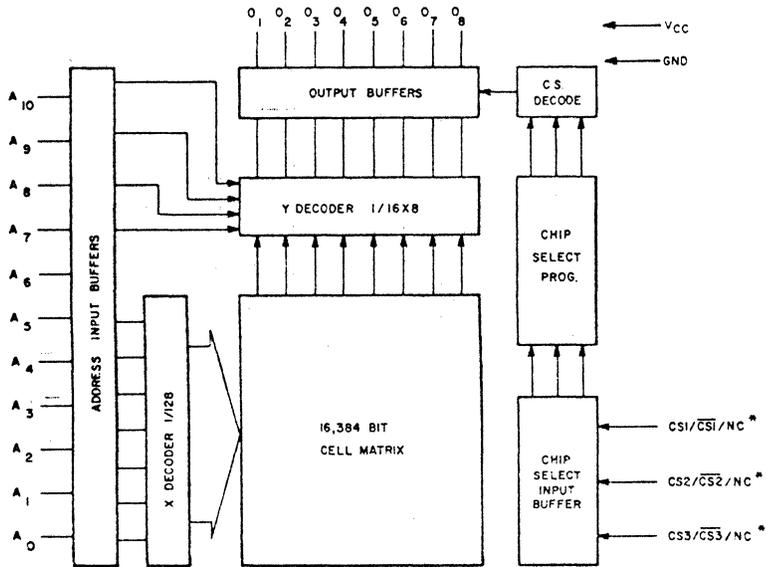
**B262**



# 22. LOGIC/BLOCK DRAWINGS

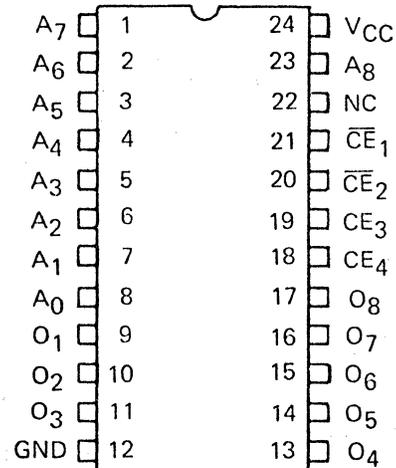
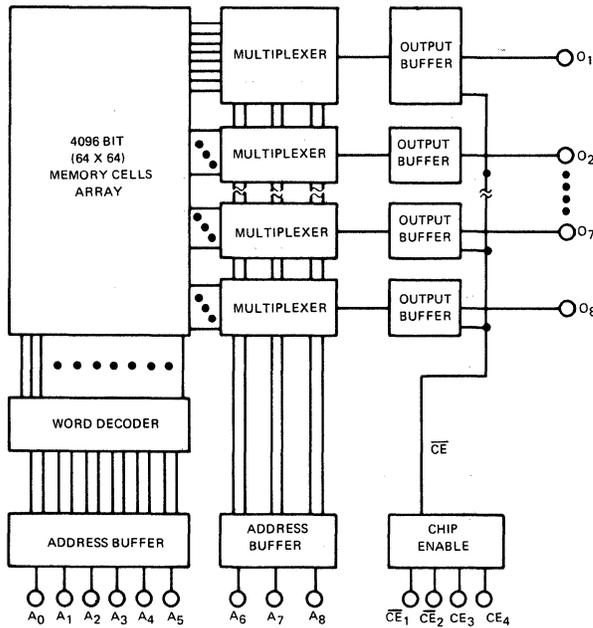
IN DRAWING NUMBER SEQUENCE

**B263**



\*Programmable Chip Selects

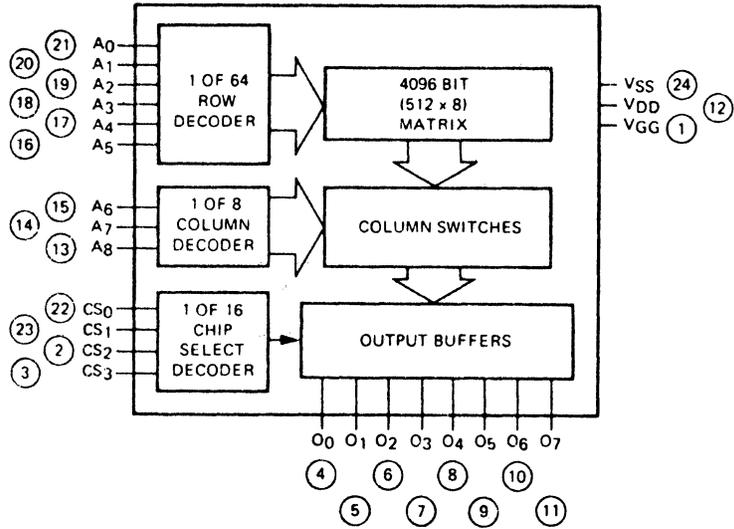
**B264**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**B265**



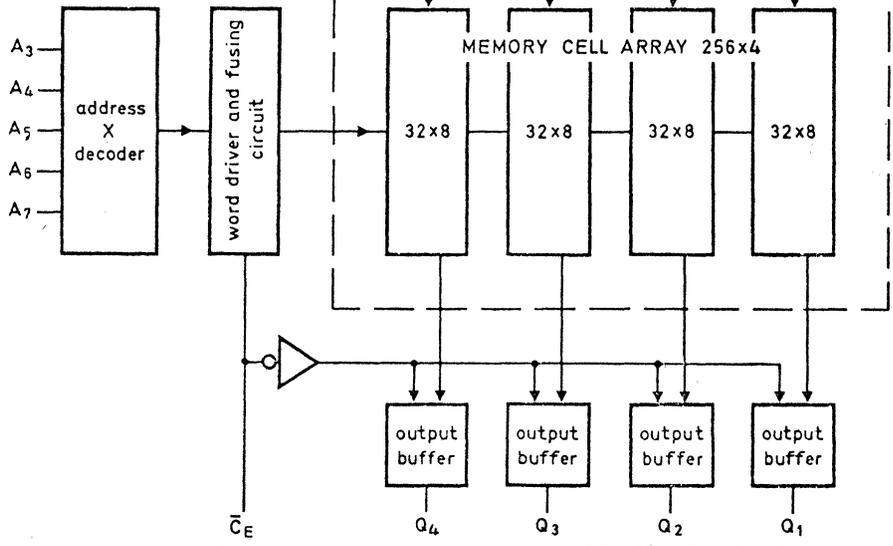
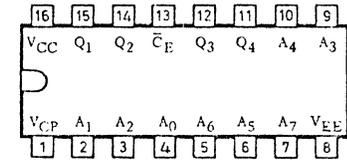
[ ]

[ ]

[ ]

[ ]

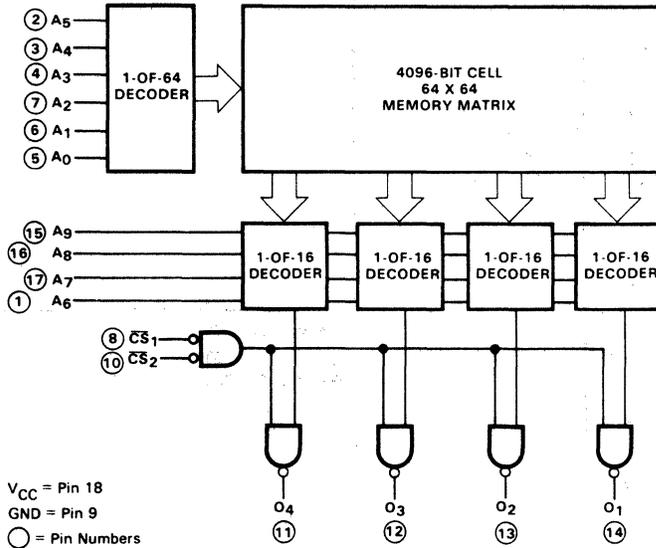
**B266**



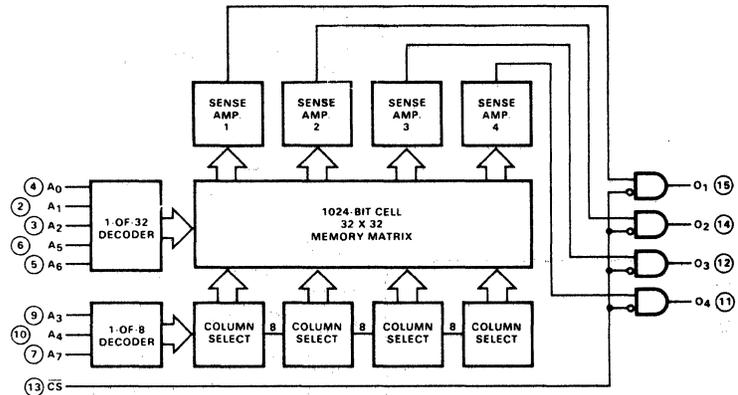
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**B267**



**B268**



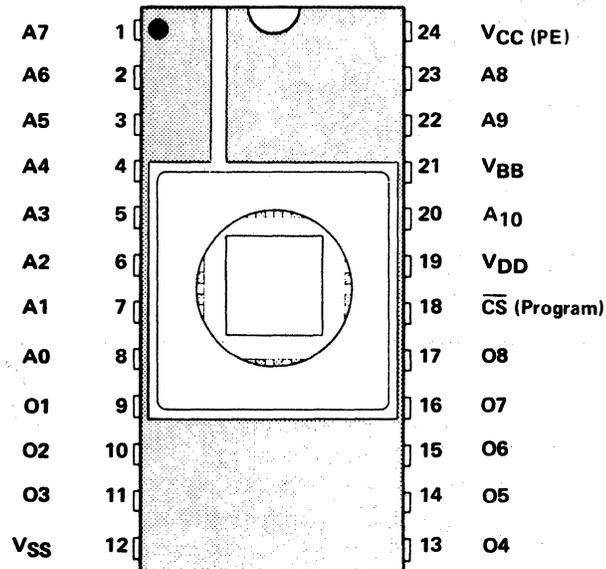
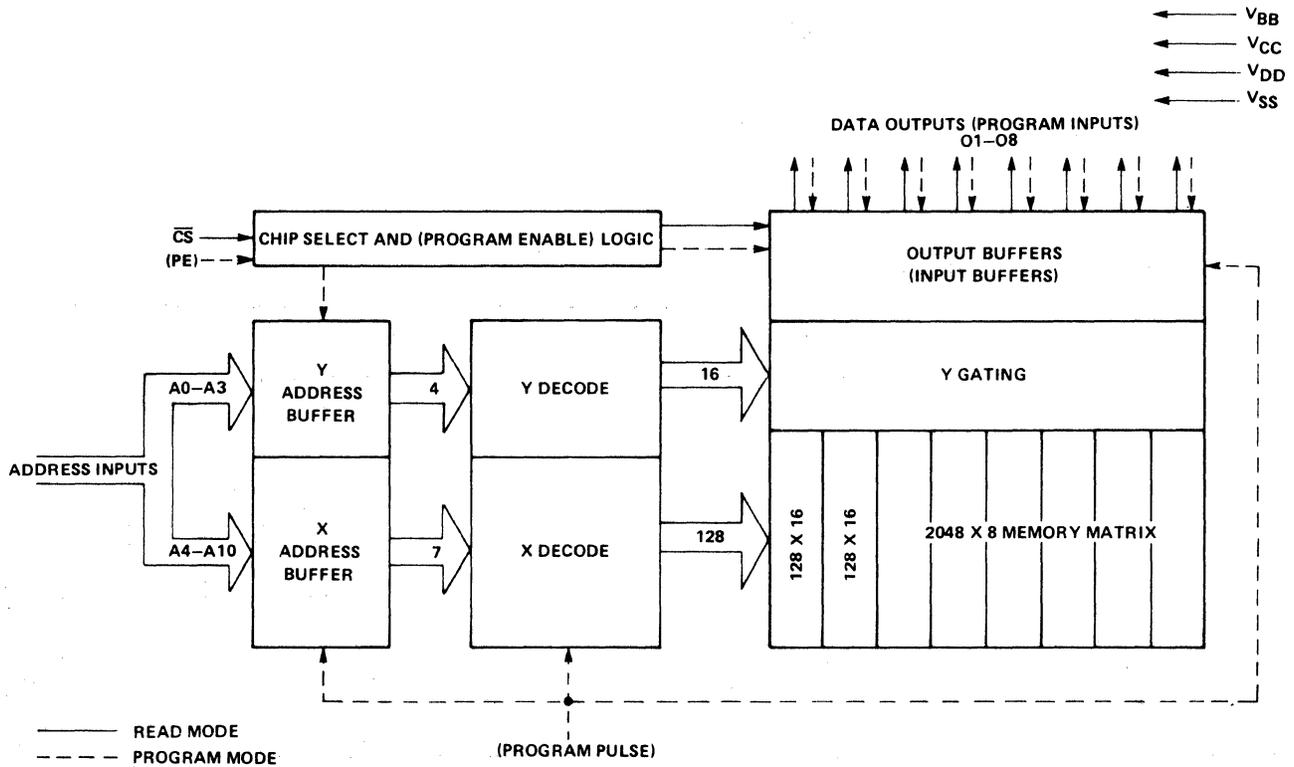
V<sub>CP</sub> = GND (Read only) = Pin 1  
V<sub>CP</sub> = +12 V (Programming only) = Pin 1  
V<sub>CC</sub> = GND = Pin 16

V<sub>EE</sub> = Pin 8  
○ = Pin Numbers

# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

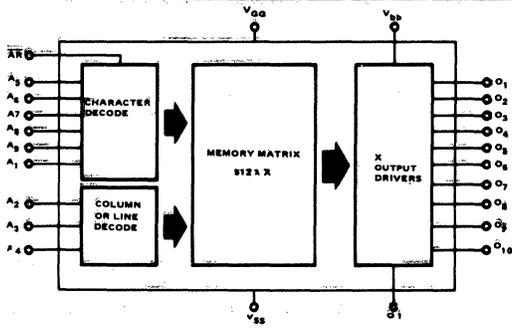
**B269**



# 22. LOGIC/BLOCK DRAWINGS

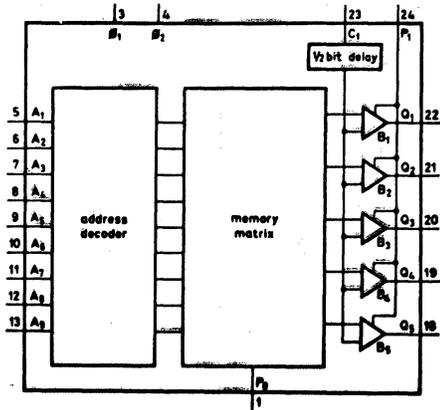
IN DRAWING NUMBER SEQUENCE

C5



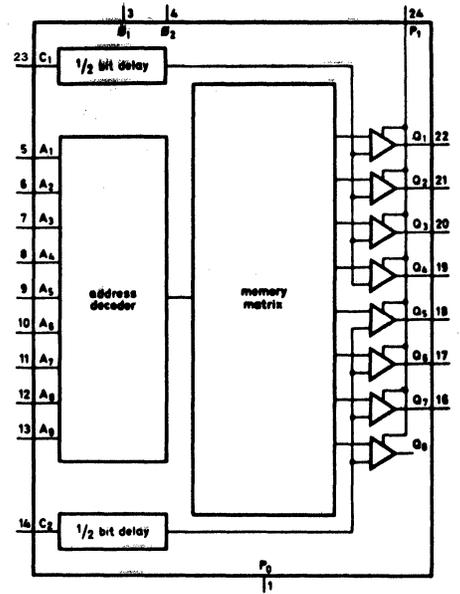
	1	2	3	4	5	6	7	8	9	AR	VBB	VGG	VSS	OI	1	2	3	4	5	6	7	8	9	10
C5	2	3	4	5	6	7	10	11	12	8	24	9	1	13	23	22	21	20	19	18	17	16	15	14
C5a	2	3	4	5	6	7	10	11	12	8	24	9	1	13	21	20	19	18	17	NA	NA	NA	NA	NA
C5b	14	13	12	11	10	9	6	5	4	8	15	7	1	NA	16	17	18	2	3	NA	NA	NA	NA	NA

C7

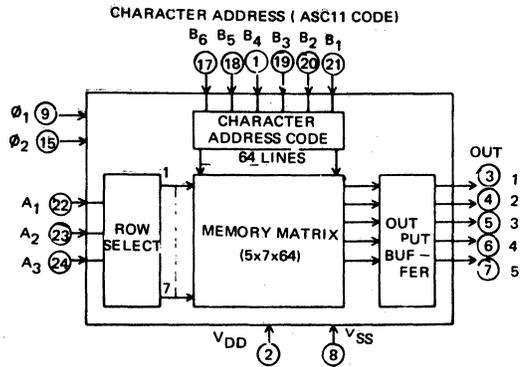


PAIRED DEVICES  
C7 FDR116Z1/FDR116Z2

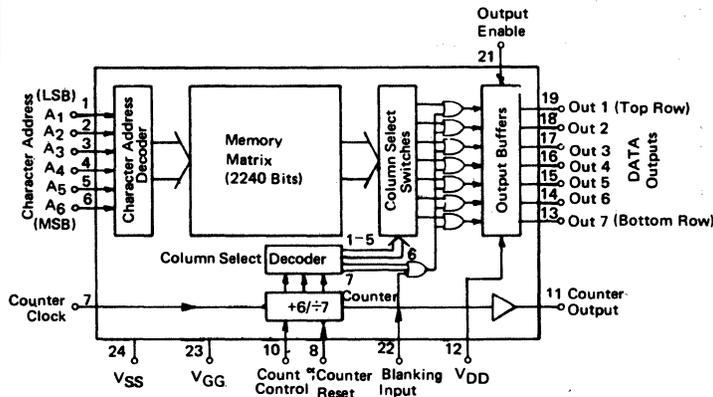
C8



C9



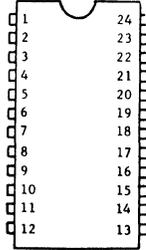
C12



# 22. LOGIC/BLOCK DRAWINGS

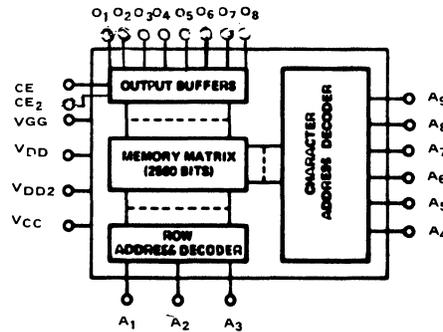
IN DRAWING NUMBER SEQUENCE

**C14**



		PIN NUMBERS																								REMARKS
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	
C14	R1 R2	NC	R3	A4	A5	I	DC	OUT	NC	CE	B1	GND	B2	B3	NC	NC	B4	B5	A3	A2	NC	NC	A1	VCC	CM2800-0/CM2900-02 PAIRED DEVICES	
C14a	L1 L2	LO	NC	NC	NC	B1	B2	B3	B4	B5	VSS	NC	CE	VDD	A0	A1	A2	A3	A4	A5	NC	NC	VDD			
C14b	S0	B7	B6	B5	B4	B3	B2	B1	O1	O2	O3	GND	O4	O5	O6	O7	NC	NC	E2	E1	S3	S2	S1	VCC	S0, S1, S2, S3 USED FOR CHARACTER SCAN	
C14c	S0	B7	B6	B5	B4	B3	B2	B1	O1	O2	O3	GND	O4	O5	O6	O7	O8	O9	E2	E1	S3	S2	S1	VCC		

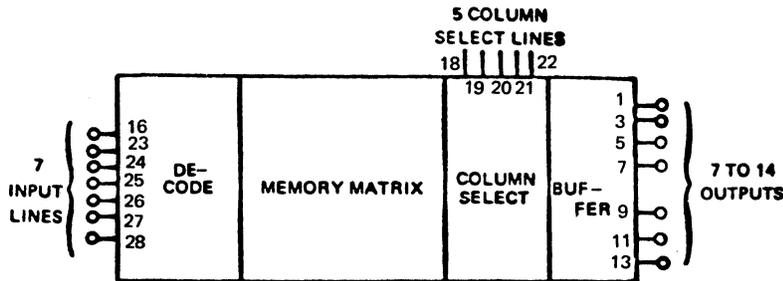
**C15**



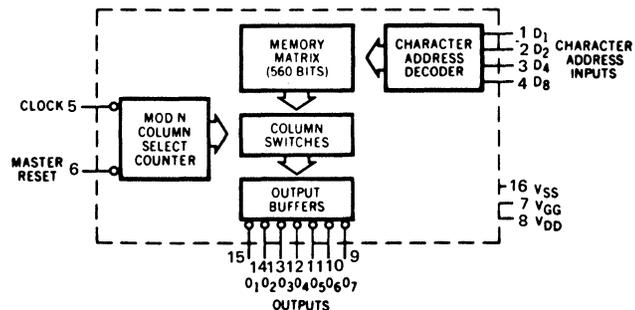
		ADDRESS										OUT								CE	VDD	VDD2	VCC	VGG
		1	2	3	4	5	6	7	8	9	10	1	2	3	4	5	6	7	8					
C15		14	15	16	17	18	19	20	21	22	4	5	6	7	8					11	12		24	1
C15a		14	15	16	17	18	19	20	21	22	4	5	6	7	8					10	12	11	24	1
C15b		14	15	16	17	18	19	20	21	22	10	9	8	7	6	5	4	3	1	12	11	24	23	

**C16**

PAIR DEVICES	
C16	FMS4177JC/FMS4178JC FMS4177NC/FMS4178NC



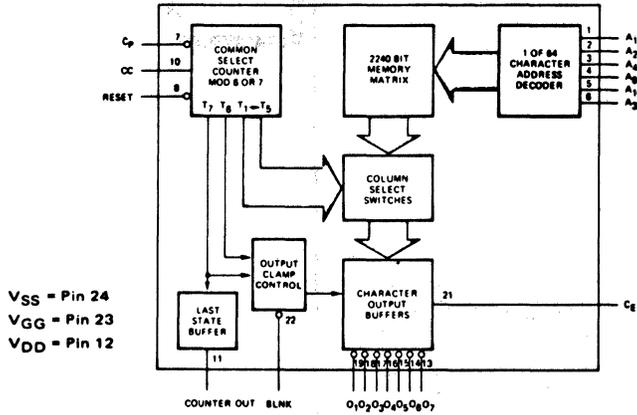
**C17**



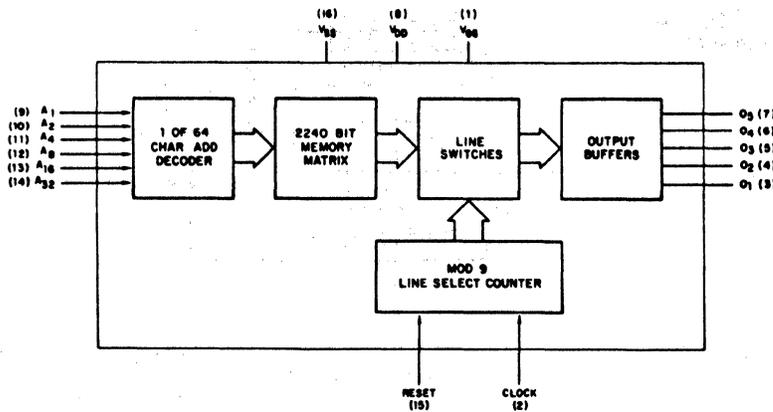
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

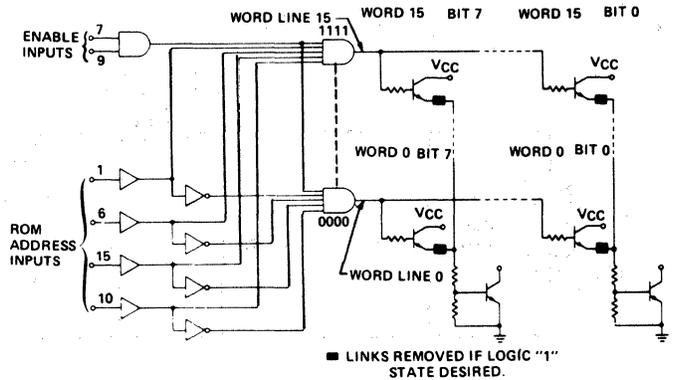
**C18**



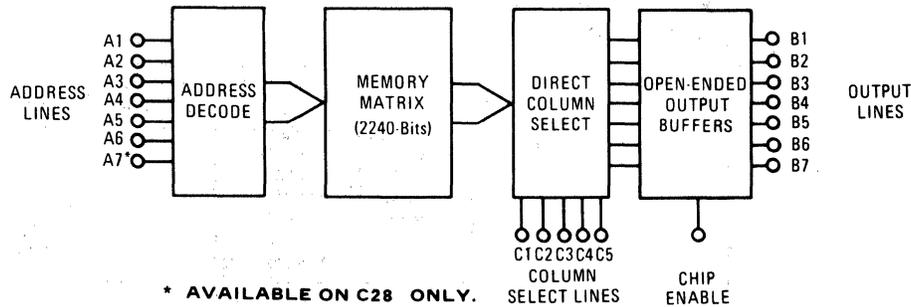
**C19**



**C26**



**C28**

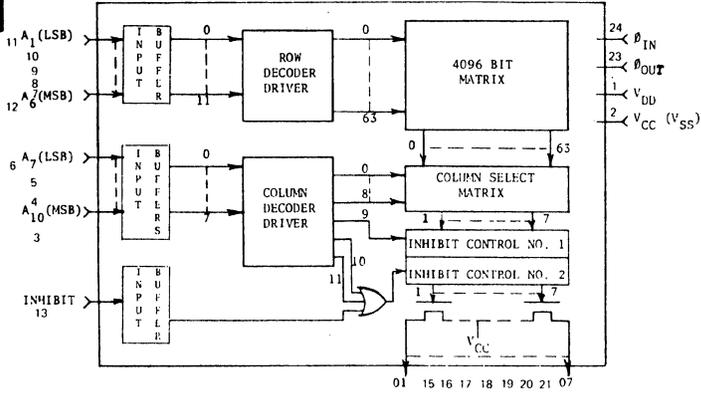


	A							B							C					CE	VSS	VGG	VDD	
	1	2	3	4	5	6	7	1	2	3	4	5	6	7	1	2	3	4	5					
C28	23	22	21	20	19	13	24	4	5	6	7	8	9	10	14	15	16	17	18	3	1	2	12	
C28a	27	26	25	24	23	16	1	3	5	7	9	11	13	18	19	20	21	22	28	17	15	14		
C28b	27	26	25	24	23	16	18	1	3	5	7	9	11	13	18	19	20	21	22		17	15	14	

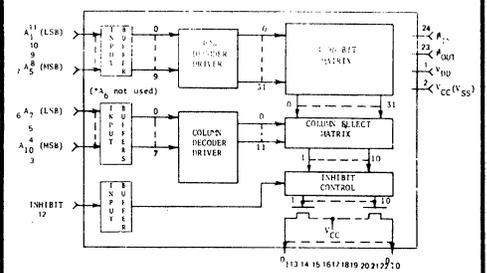
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

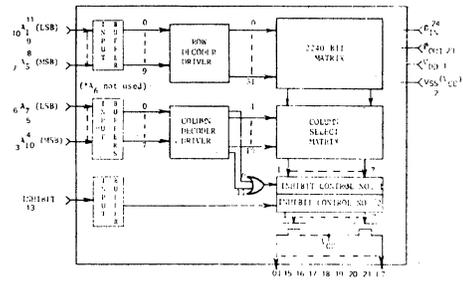
**C32**



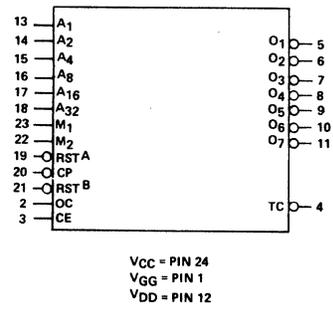
**C33**



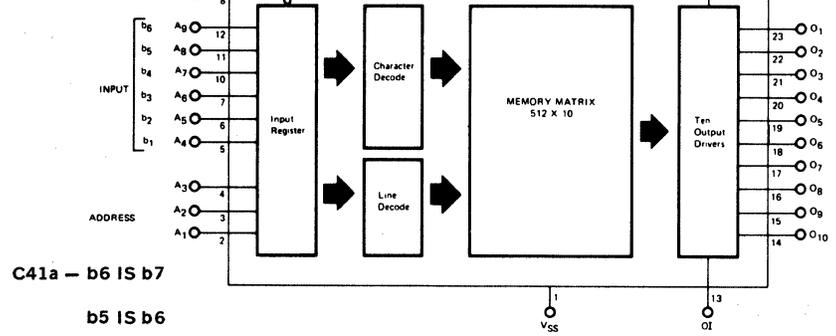
**C34**



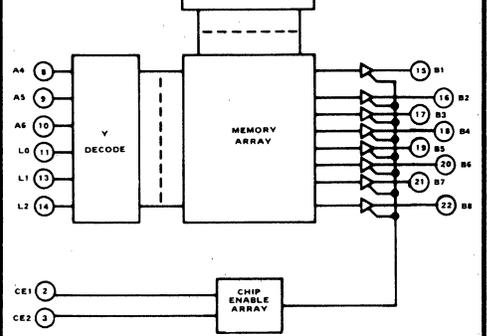
**C40**



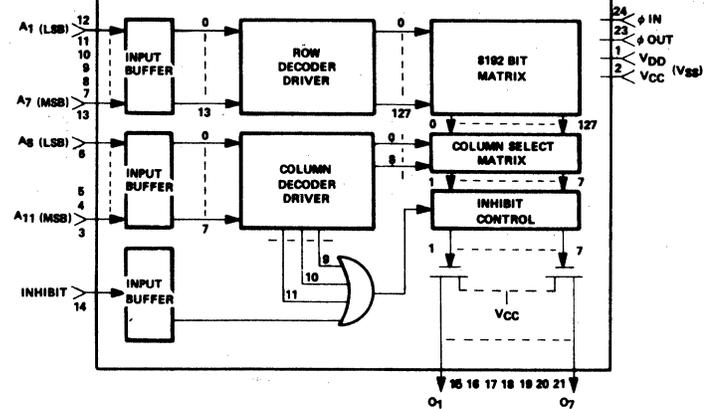
**C41**



**C42**



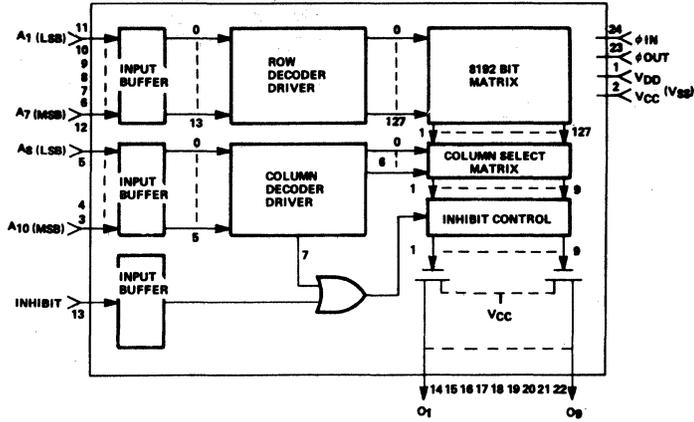
**C43**



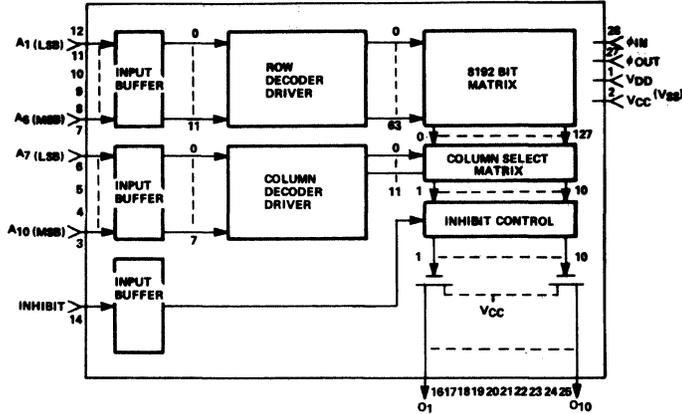
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

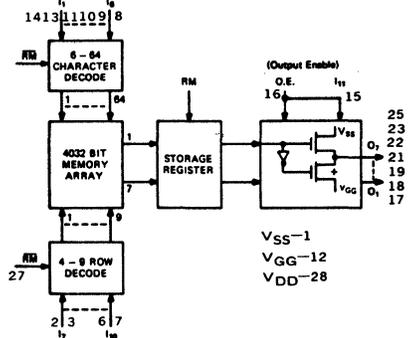
**C44**



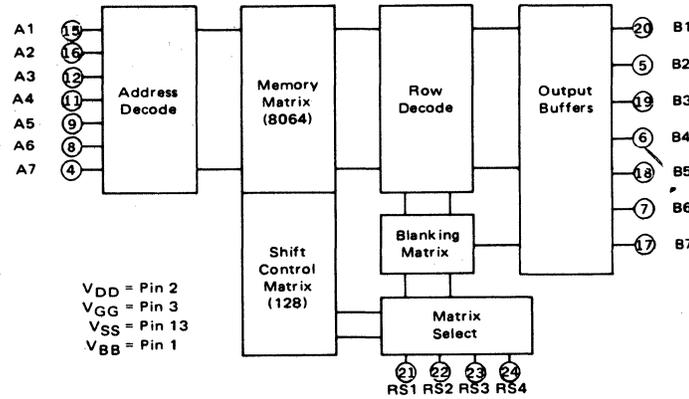
**C45**



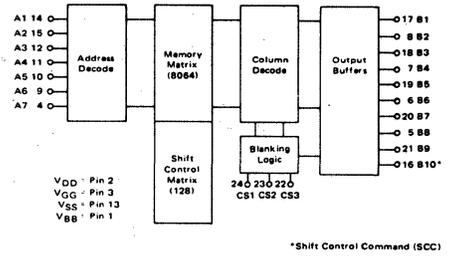
**C47**



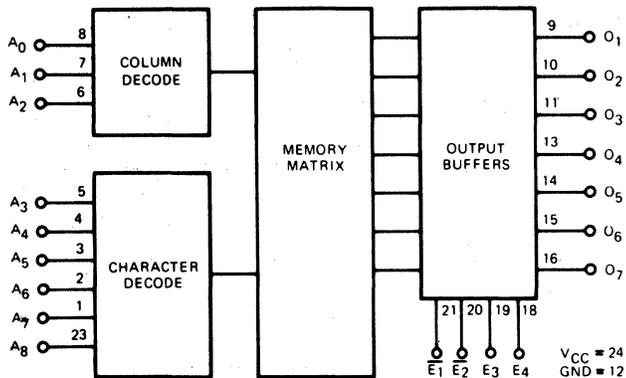
**C50**



**C51**



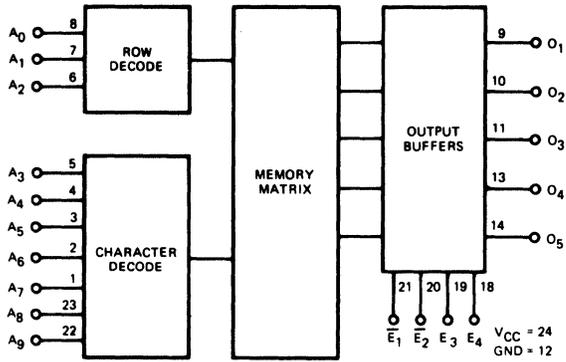
**C52**



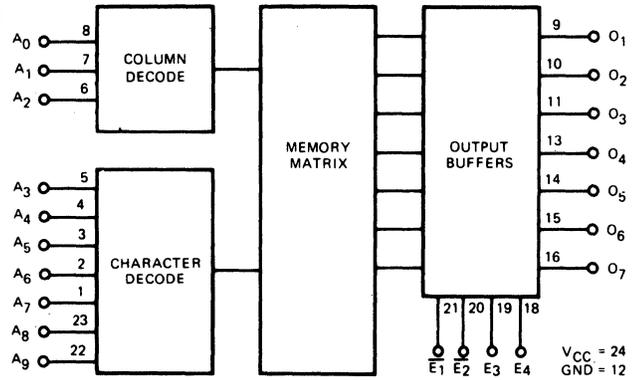
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

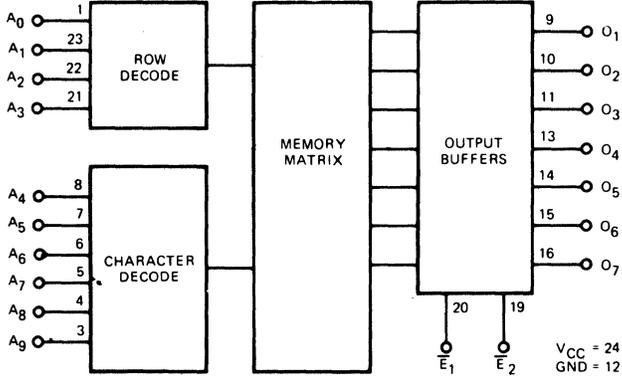
**C53**



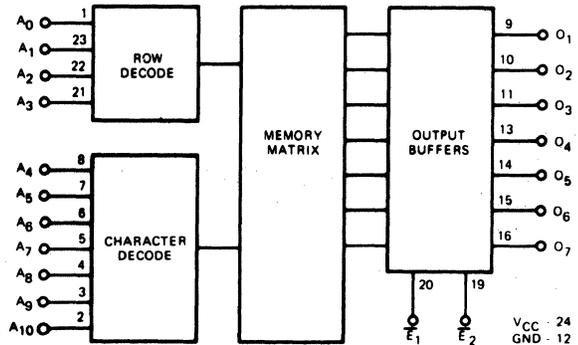
**C54**



**C55**



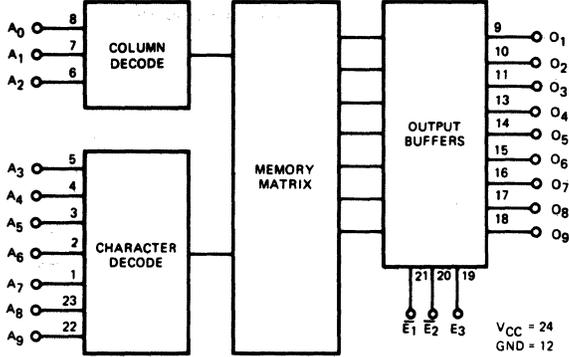
**C56**



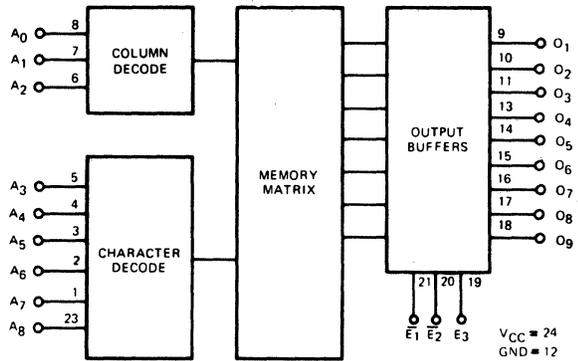
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

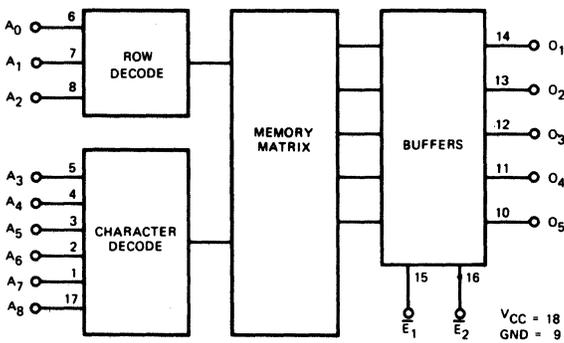
C57



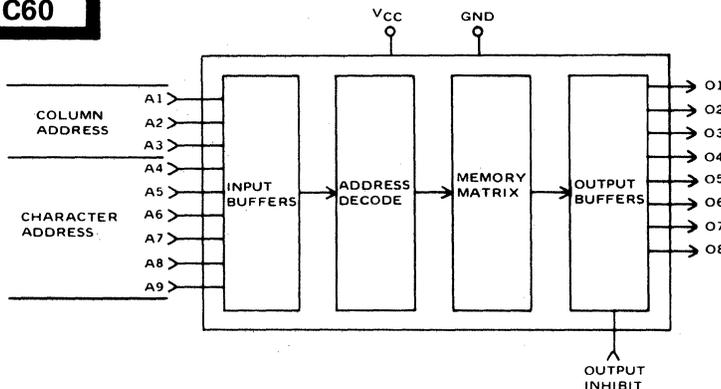
C58



C59



C60



N.C.	1	24	V <sub>CC</sub> (+5V)
N.C.	2	23	N.C.
N.C.	3	22	A9
01	4	21	A8
02	5	20	A7
03	6	19	A6
04	7	18	A5
05	8	17	A4
N.C.	9	16	A3
GND	10	15	A2
OUT INH	11	14	A1
N.C.	12	13	N.C.

A1 - A9 ADDRESS INPUTS  
O1 - O5 DATA OUTPUTS  
OUT INH OUTPUT INHIBIT  
N.C. NO CONNECTION

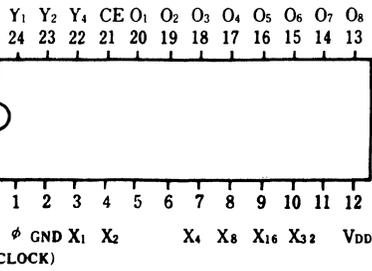
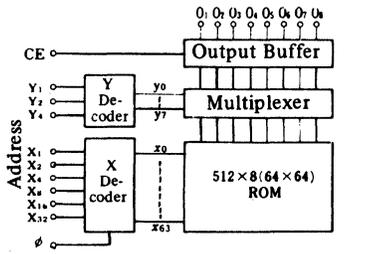
PIN FUNCTION	PIN FUNCTION
1	OUT INH.
2	N.C.
3	OUT 8
4	OUT 7
5	OUT 6
6	OUT 5
7	OUT 4
8	OUT 3
9	OUT 2
10	OUT 1
11	GND
12	N.C.
13	N.C.
14	A1
15	A2
16	A3
17	A4
18	A5
19	A6
20	A7
21	A8
22	A9
23	N.C.
24	V <sub>CC</sub> (+5V)

ORGANIZATION	OUTPUT
C60	512x5 01-9
C60a	384x8 01-8

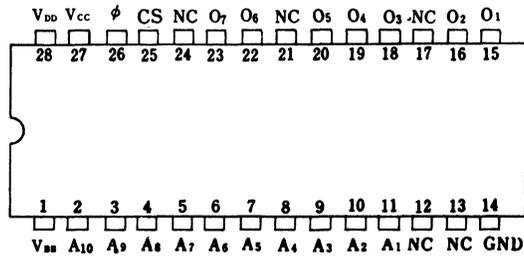
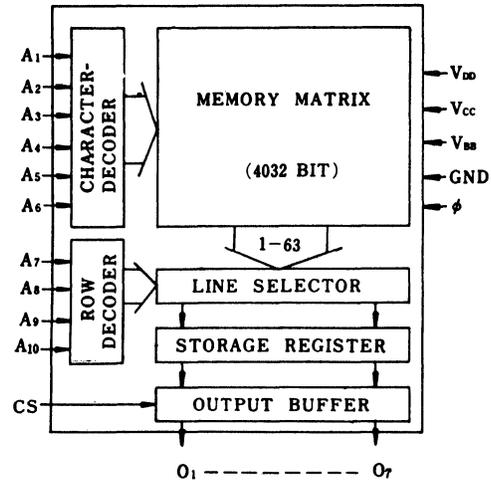
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

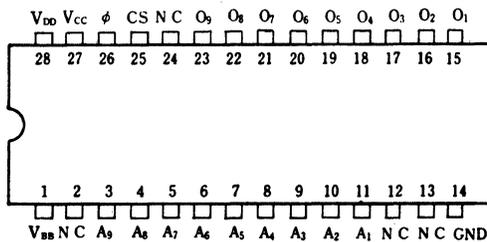
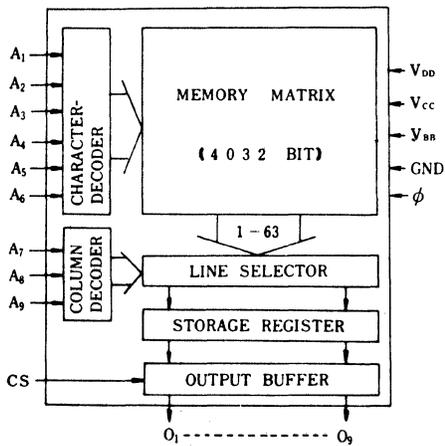
**C61**



**C62**



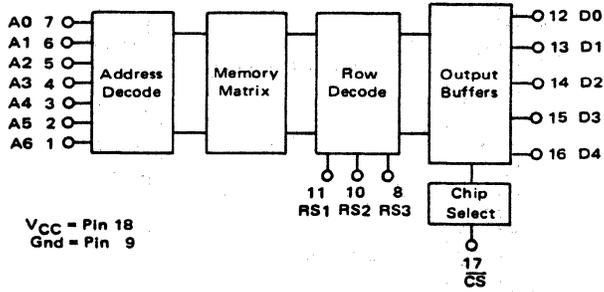
**C63**



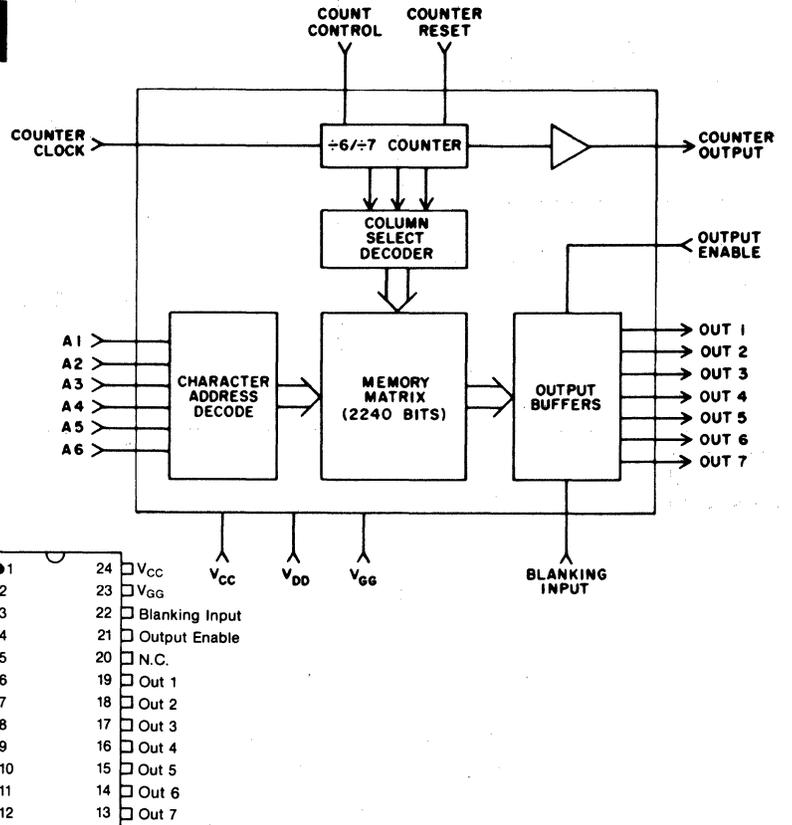
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**C64**



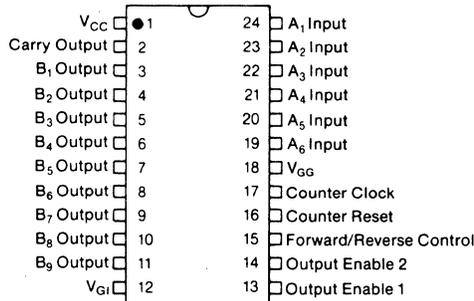
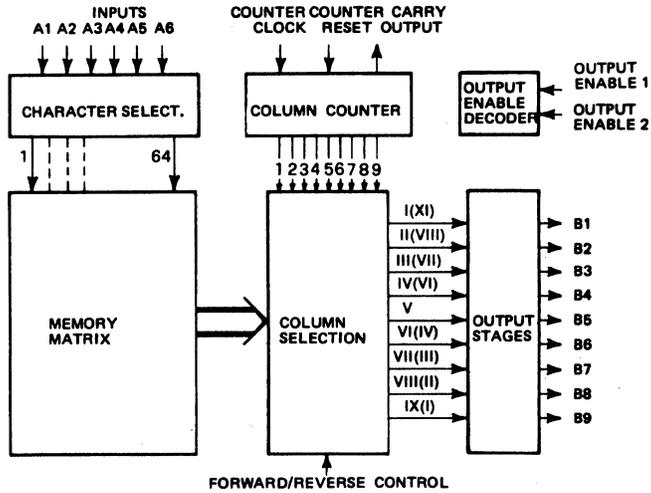
**C65**



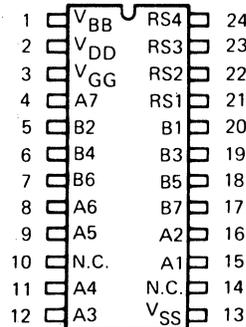
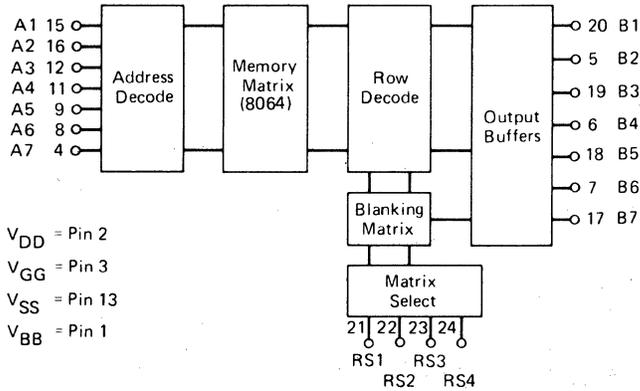
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**C66**



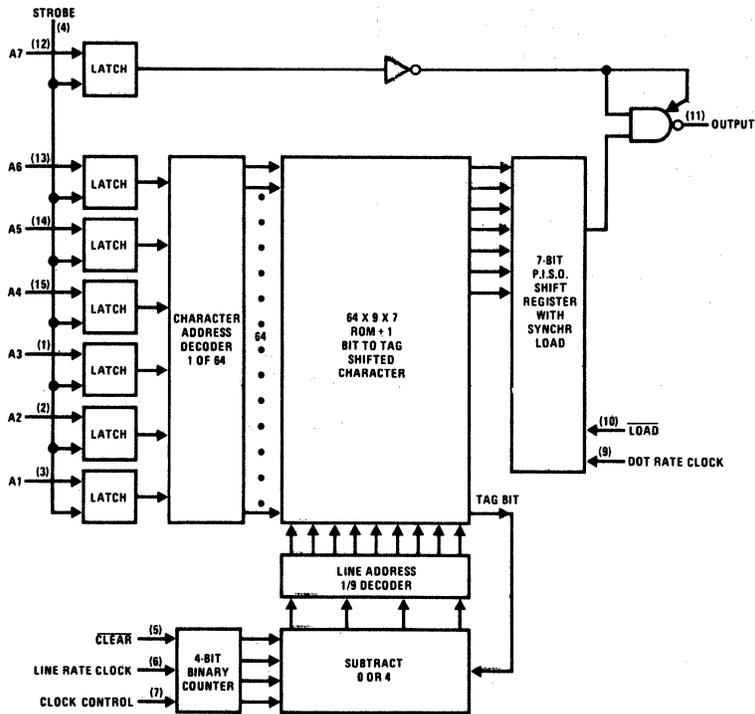
**C67**



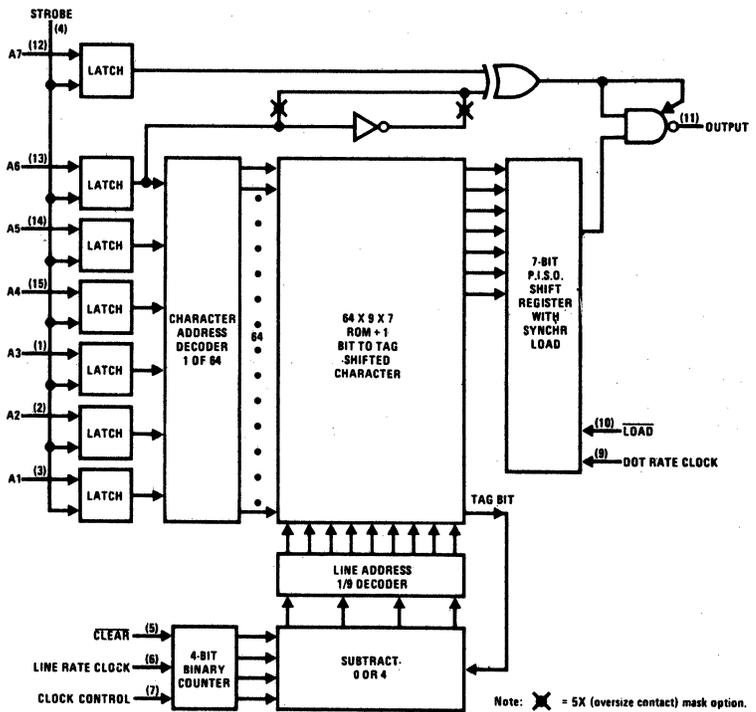
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

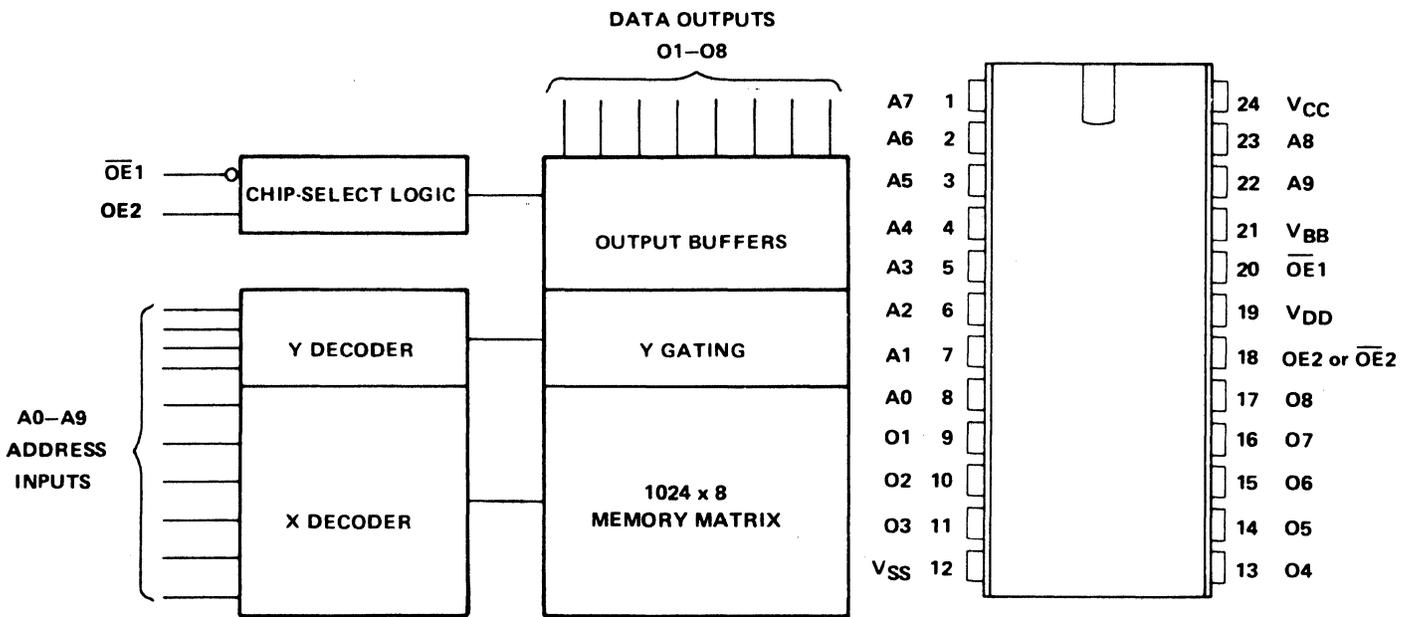
C68



C69



C70

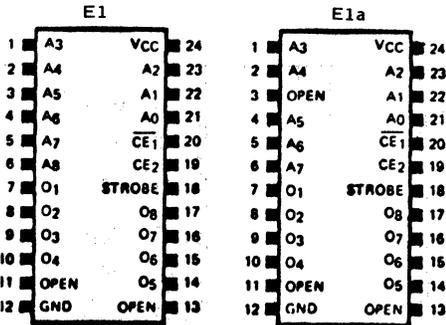


<div style="border: 2px solid black; width: 50px; height: 20px; margin-bottom: 10px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin-bottom: 10px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin-bottom: 10px;"></div>
<div style="border: 2px solid black; width: 50px; height: 20px; margin-bottom: 10px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin-bottom: 10px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin-bottom: 10px;"></div>

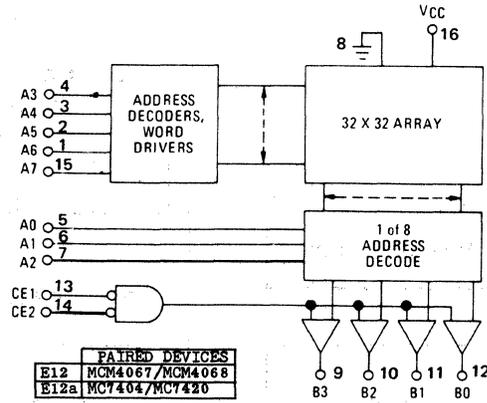
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

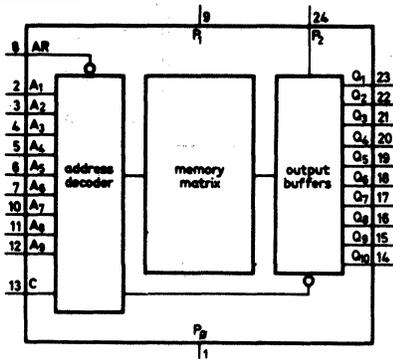
**E1**



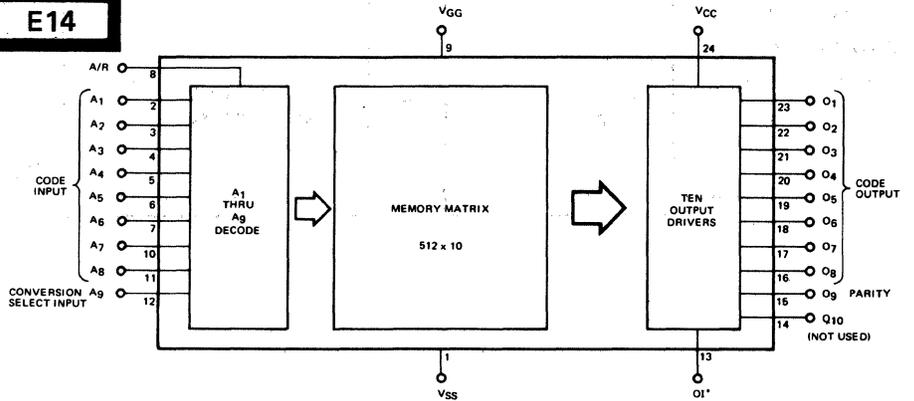
**E12**



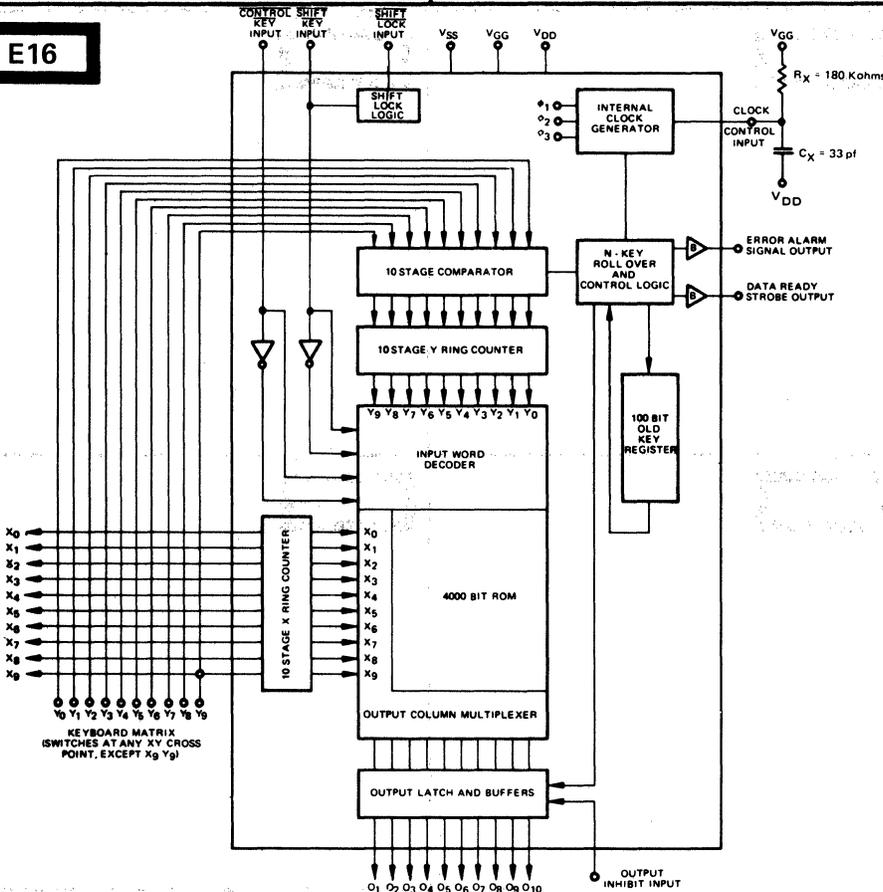
**E13**



**E14**



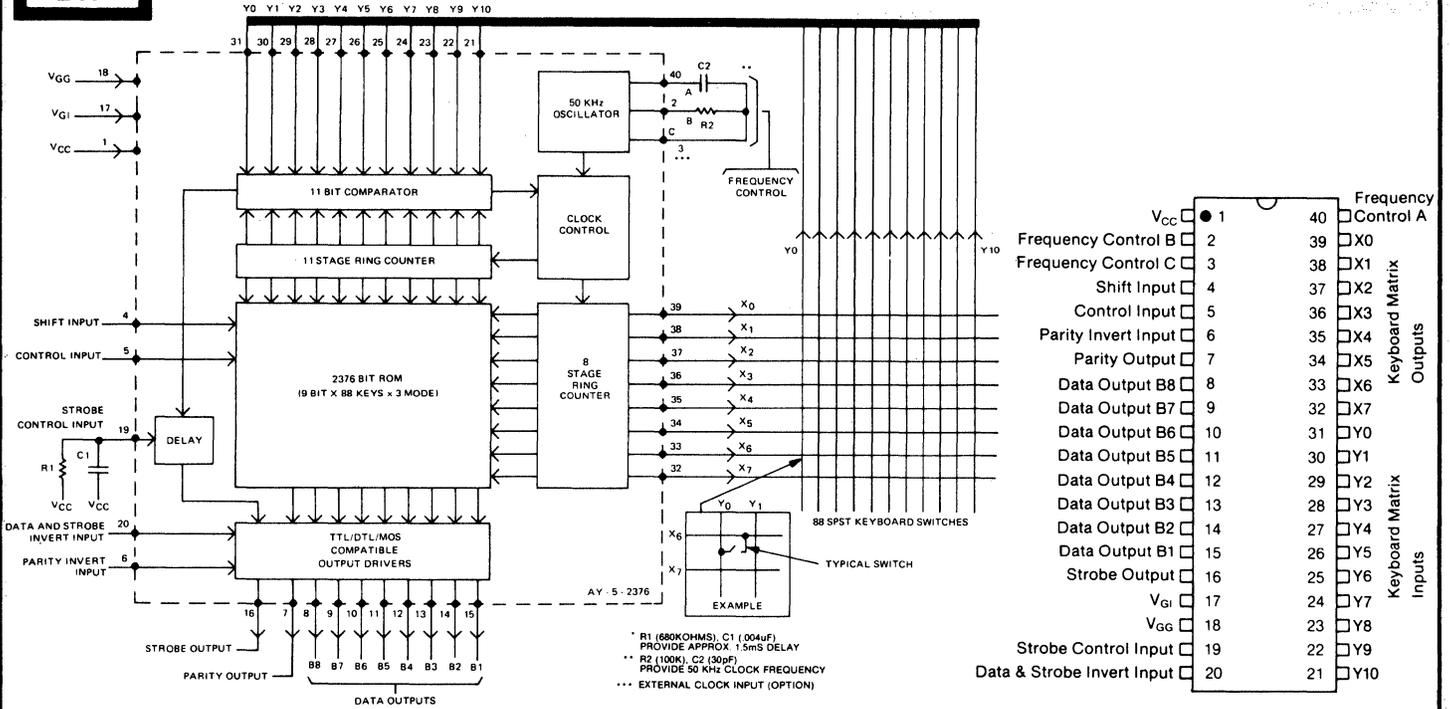
**E16**



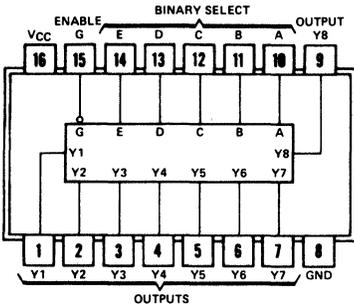
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**E17**



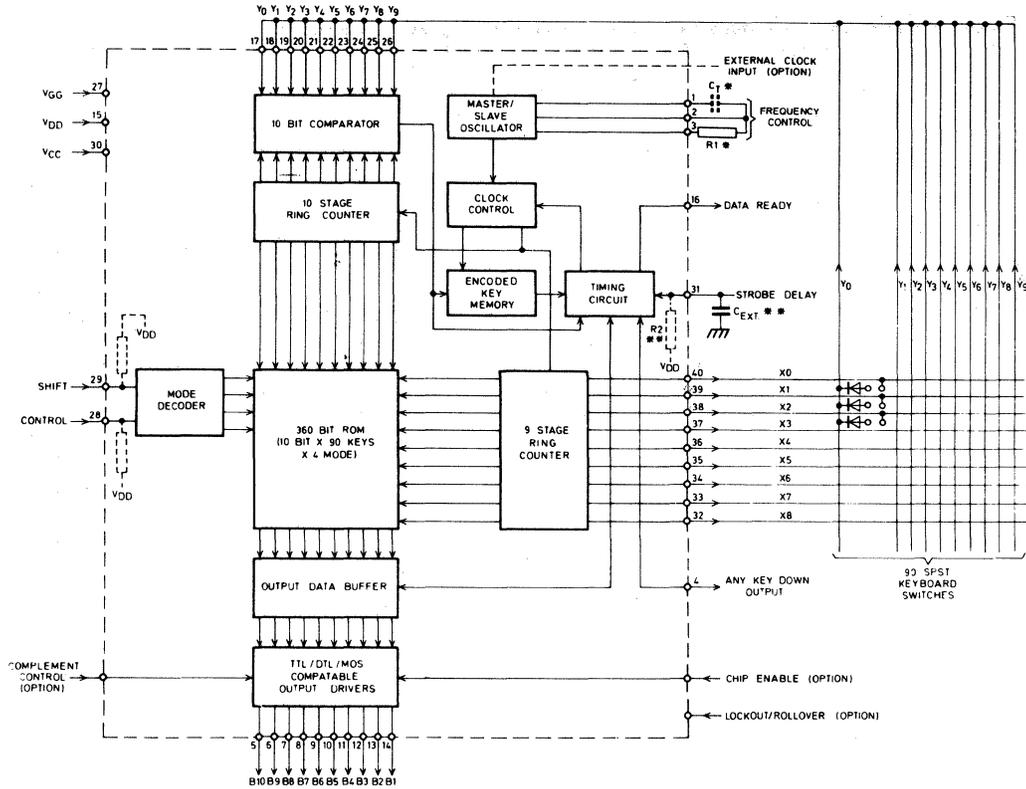
**E18**



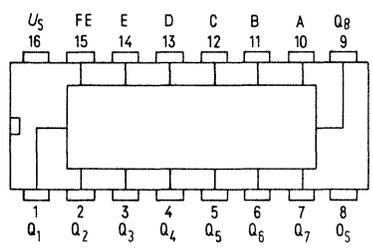
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**E19**



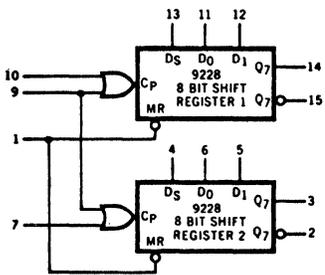
**E20**



# 22. LOGIC/BLOCK DRAWINGS

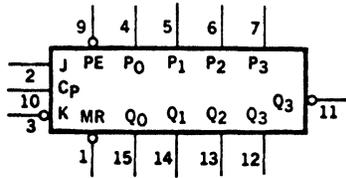
IN DRAWING NUMBER SEQUENCE

**F1**



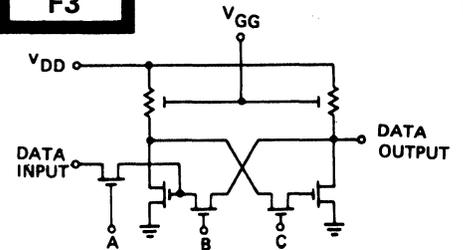
V<sub>CC</sub> = Pin 16  
Gnd = Pin 8

**F2**



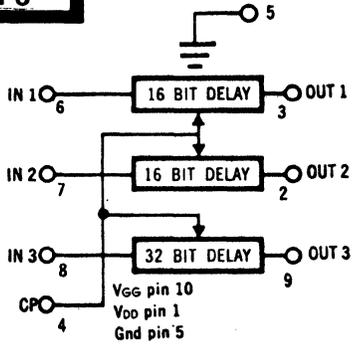
V<sub>CC</sub> = PIN 16  
GND = PIN 8

**F3**

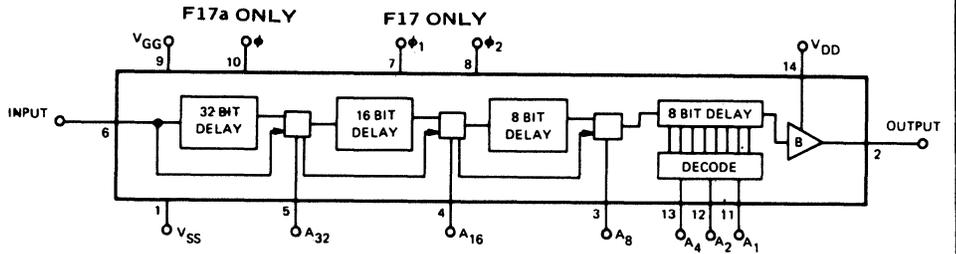


CKT No.	DATA INP	DATA OUTP	A	B	C	VDD	VGG	GND
F3	1	8	7	4	4	10	6	5
	2	1	9	4	4	10	6	5
	3	3	2	4	4	10	6	5
F3a	1	3	9	8	2	10	6	1, 5
	2	4	7	8	2	10	6	1, 5
F3b	1	2	1	8	7	10	6	5
	2	3	4	8	7	10	6	5

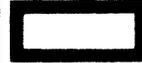
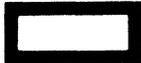
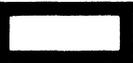
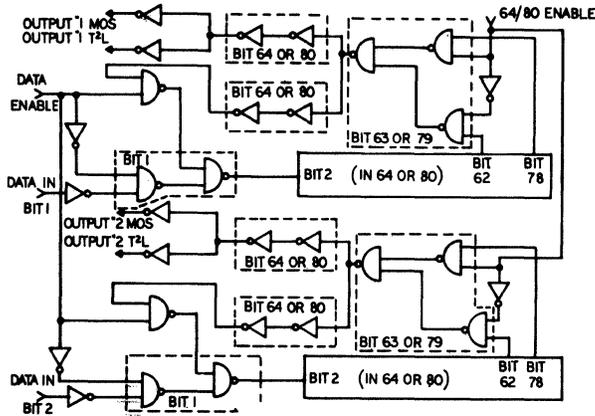
**F6**



**F17**



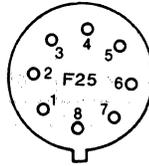
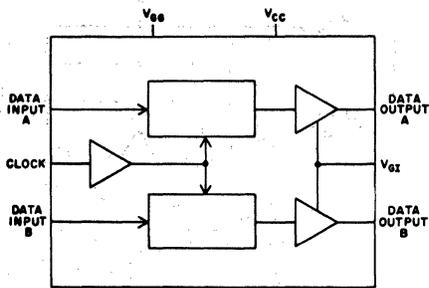
**F19**



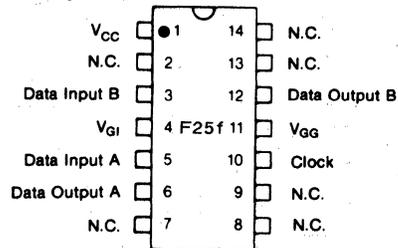
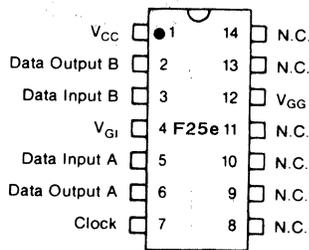
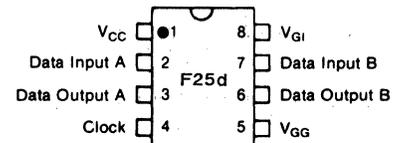
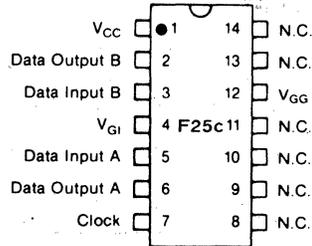
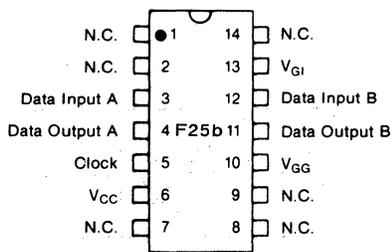
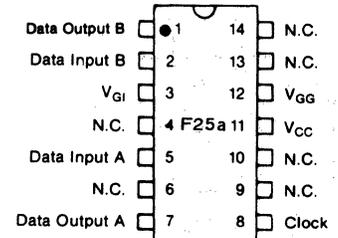
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F25**



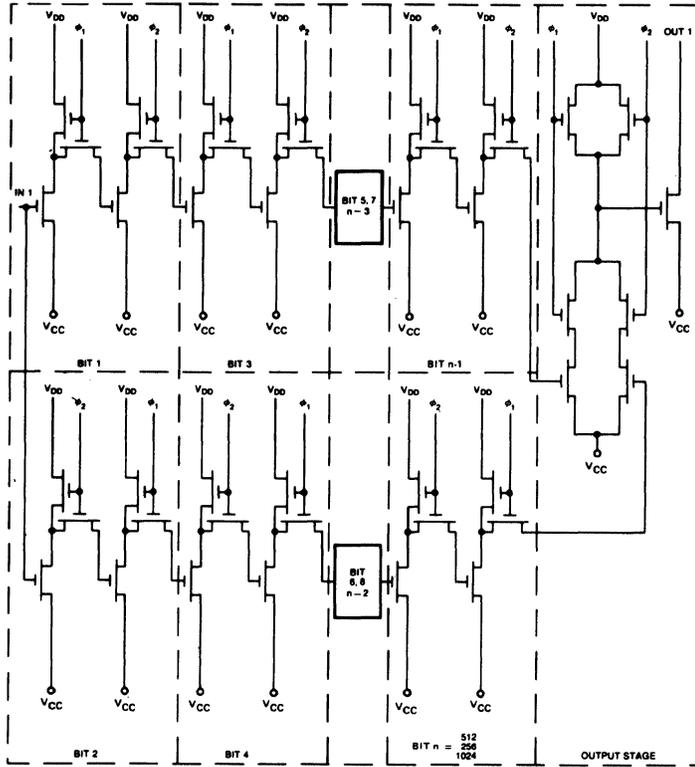
- 1 Data Input A
- 2 Data Output A
- 3 Clock
- 4 V<sub>CC</sub>
- 5 V<sub>GG</sub>
- 6 Data Output B
- 7 Data Input B
- 8 V<sub>GI</sub>



# 22. LOGIC/BLOCK DRAWINGS

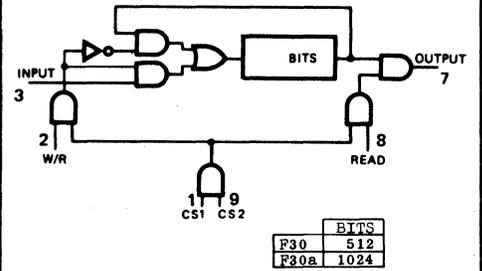
IN DRAWING NUMBER SEQUENCE

**F29**

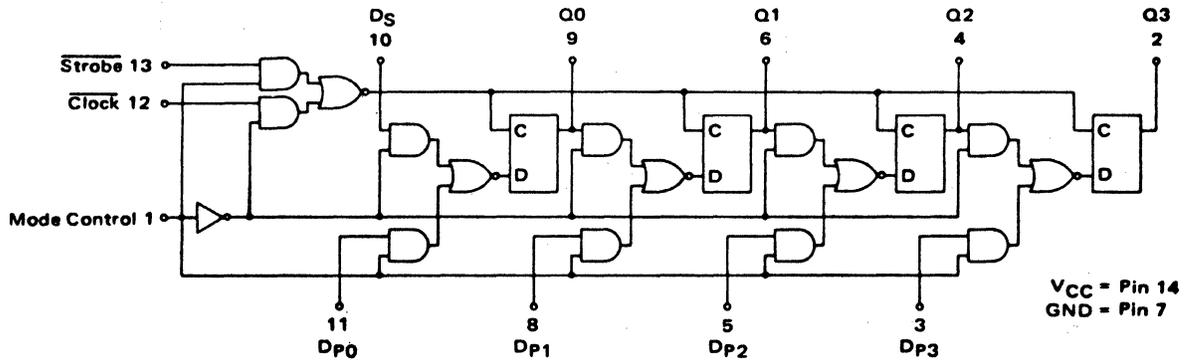


NO. OF BITS	COMMENT
F29 256	3 MORE IDENTICAL REGISTERS
F29a 512	1 MORE IDENTICAL REGISTER
F29b 1024	ONLY REGISTER

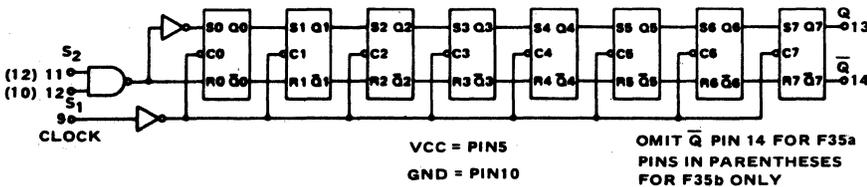
**F30**



**F31**



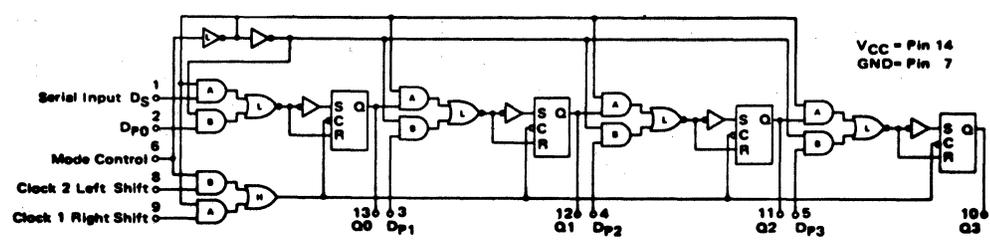
**F35**



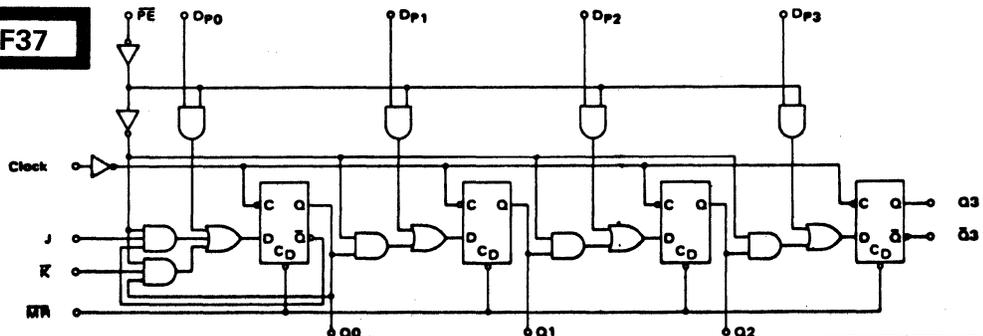
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F36**

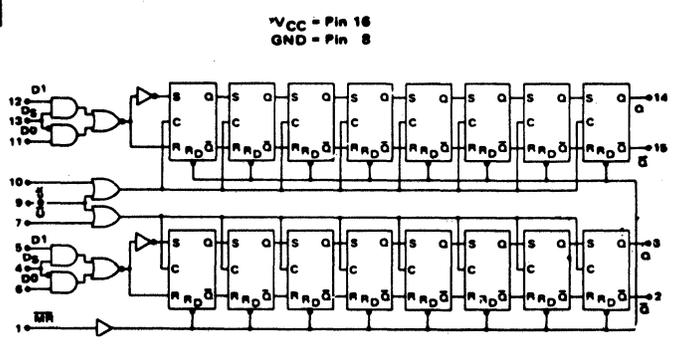


**F37**

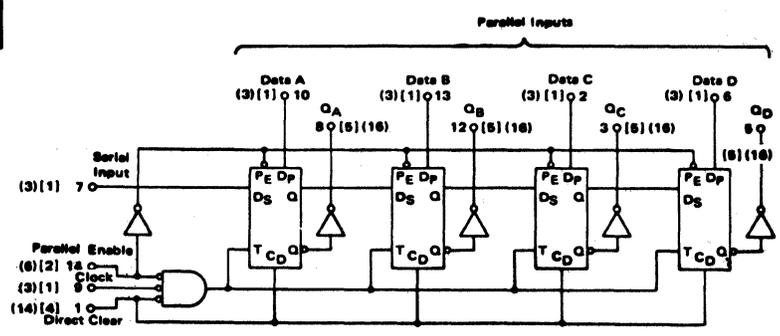


	CLOCK	J	K	MR	Q	DP	FE	VCC	GND
F37	10	2	3	1	15 14 13 12	11 4 5 6 7 9 16 8			
F37a	1	11	12	9	13 15 3 7	6 10 14 2 5 4 16 8			

**F38**



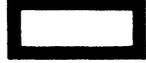
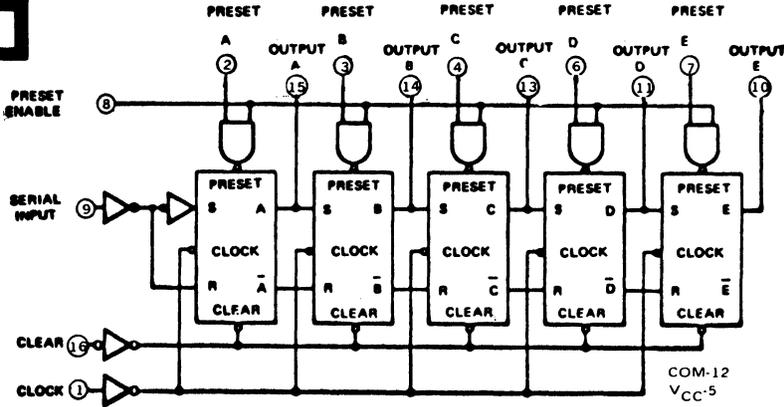
**F39**



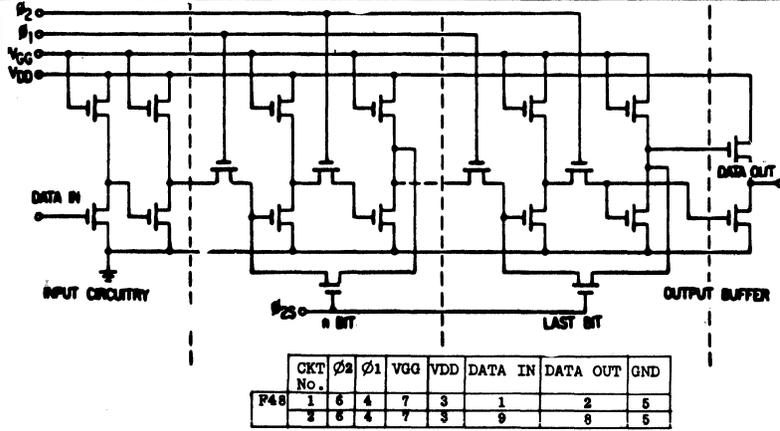
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

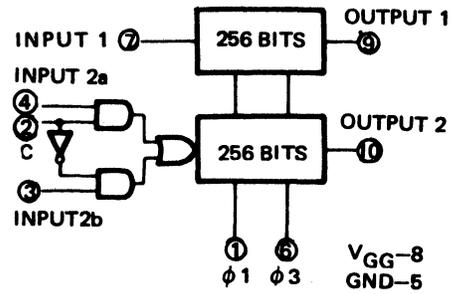
**F40**



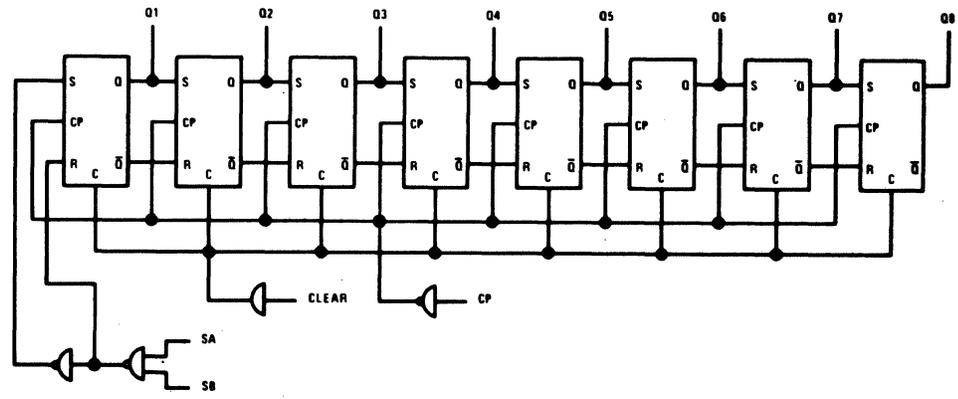
**F48**



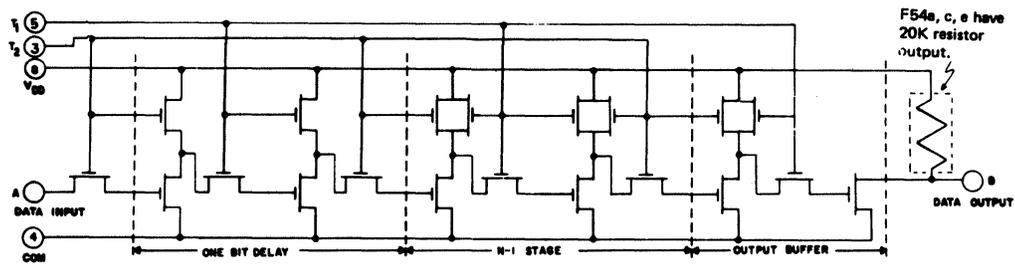
**F49**



**F52**



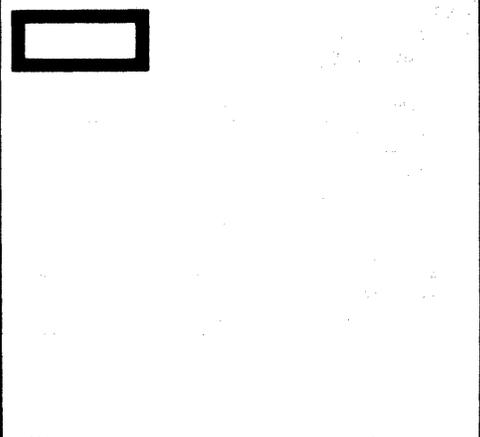
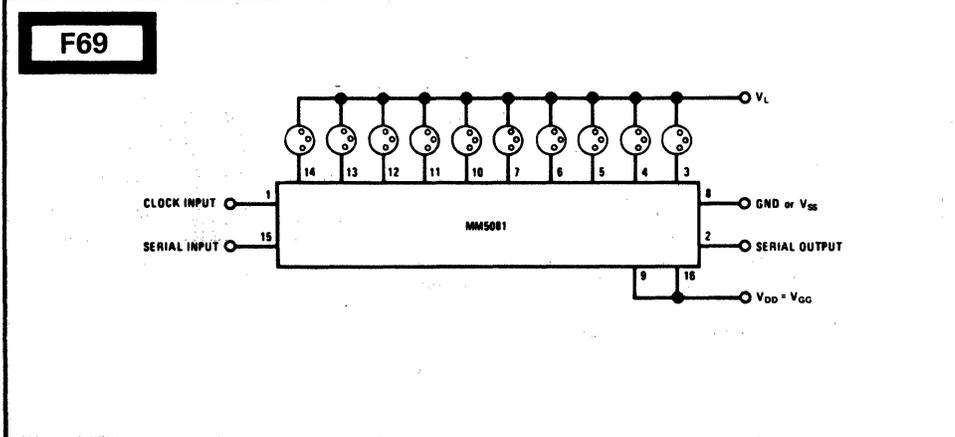
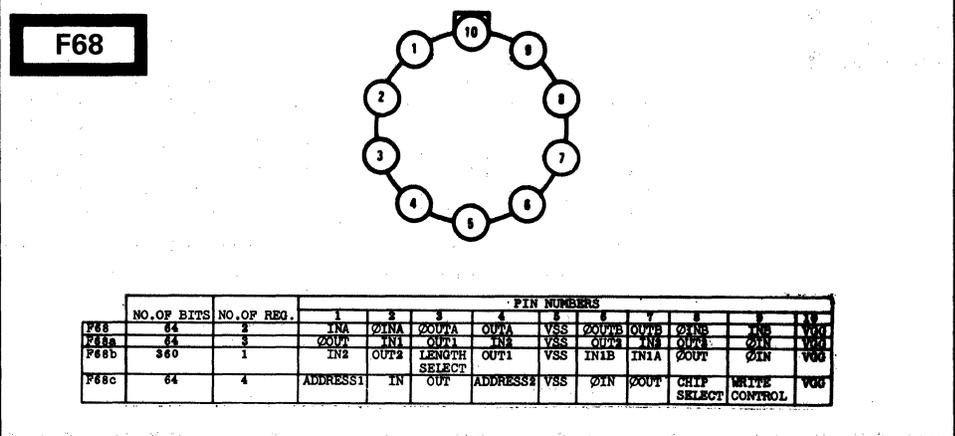
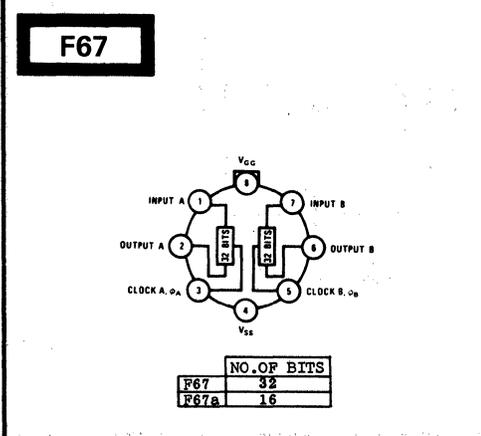
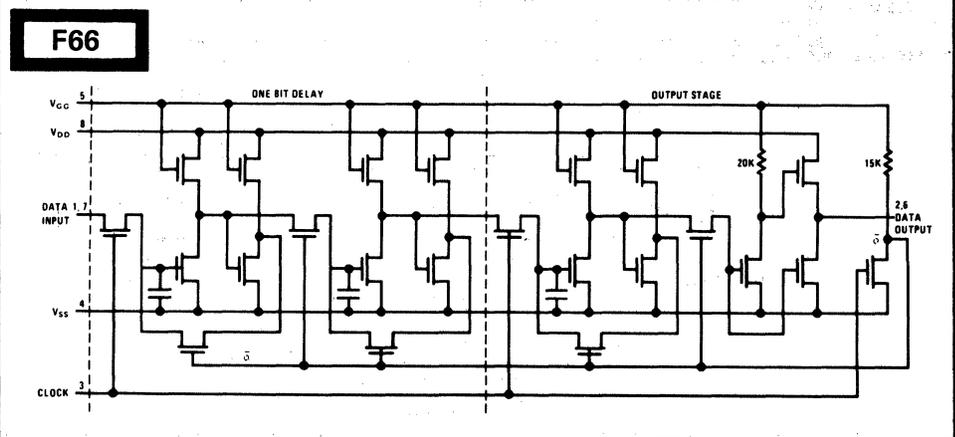
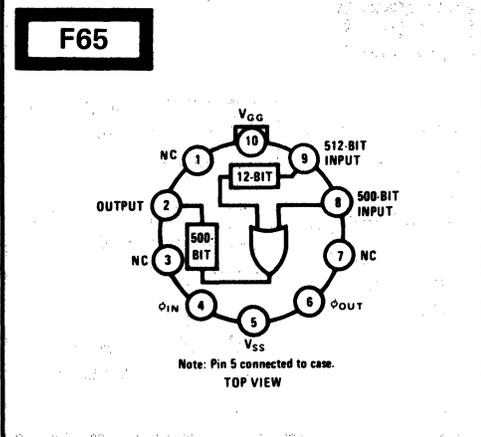
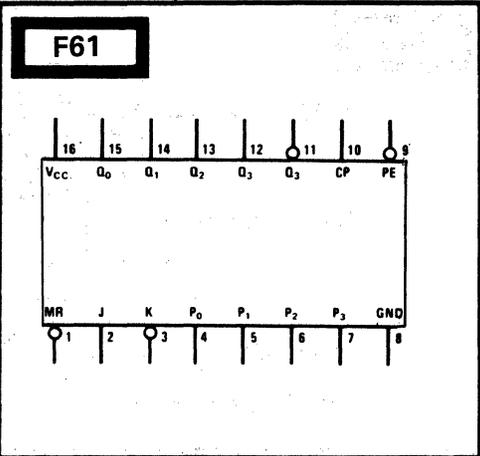
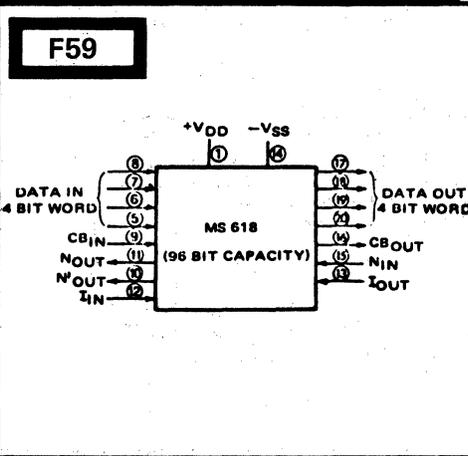
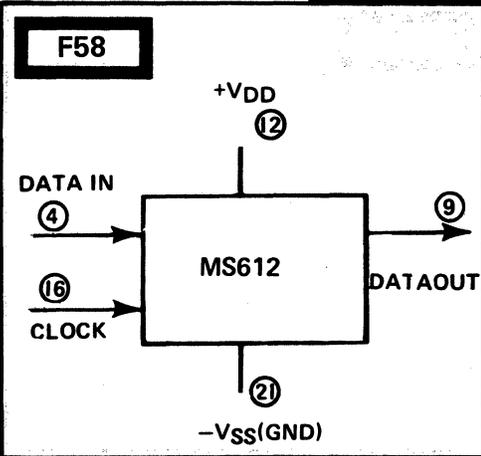
**F54**



CKT No.	A	B	NO. OF BITS	
F54, a	1	1	2	25
F54, c	2	7	6	50
F54, e	1	1	2	100
	2	7	6	

# 22. LOGIC/BLOCK DRAWINGS

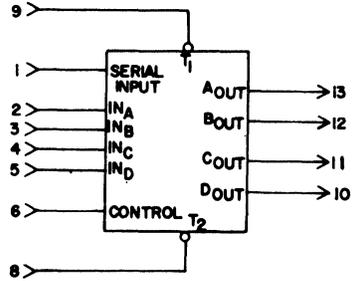
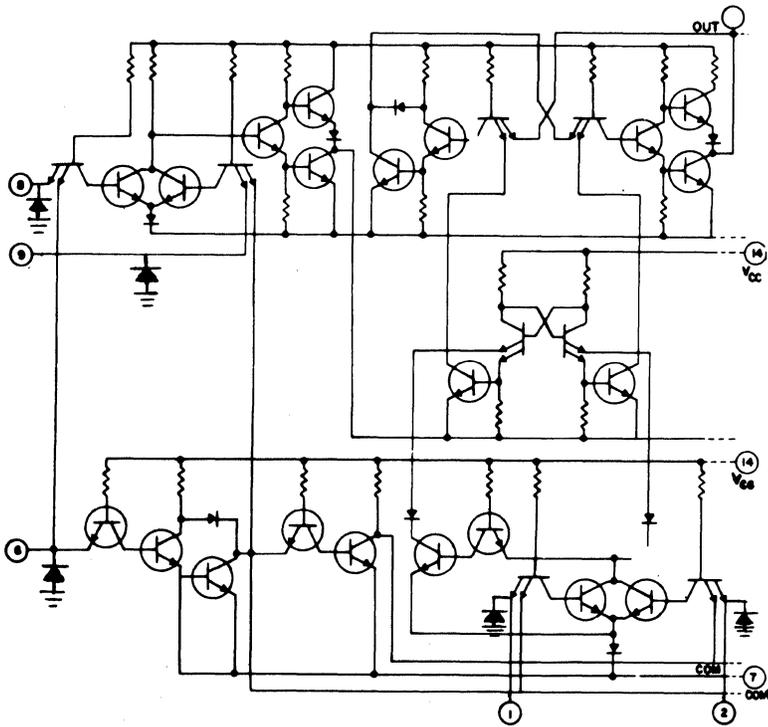
IN DRAWING NUMBER SEQUENCE



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

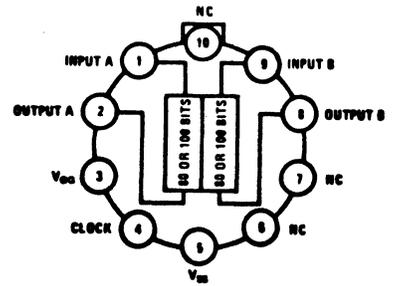
**F70**



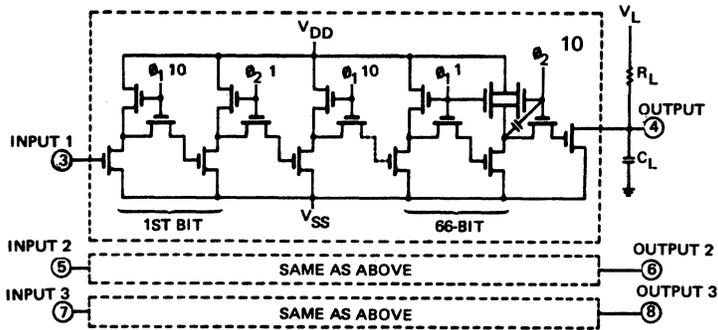
CLAMPING DIODES  
FOR F70a ONLY

**F71**

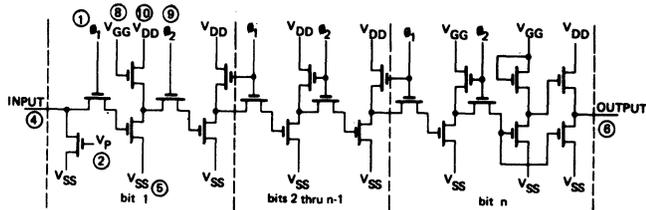
	No. OF BITS
F71	80
F71a	100



**F73**



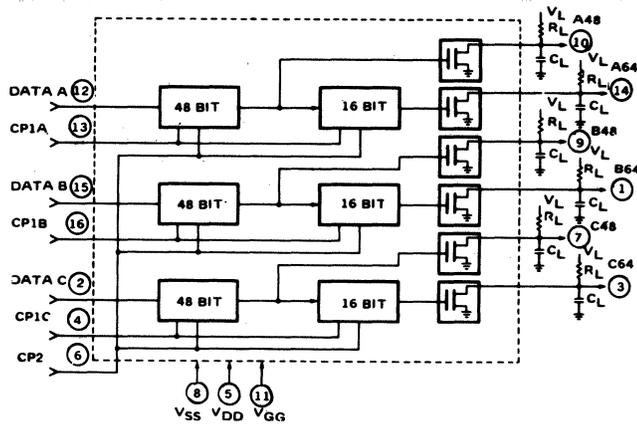
**F74**



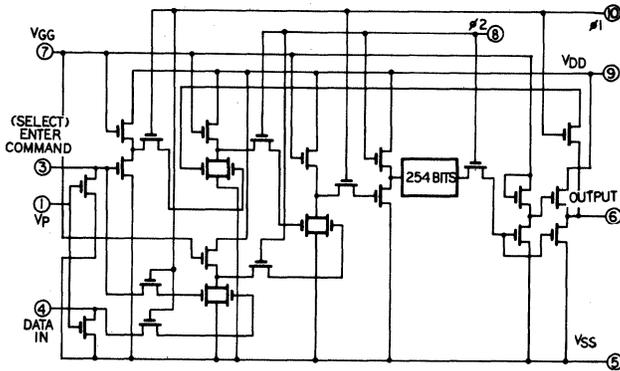
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

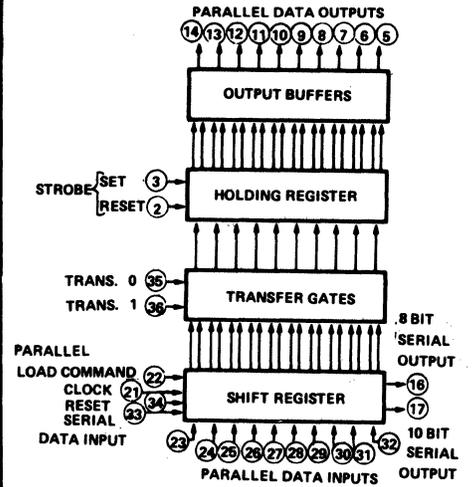
**F75**



**F76**

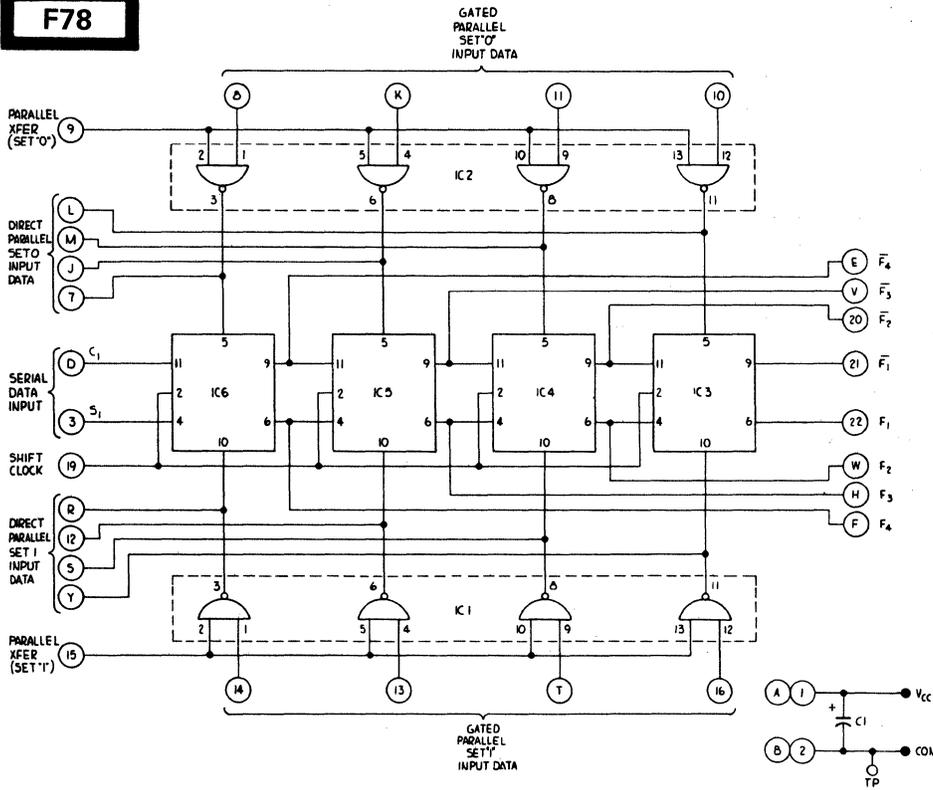


**F77**



VGG 18  
GND 19

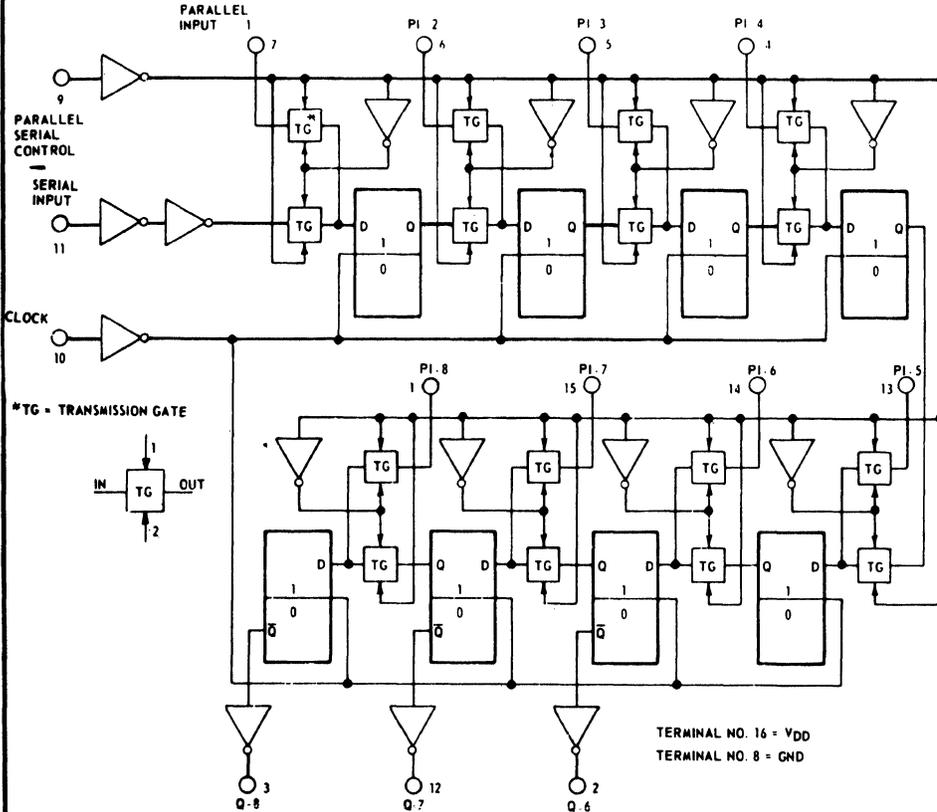
**F78**



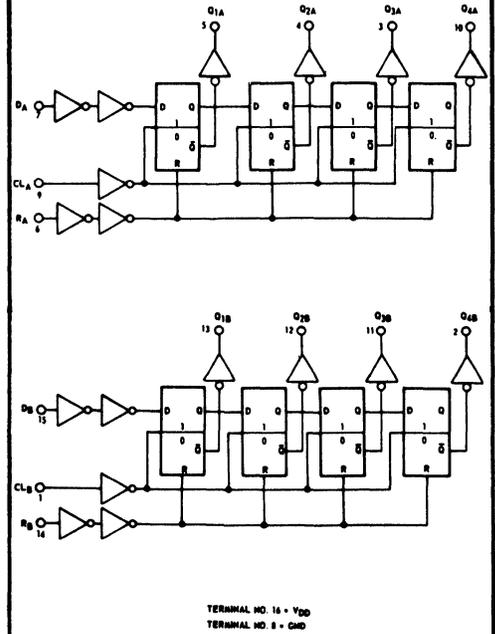
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

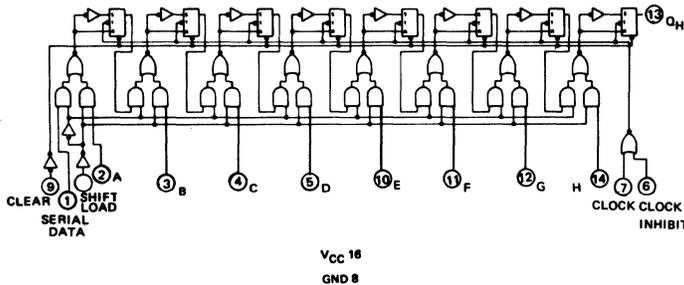
**F79**



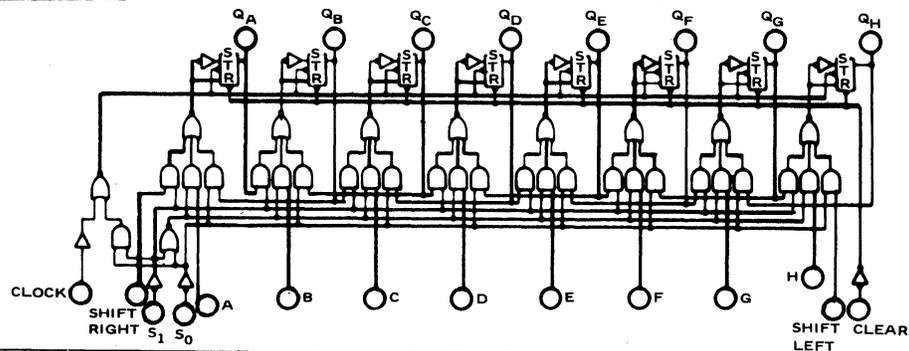
**F80**



**F88**



**F89**

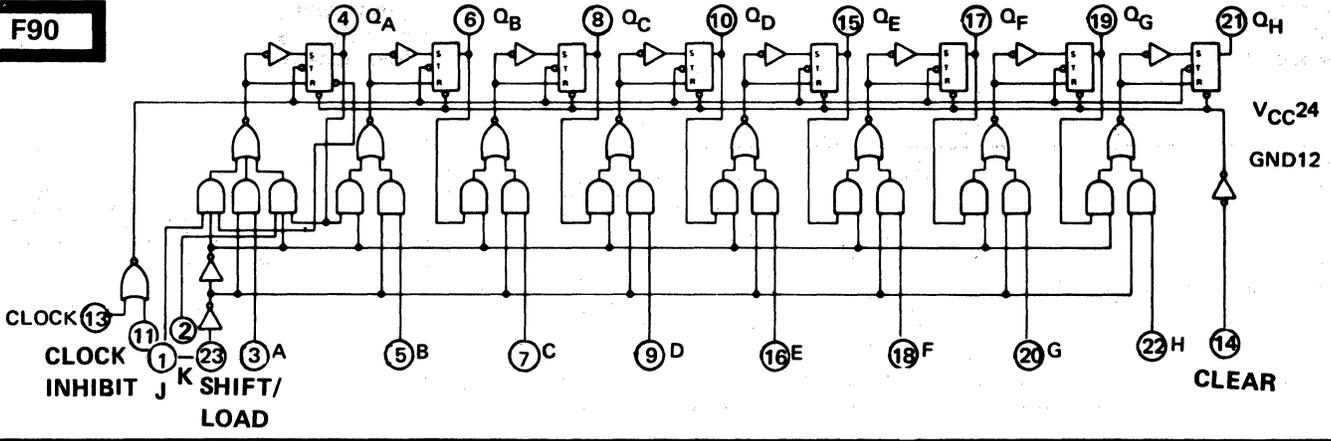


	NO. OF BITS	CLOCK	SHIFT R	S1	S0	Q								CLEAR	Q				VCC	GND					
						A	B	C	D	E	F	G	H		A	B	C	D							
F89	8	11	2	23	1	3	5	7	9	15	17	19	21	22	13	4	6	8	10	14	16	18	20	24	12
F89a	4	11	2	10	9	3	4	5	6					7	1	15	14	13	12					16	8

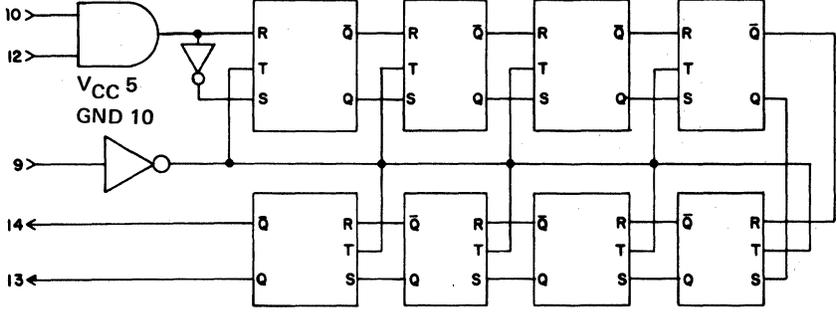
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F90**

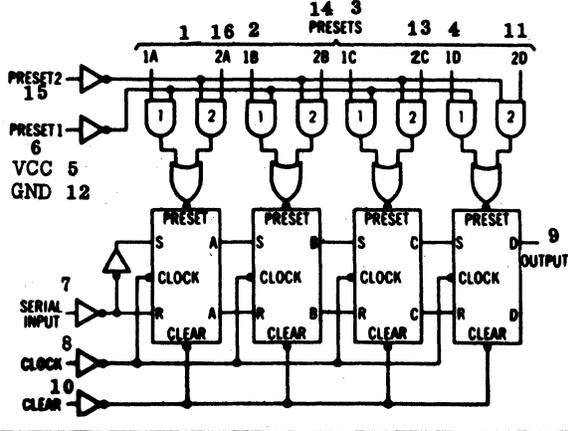


**F91**



\*FOR MP STYLE PACKAGE THIS TERMINAL IS PIN NO.11

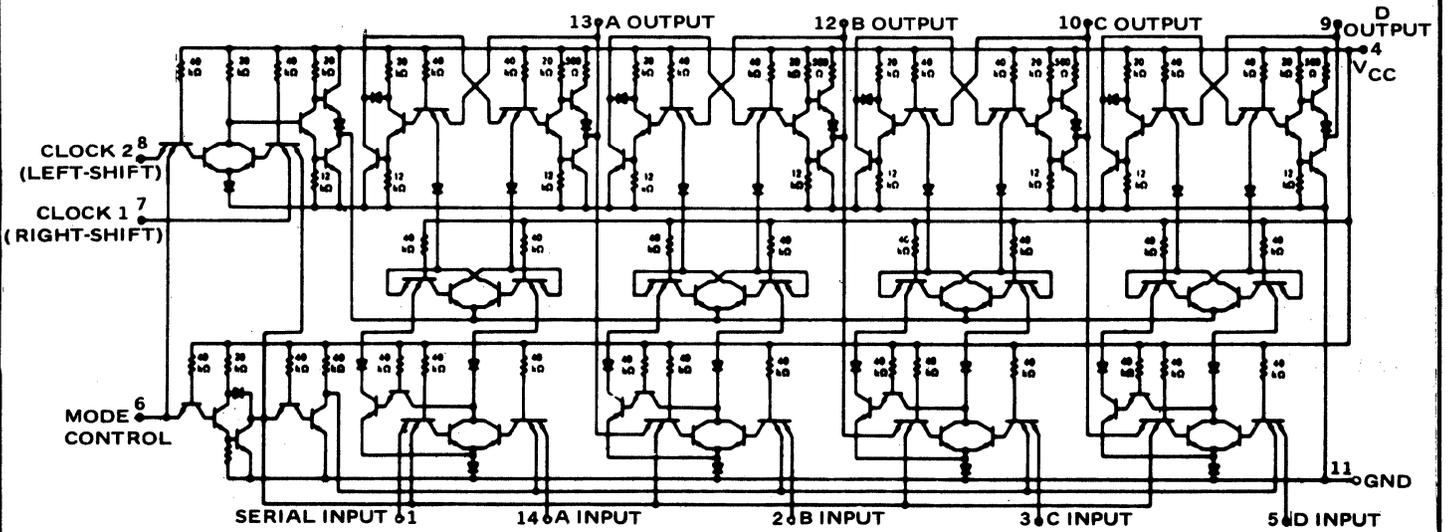
**F92**



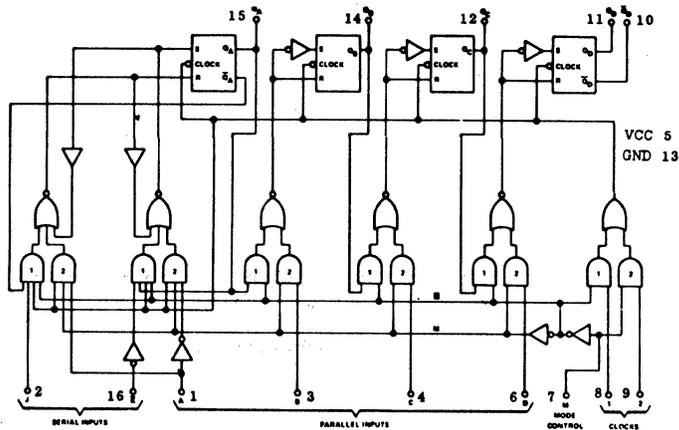
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

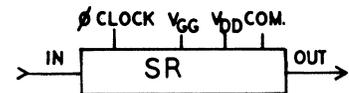
F93



F94



F95

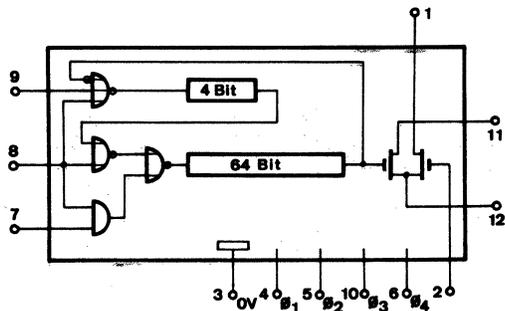


	CKT	NO. OF BITS	IN	OUT	VGG	VDD	COM
F95	1	25	1	2	4	7	3 5
	2	25	9	8	6	7	3 5
F95a	1	32	1	2	4	7	3 5
	2	32	9	8	6	7	3 5
F95b	1	50	1	2	4	7	3 5
	2	50	9	8	6	7	3 5
F95c	1	100	1	2	4	7	3 5
	2	100	9	8	6	7	3 5
F95d	1	128	1	2	4	7	3 5
	2	128	9	8	6	7	3 5
F95e	1	16	3	9	8	6	10 5
	2	16	4	7	2	6	10 5
F95f	1	100	1	2	5		8 4
	2	100	7	6	3		8 4
F95g	1	25	6	3	7	12	1 4
	2	25	8	2	7	12	1 4
	3	25	9	14	7	12	1 4
	4	25	11	13	7	12	1 4
F95h	1	64	1	2	3	5	4 8
	2	64	7	6	3	5	4 8

# 22. LOGIC/BLOCK DRAWINGS

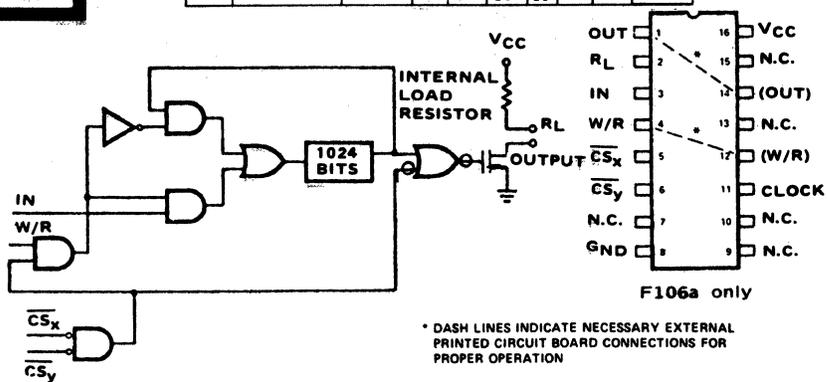
IN DRAWING NUMBER  
SEQUENCE

**F104**

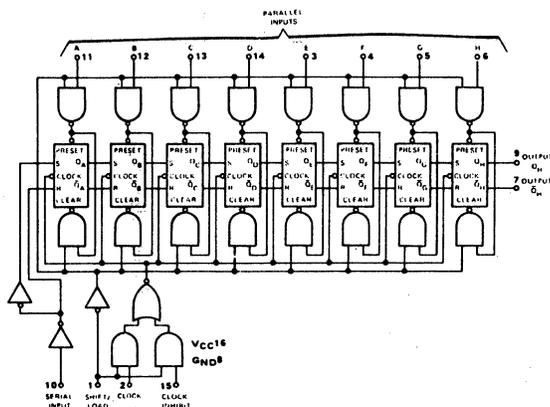


**F106**

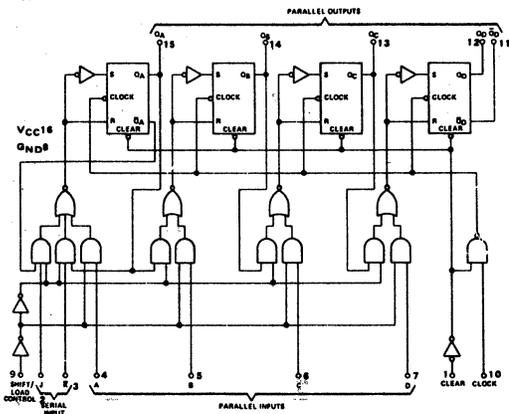
F106	NC.	OF	CKTS	IN	W/R	ESX	CSY	OUT	RL	VCC	GND	CLOCK
	1			3	4	5	6	1	2	16	8	11
	2			15	12	5	6	14	13			



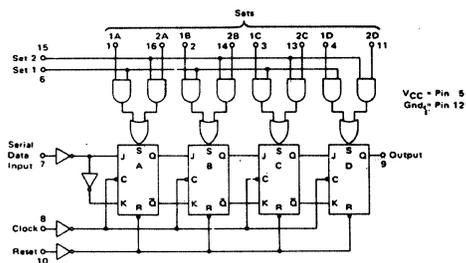
**F107**



**F108**



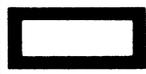
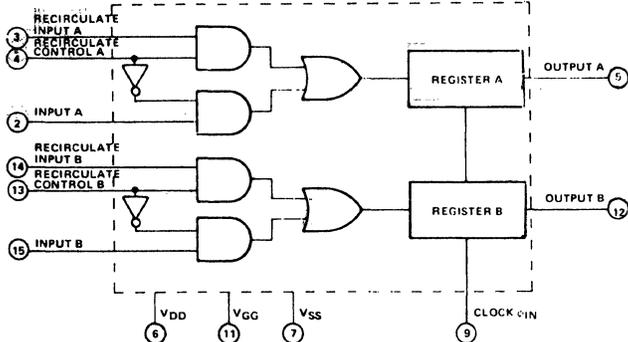
**F110**



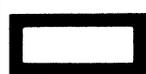
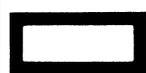
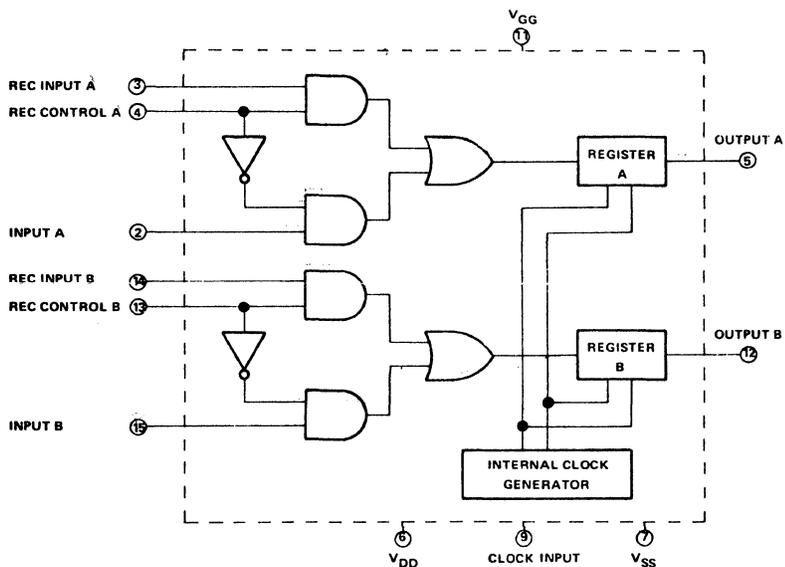
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

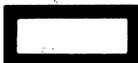
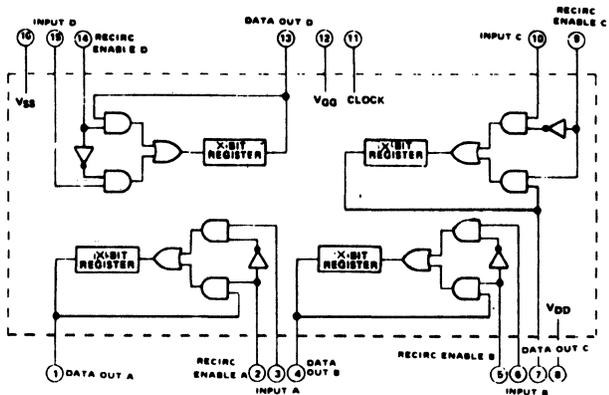
**F113**



**F115**



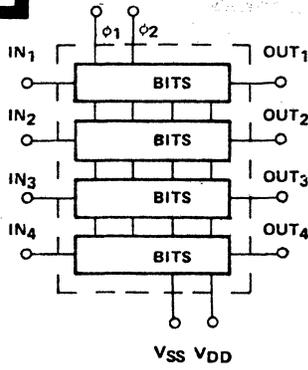
**F119**



# 22. LOGIC/BLOCK DRAWINGS

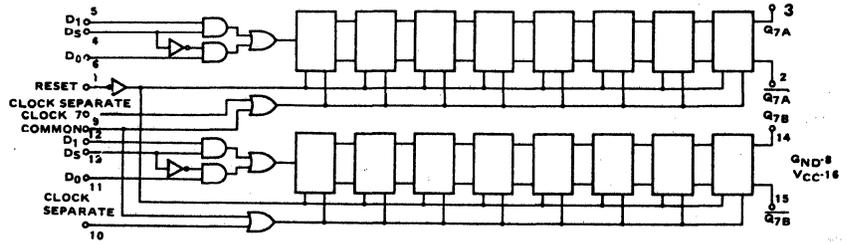
IN DRAWING NUMBER  
SEQUENCE

**F120**

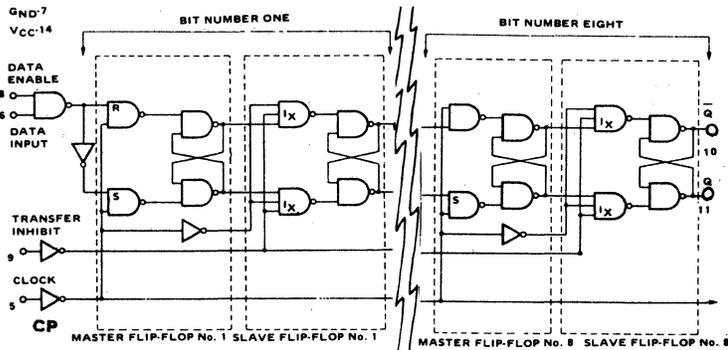


	REG	BITS	IN	OUT	Ø1	Ø2	VSS	VDD	GND
F120	1	256	3	1	4	11	5	12	
	2		8	6					
	3		10	9					
	4		16	14					
F120a	1	512	2	1	3	7	4	8	
F120b	1	1024	2	5	3	7	4	8	
F120c	1	512	3	14	4	11	5	12	
	2		10	6					
F120d	1	1024	3	6	4	11	5	12	
F120e	1	64	1	2	14	5	7	8	13
	2		3	4					
	3		9	10					
	4		11	12					
F120f	1	2	1	2	5	3	4	8	
	2		7	6					
F120g	1	256	1	2	5	3	4	8	
F120h	1	100	1	2	5	3		8	4
	2		7	6					
F120j	1	32	1	2	3		5	8	4
	2		7	6					
F120k	1	512	2	9	4	6	5	10	

**F122**



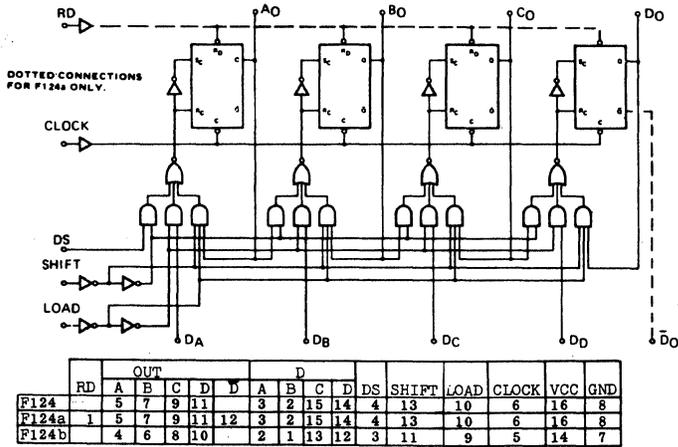
**F123**



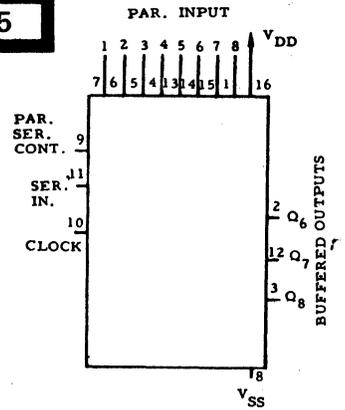
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

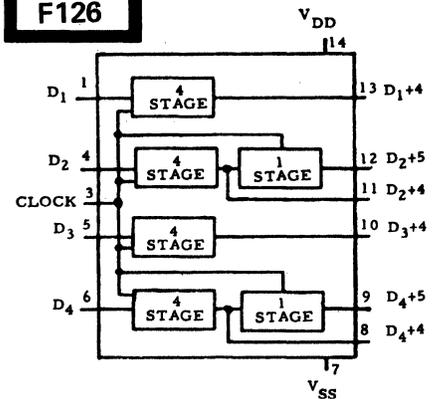
**F124**



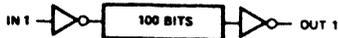
**F125**



**F126**



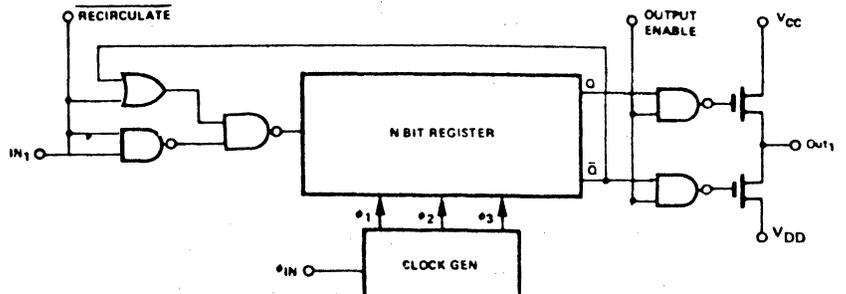
**F127**



	PKG	CKT	IN	OUT	CLOCK IN	CLOCK OUT	VCC	VDD
F127	CY	1	1	2	3	5	4	8
F127a	ML	2	7	6	3	5	4	8
		1	5	6	7	1	8	4
		2	3	2	7	1	8	4

**F128**

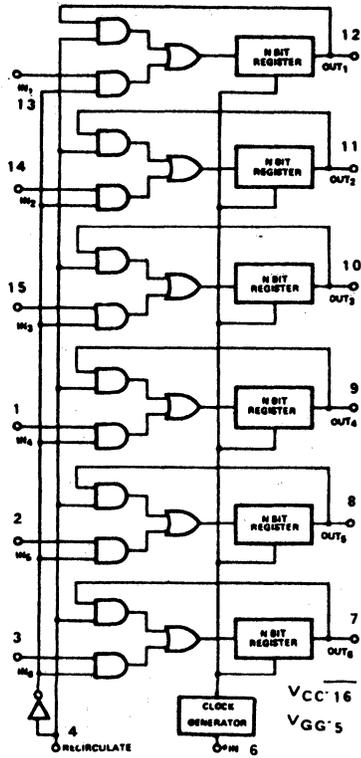
	PKG	CKT	RECIRCULATE	IN	OUT	IN	OUTPUT ENABLE	VCC	VDD	VGG
F128	ML	1	1	2	3	8	9	14	7	10
		2		13	12					
F128a	CY	1	10	1	2	4	6	5	3	7
		2		9	8					



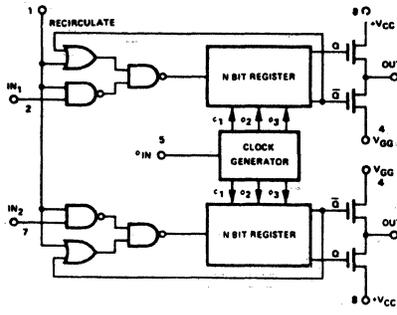
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

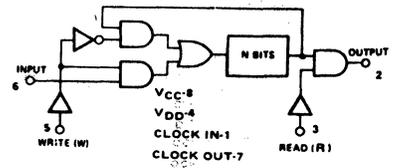
**F129**



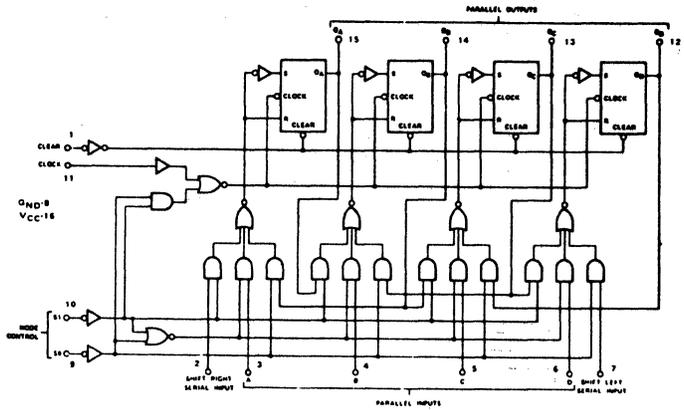
**F130**



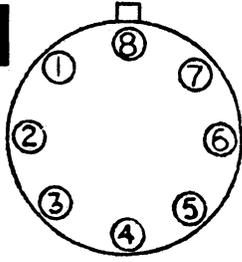
**F131**



**F132**

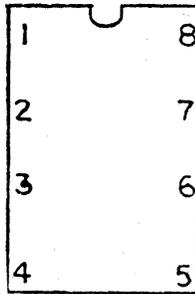


**F133**



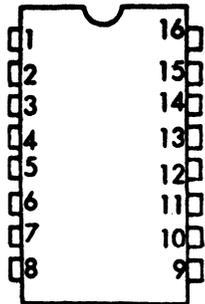
		PIN NUMBERS							
		1	2	3	4	5	6	7	8
F133	O1	In1	O1	VCC	O2	In2	O2	VDD	
F133a	NC	In	O1	VCC	OUT	NC	O2	VDD	
F133b	INA	OUTA	O1n	VSS	OUTB	INB	INB	VGG	
F133c	NC	IN	O1	VSS	OUT	RES	O2	VDD	
F133d	Inp1	OUTP1		VSS	VGG	OUTP2	Inp2	VDD	

**F134**



		PIN NUMBERS							
		1	2	3	4	5	6	7	8
F134	O1	In1	O1	VCC	O2	In2	O2	VDD	
F134a	NC	In	O1	VCC	OUT	NC	O2	VDD	
F134b	NC	OUT	O1n	VSS	OUTA	In	In	VGG	
F134c	O2	O2	VDD	O1	O1	O1	VCC		
F134d	OUT	O2	NC	VDD	In	O1	NC	VCC	
F134e	RECIRCULATE	In1	OUT1	VGG	O2	OUT2	In2	VCC	

**F140**

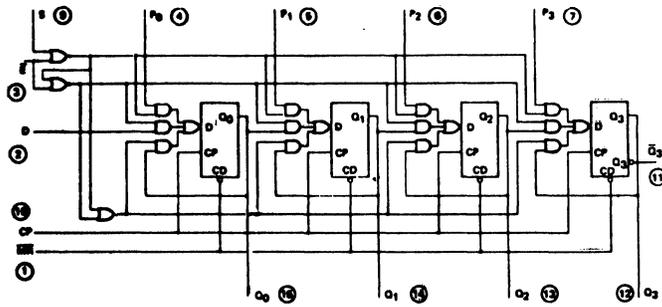


		PIN NUMBERS															
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
F140	OUT1	NC	In1	O1	VCC	OUT2	NC	In2	OUT3	In3	O2	VDD	NC	OUT4	NC	In4	
F140a	OUT	NC	InA	OUTA	InB	OUTB	NC	VSS	NC	InC	OUTC	InD	OUTD	NC	O1n	VGG	
F140b	OUT	NC	InA	OUTA	InB	OUTB	NC	VSS	NC	NC	OUTB	InC	OUTC	NC	O1n	VGG	
F140c	O12	D11	I1	I1	Y2	I2	O21	GND	X	O22	I3	Y3	I4	I4	VDD	I	
F140d	I1	O1	O1	I2	O2	O2	I3	GND	C3	O3	O2	O1	O3	O3	O1	VDD	
F140e	CLEAR	Q1	Q1	D1	D2	Q2	Q2	GND	CLOCK	Q3	Q3	D3	D4	Q4	Q4	VCC	

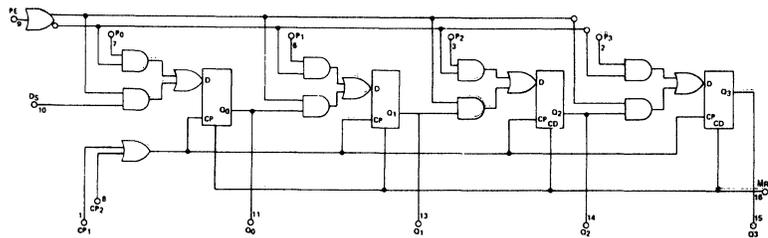
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

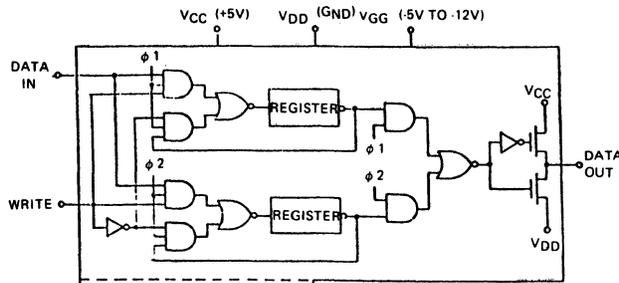
**F141**



**F142**

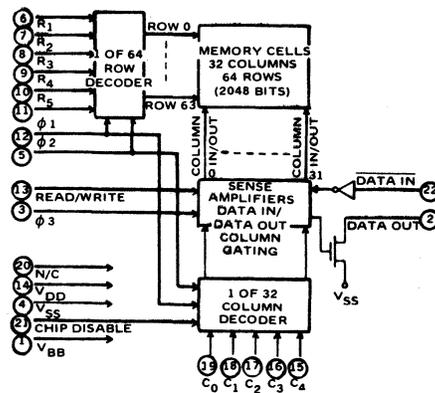


**F147**



	PKG	DATA IN				DATA OUT				CLOCK	WRITE	VCC	VGG	VDD
		1	2	3	4	1	2	3	4			1	5	10
F147	ML	3	6	9	12	2	7	8	14	4	13	1	6	10
F147a	CN	3	NC	NC	NC	5	NC	NC	NC	10	2	1	9	8
F147b	CN	3	6	NC	NC	4	5	NC	NC	10	2	1	9	8
F147c	ML	6	NC	NC	NC	8	NC	NC	NC	2	4	1	14	13
F147d	ML	6	9	NC	NC	7	8	NC	NC	2	4	1	14	13

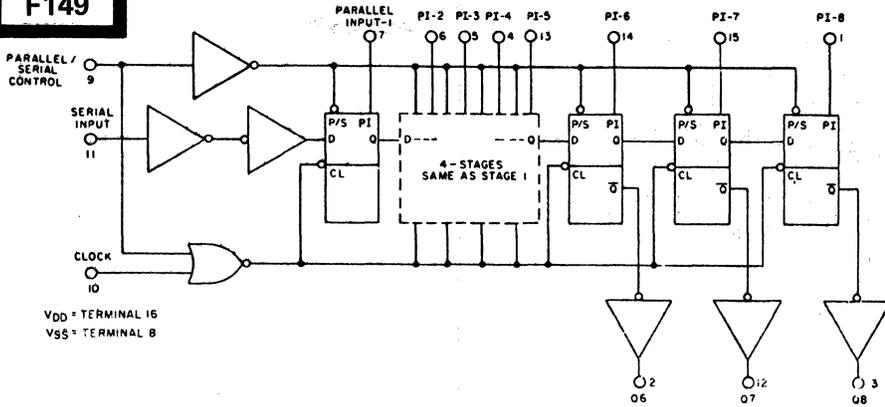
**F148**



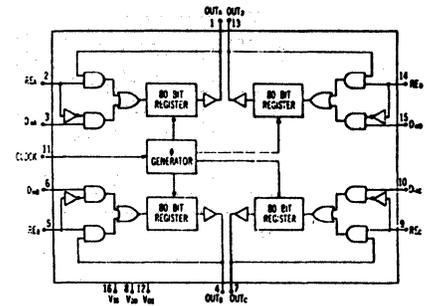
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

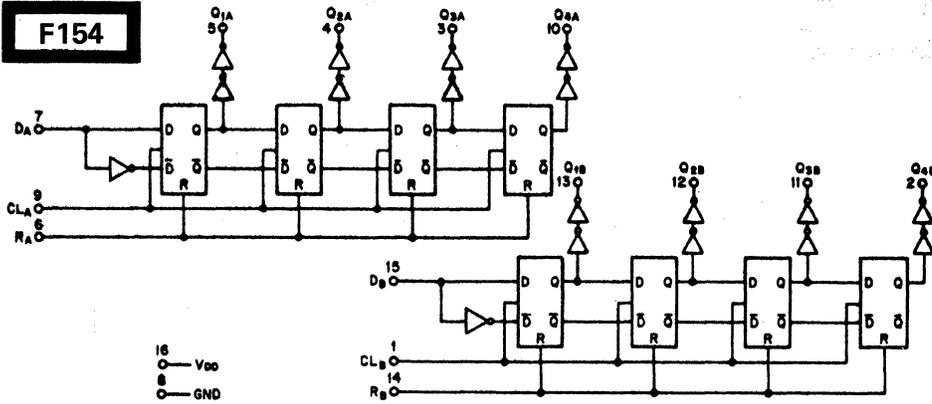
**F149**



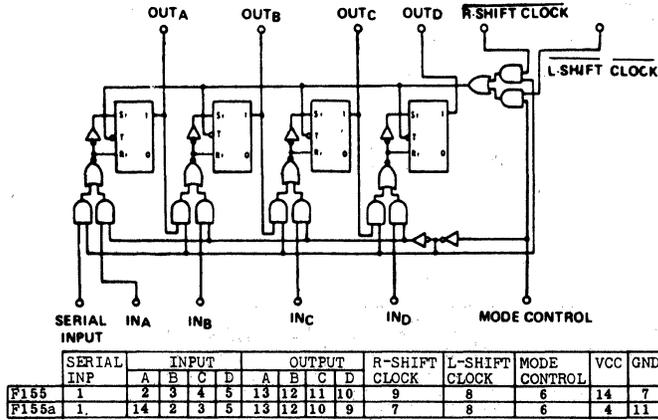
**F152**



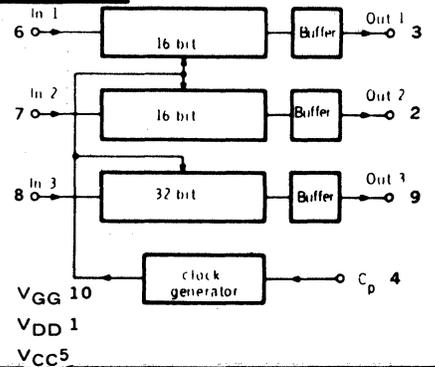
**F154**



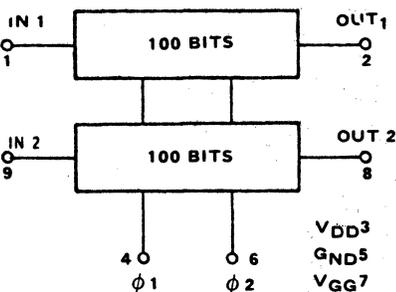
**F155**



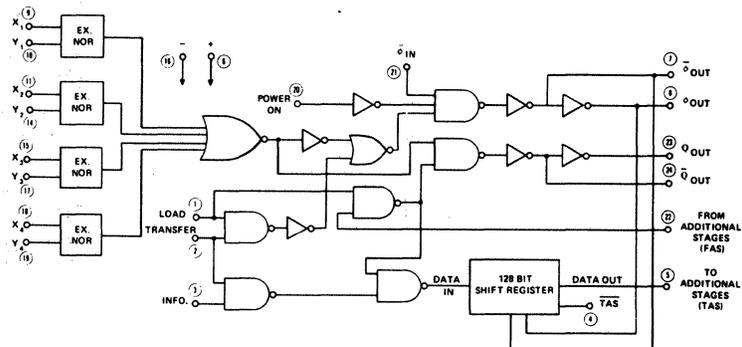
**F163**



**F164**



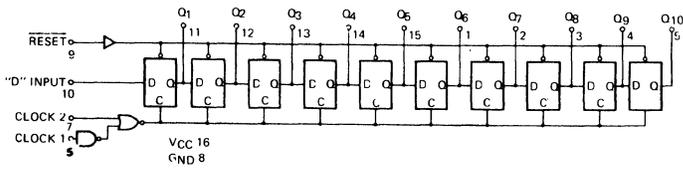
**F165**



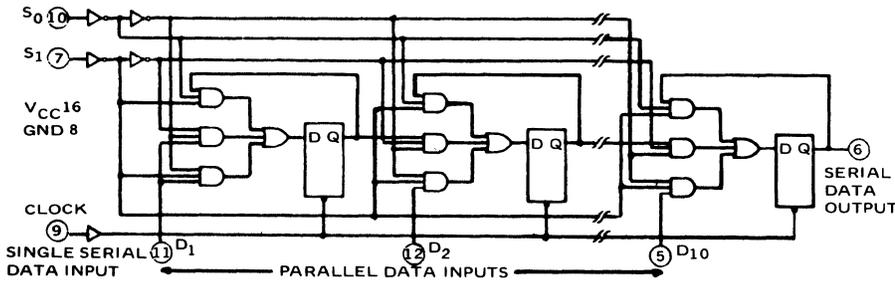
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

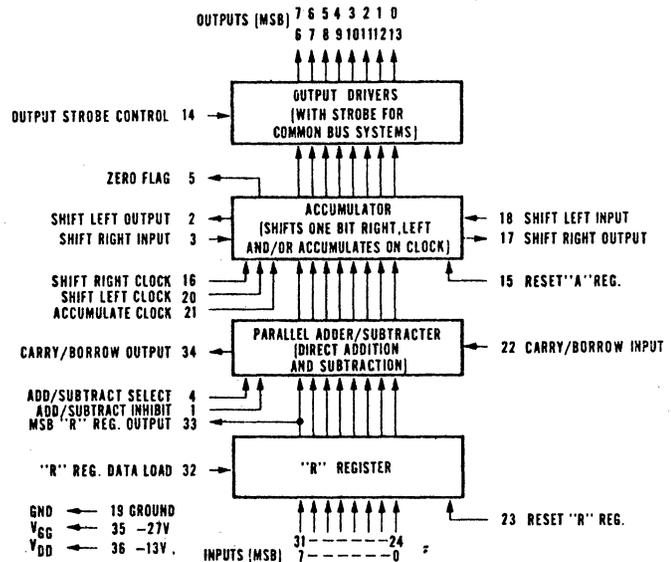
**F167**



**F168**



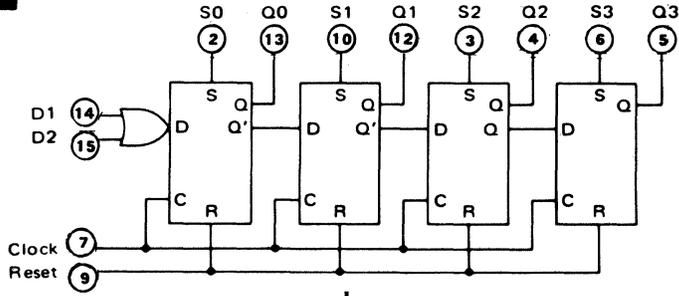
**F171**



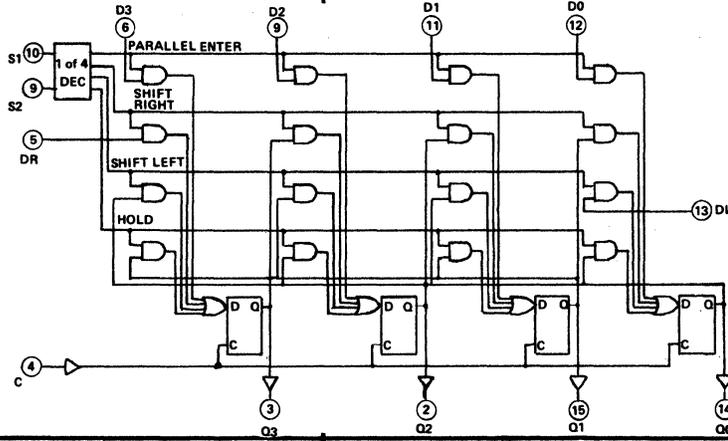
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

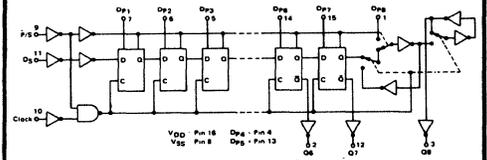
**F173**



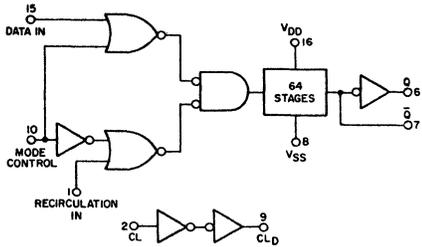
**F174**



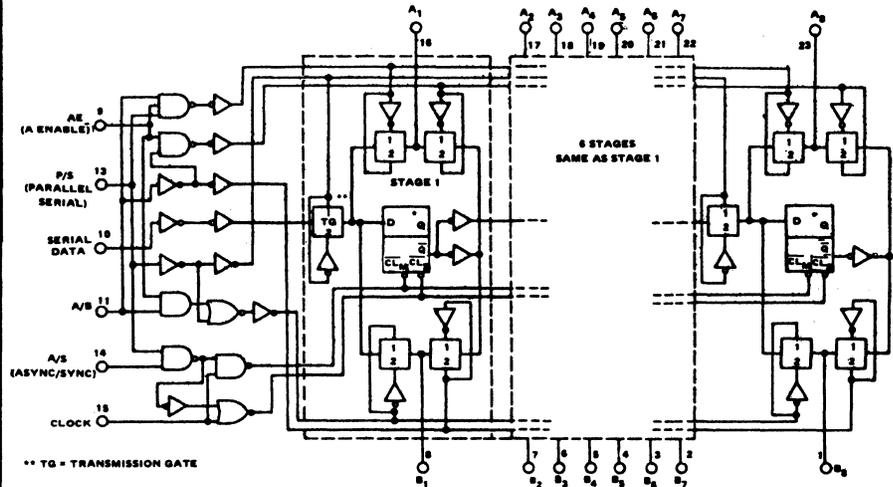
**F175**



**F176**

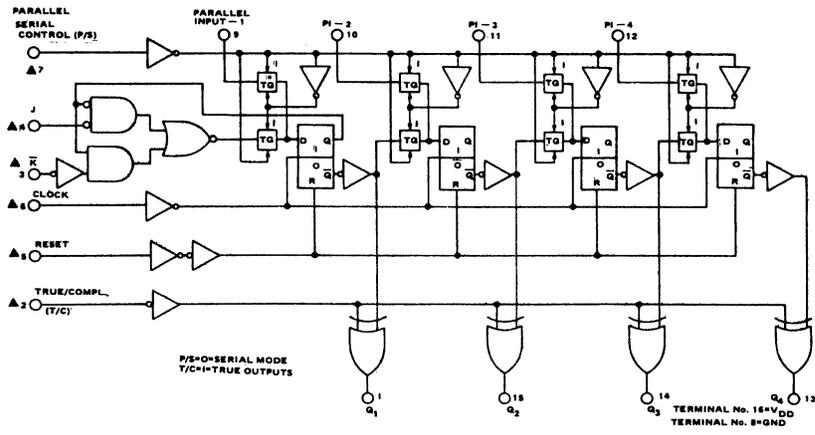


**F177**

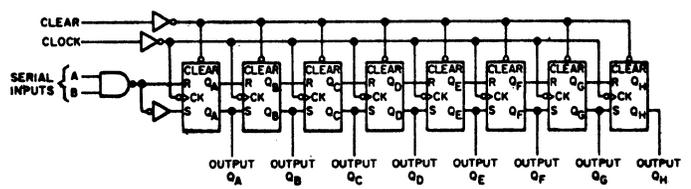
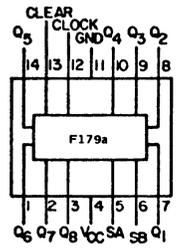
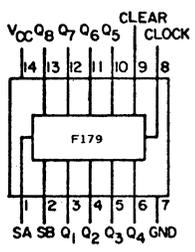


# 22. LOGIC/BLOCK DRAWINGS IN DRAWING NUMBER SEQUENCE

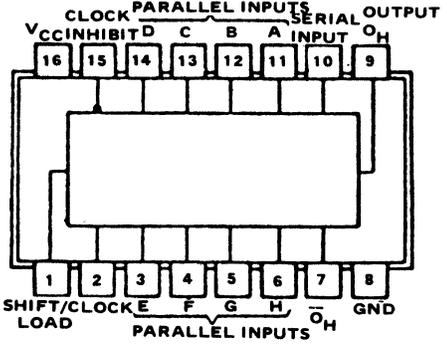
**F178**



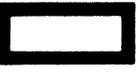
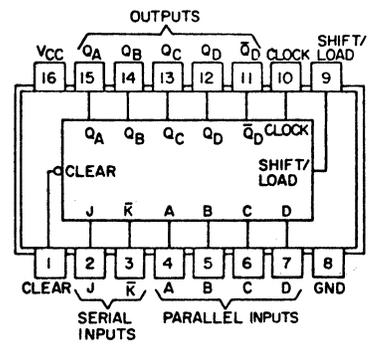
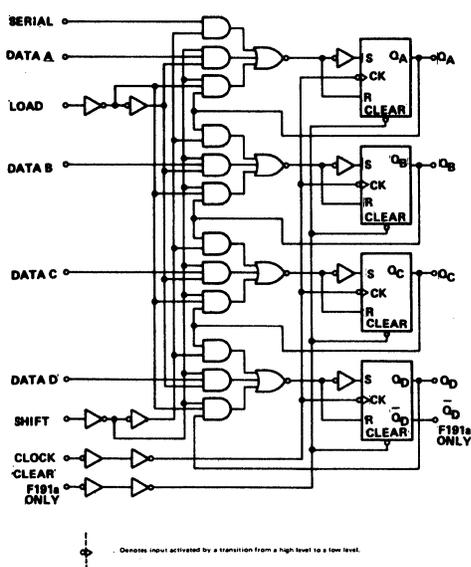
**F179**



**F180**



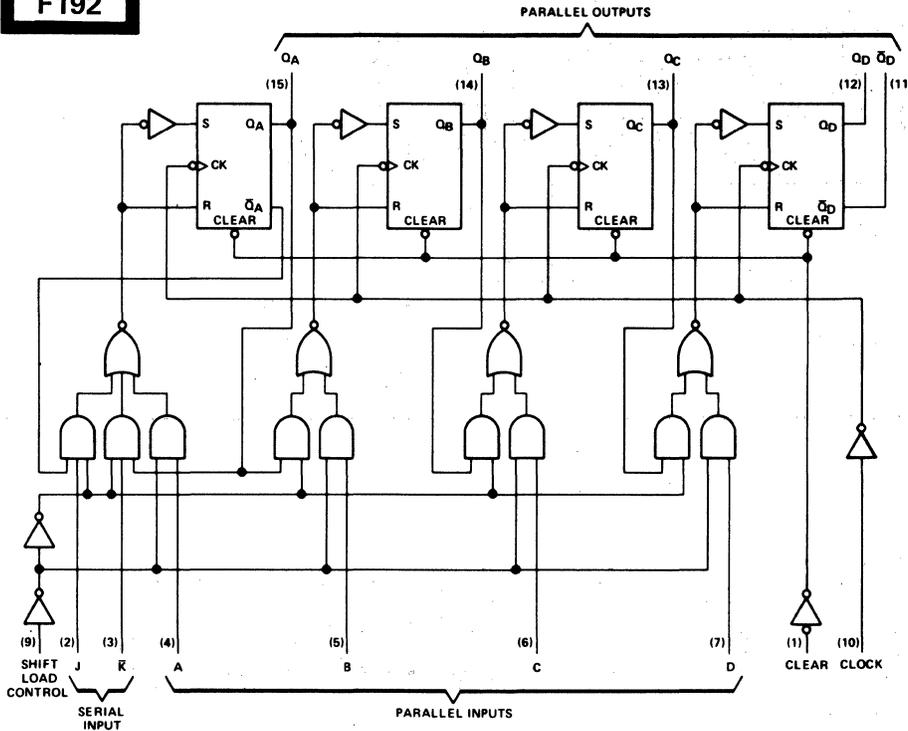
**F191**



# 22. LOGIC/BLOCK DRAWINGS

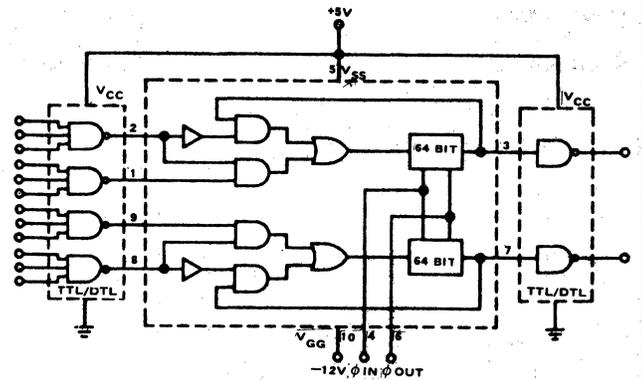
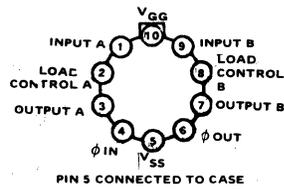
IN DRAWING NUMBER SEQUENCE

**F192**

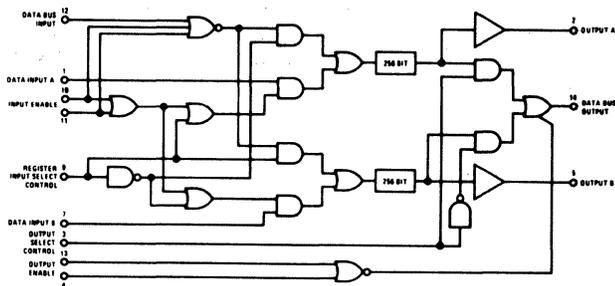


	DATA		SERIAL		OUTPUT										
	A	B	C	D	IN	SHIFT	CK	A	B	C	D	LOAD	VCC	GND	
F192	2	1	13	12	3	11	5	4	6	8	10	NA	9	14	7
F192a	3	2	15	14	4	13	6	5	7	9	11	12	10	16	8

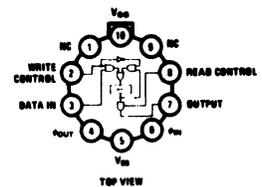
**F193**



**F195**



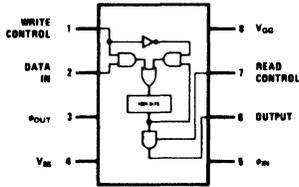
**F196**



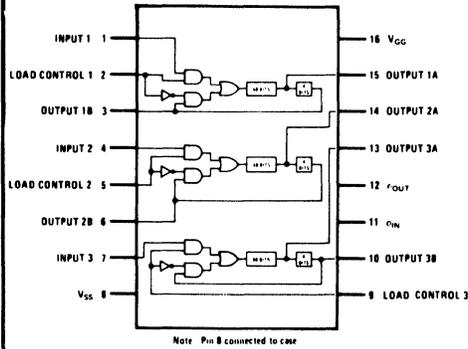
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F197**

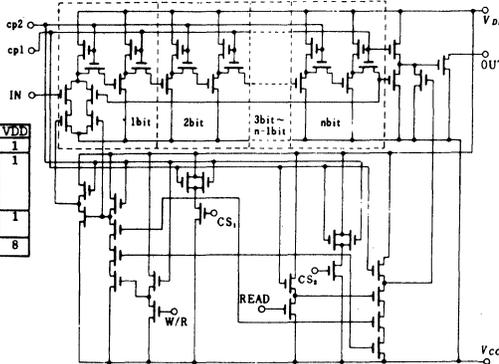


**F198**

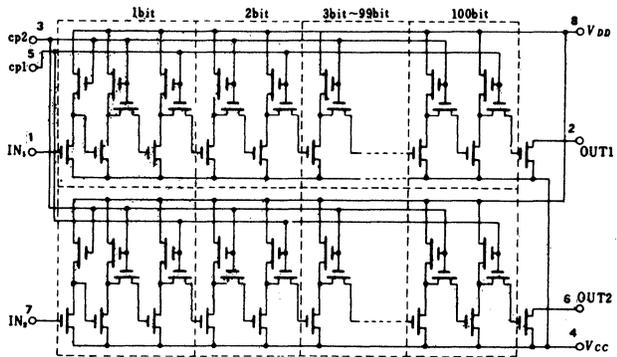


**F204**

	CKT NO.	IN	CP1	CP2	CS1	CS2	READ	W/R	OUT	VCC	VDD
F204	1	5	6	11	2	16	13	4	12	8	1
F204a	1	11	9	15			2	7	10	8	1
	2	14							12		
	3	4							3		
	4	6							5		
F204b	1	6	9	15			2	7	3	8	1
	2	14							10		
F204c	1	2	5	8			1	3	6	4	8

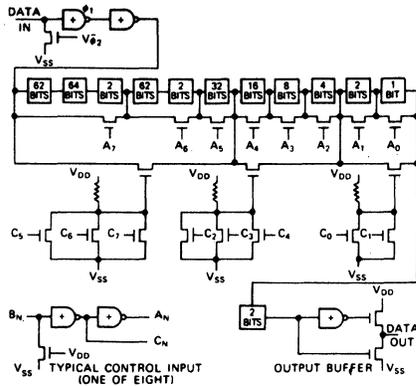


**F205**



**F208**

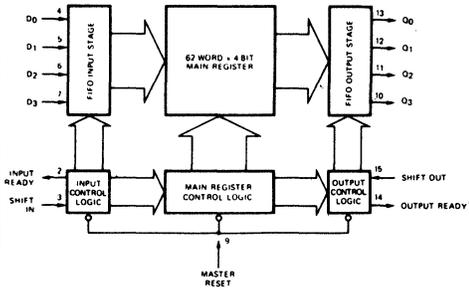
PIN	FUNCTION
1	B6
2	B7
3	B5
4	B3
5	B2
6	B4
7	B1
8	B0
9	DATA IN
10	OUT 2-257
11	VGG
12	O1
13	O2
14	VSS



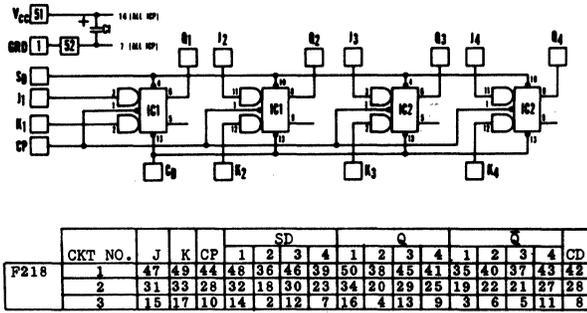
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

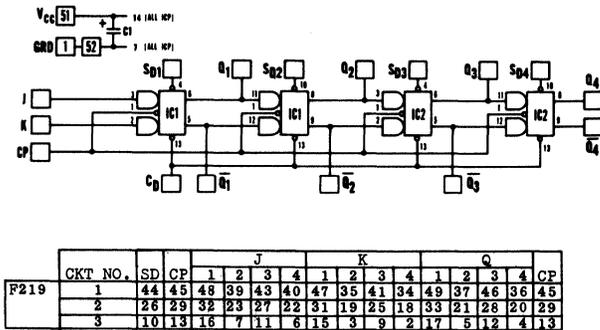
**F209**



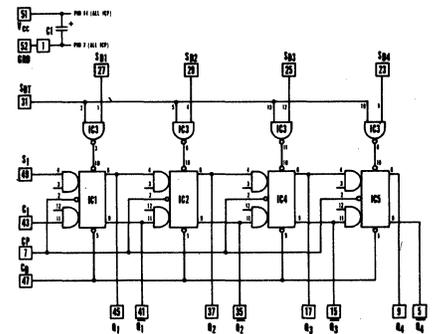
**F218**



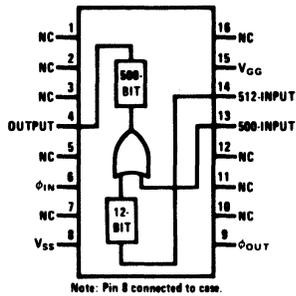
**F219**



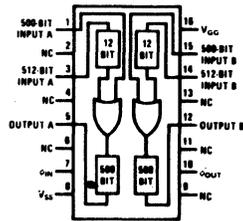
**F220**



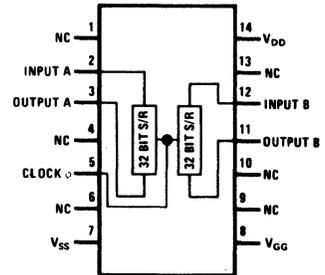
**F223**



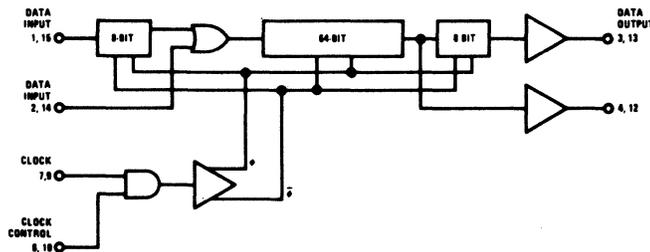
**F225**



**F226**



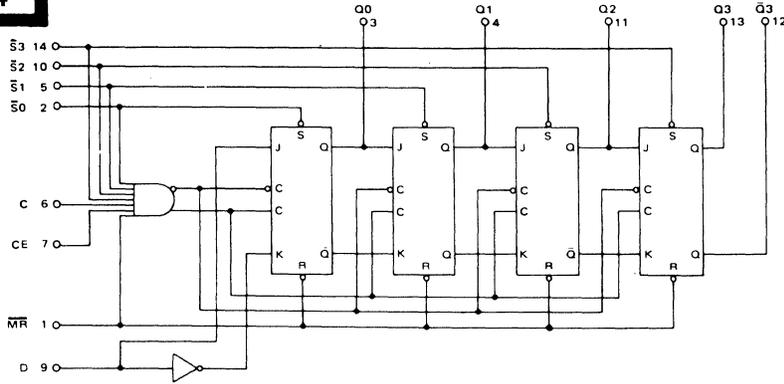
**F227**



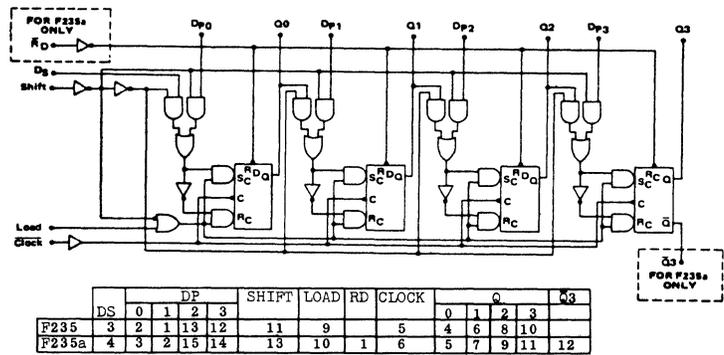
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

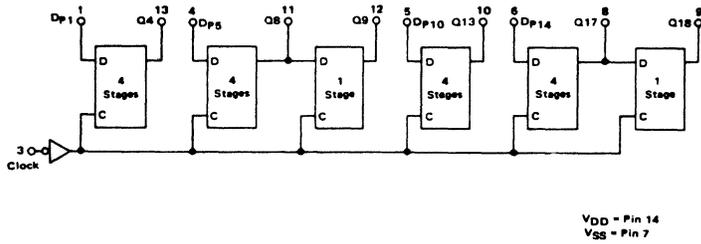
**F234**



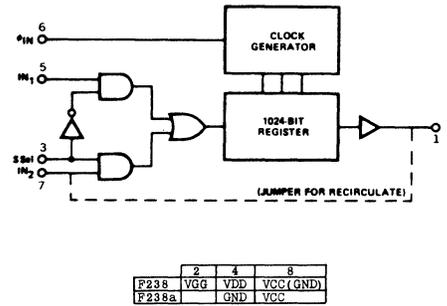
**F235**



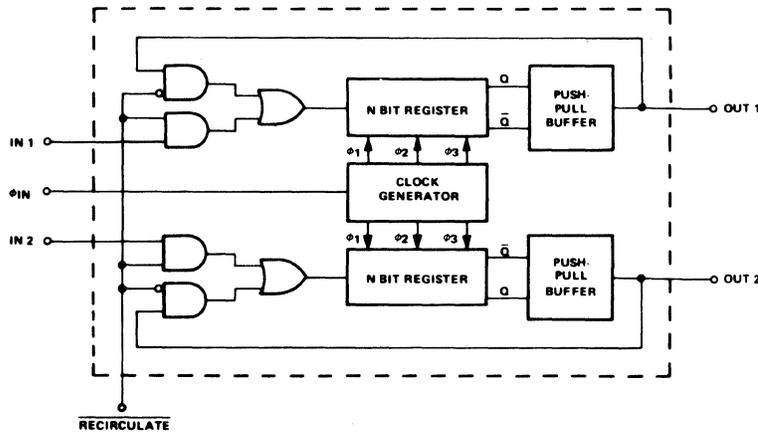
**F236**



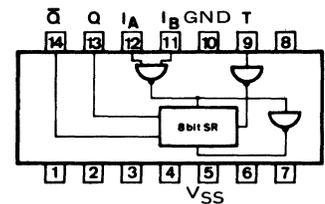
**F238**



**F240**



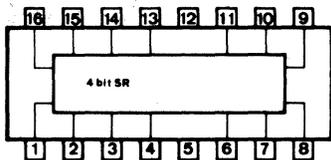
**F242**



# 22. LOGIC/BLOCK DRAWINGS

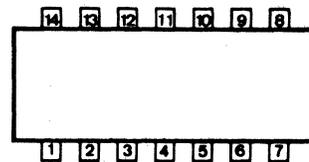
IN DRAWING NUMBER SEQUENCE

**F243**



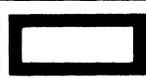
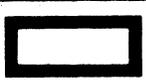
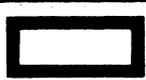
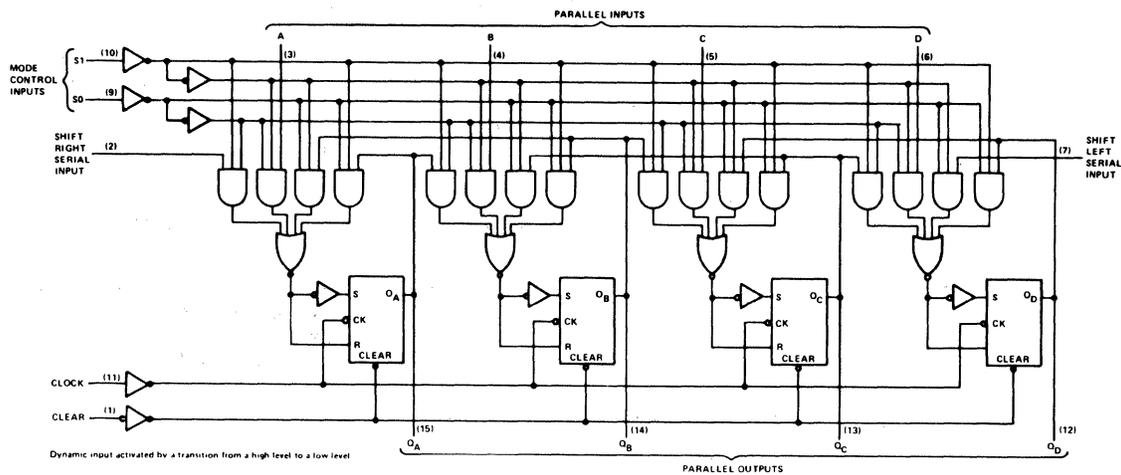
		PIN NUMBERS															
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
F243	IA1	IB1	IC1	ID1	VSS	SI	ST	T	Q	R	ID2	Gnd	IC2	IB2	SA2	IA2	
F243a	T	IA	IB	IC	VSS	ID	IE	S	ST	Q	QD	Gnd	QC	QB	QS	H	
F243b	CLEAR	J	K	A	B	C	L	GND	SHIFT/LOAD	CLOCK	QD	QD	QC	QB	QA	VCC	

**F244**

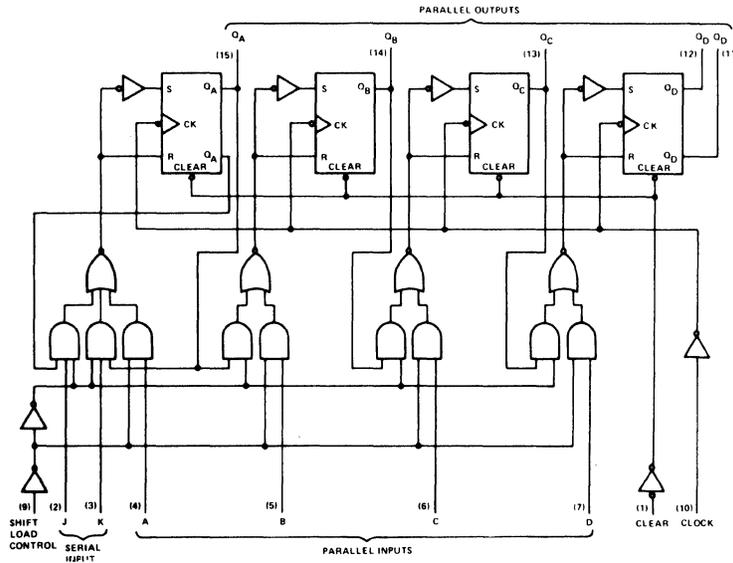


		PIN NUMBERS													
		1	2	3	4	5	6	7	8	9	10	11	12	13	14
F244	S1	IA	IB	IC	ID	BA	GND	TSL	TSR	QA	QB	QC	QD	VSS	
F244a	A	B	CA	CB	CC	CD	GND	CLOCK	CLR	QE	QF	QG	QH	VCC	

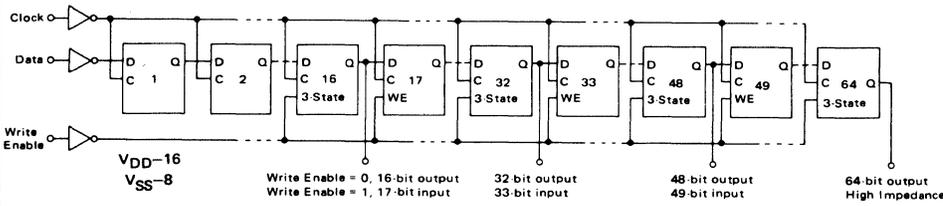
**F245**



**F246**

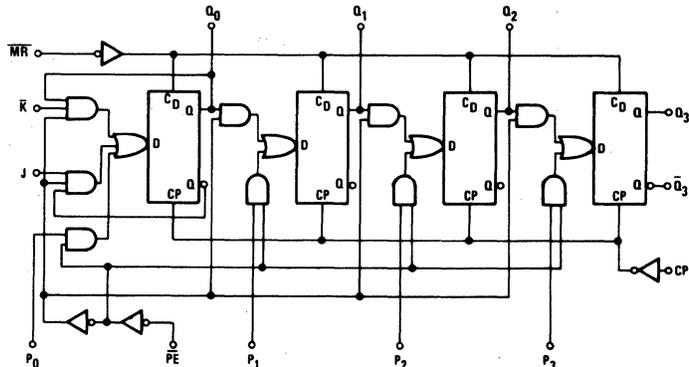


**F248**



CKT	CLOCK	16OUT/17IN	48OUT/49IN	WE	64OUT	32OUT/33IN	DATA
F248	1	4	1	2	3	5	6
	2	12	15	14	13	11	10
							9

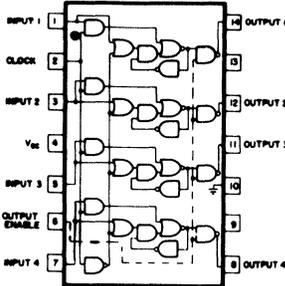
**F249**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

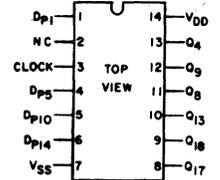
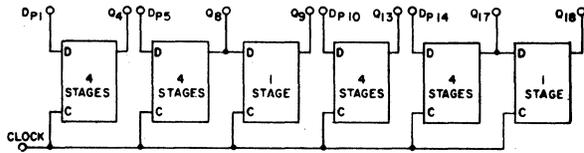
**F250**



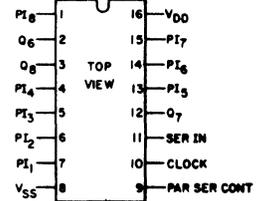
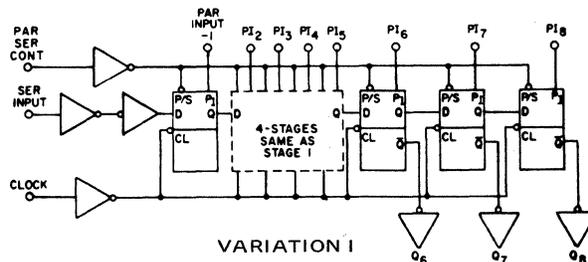
F250—TWO 5K PULLUP RESISTORS ARE PROVIDED IN THE PACKAGE AND ARE INTERNALLY CONNECTED TO V<sub>CC</sub> AND BROUGHT OUT ON PINS 9 AND 13.

F250a—OUTPUT ENABLE AND ALL CONNECTIONS EXCLUDED. (DOTTED LINE PORTION.)

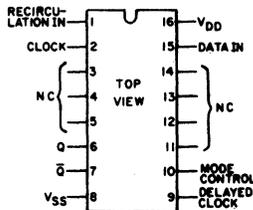
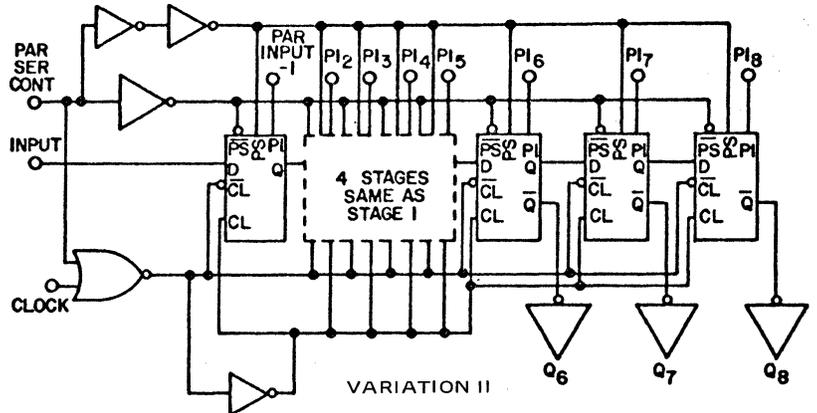
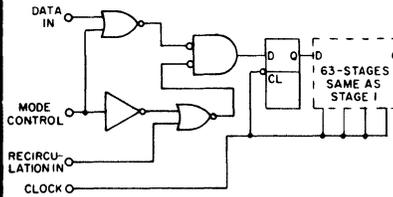
**F251**



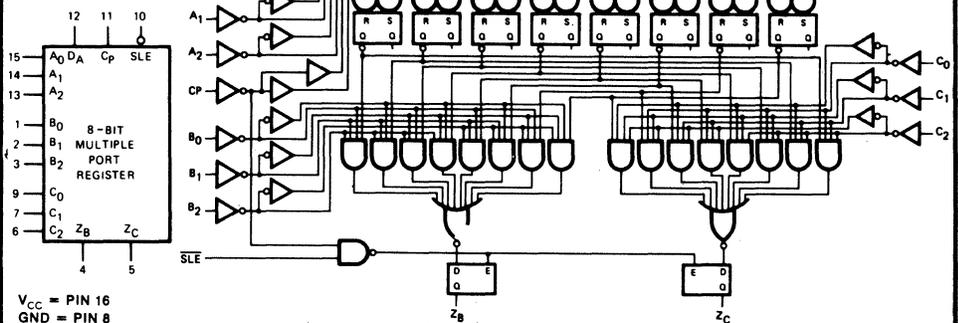
**F252**



**F253**



**F254**

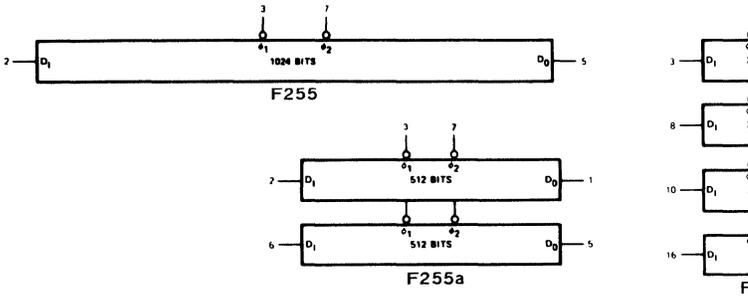


V<sub>CC</sub> = PIN 16  
 GND = PIN 8

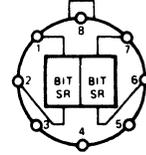
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F255**

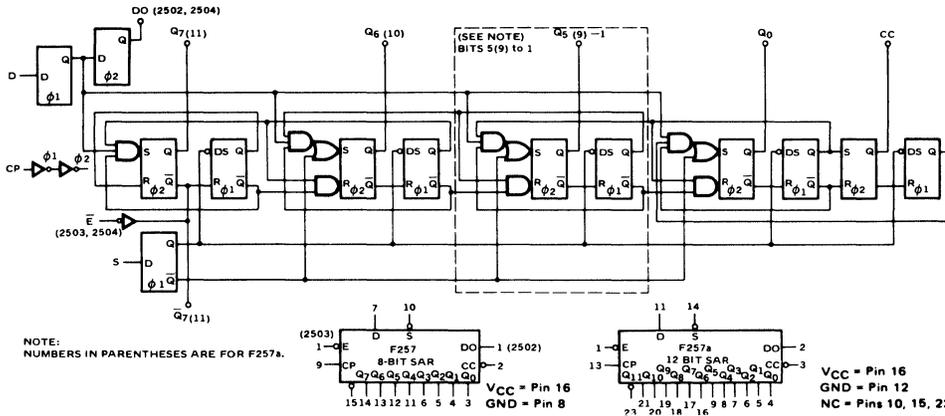


**F256**

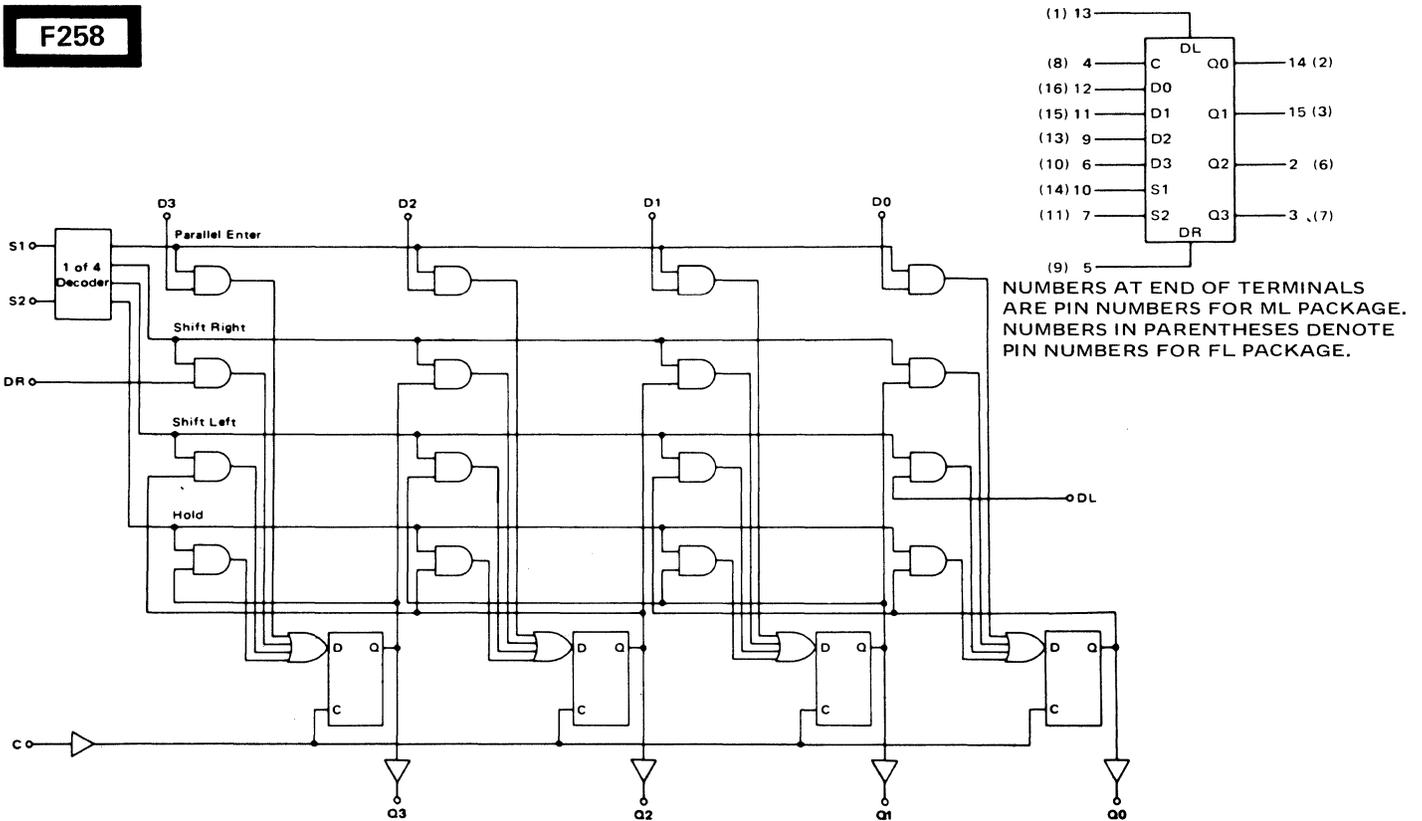


PIN NUMBERS								
1	2	3	4	5	6	7	8	
F255b	Inp1	OUTP1	Q2	VCC	Q1	OUTP2	Inp2	VDD
F255a	Inp1	OUTP1	V0	VSS	VGG	OUTP2	Inp2	VDD

**F257**



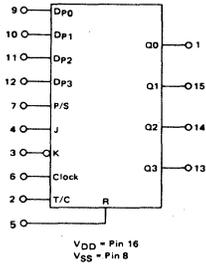
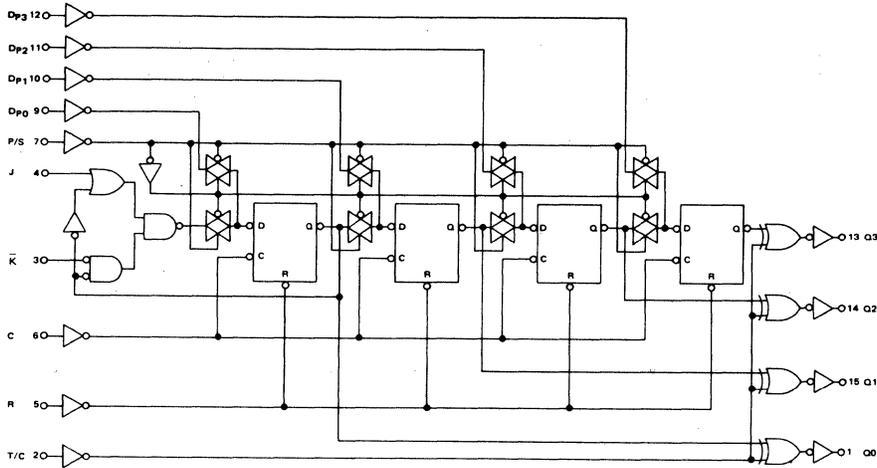
**F258**



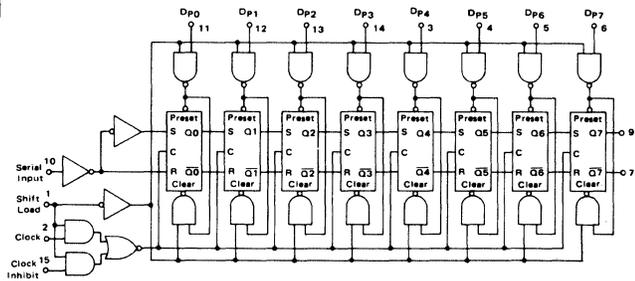
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

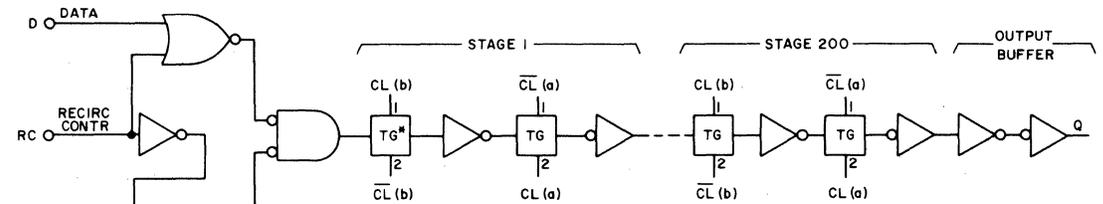
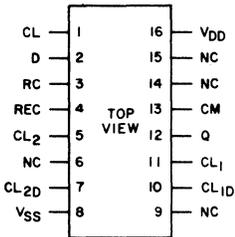
**F259**



**F261**



**F262**



\* TRANSMISSION GATE

INPUT TO OUTPUT IS:  
 (a) A BIDIRECTIONAL LOW IMPEDANCE WHEN CONTROL INPUT 1 IS "LOW" AND CONTROL INPUT 2 IS "HIGH"

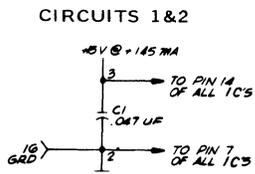
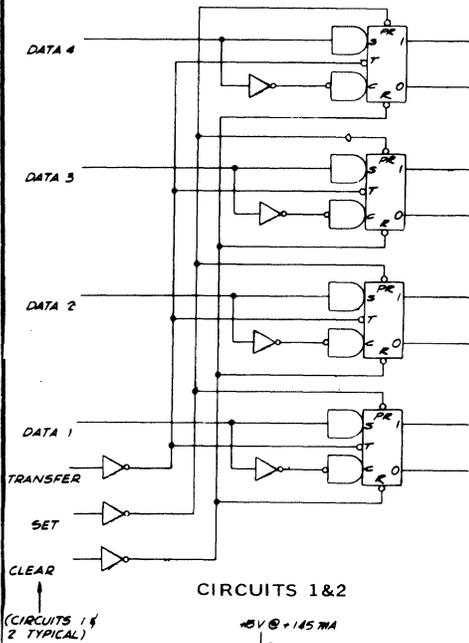
(b) AN OPEN CIRCUIT WHEN CONTROL INPUT 1 IS "HIGH" AND CONTROL INPUT 2 IS "LOW"

CL (a) = INTERNAL CLOCK IN PHASE WITH CL<sub>1</sub>  
 CL (b) = INTERNAL CLOCK IN PHASE WITH CL<sub>2</sub>

# 22. LOGIC/BLOCK DRAWINGS

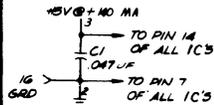
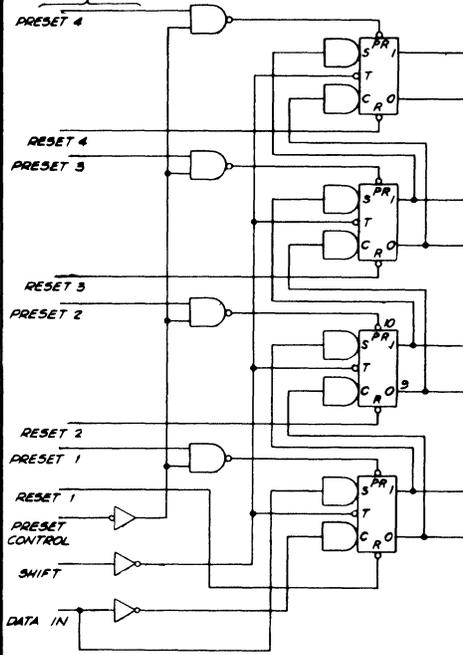
IN DRAWING NUMBER  
SEQUENCE

**F263**

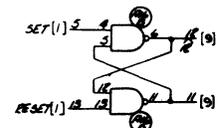
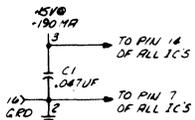
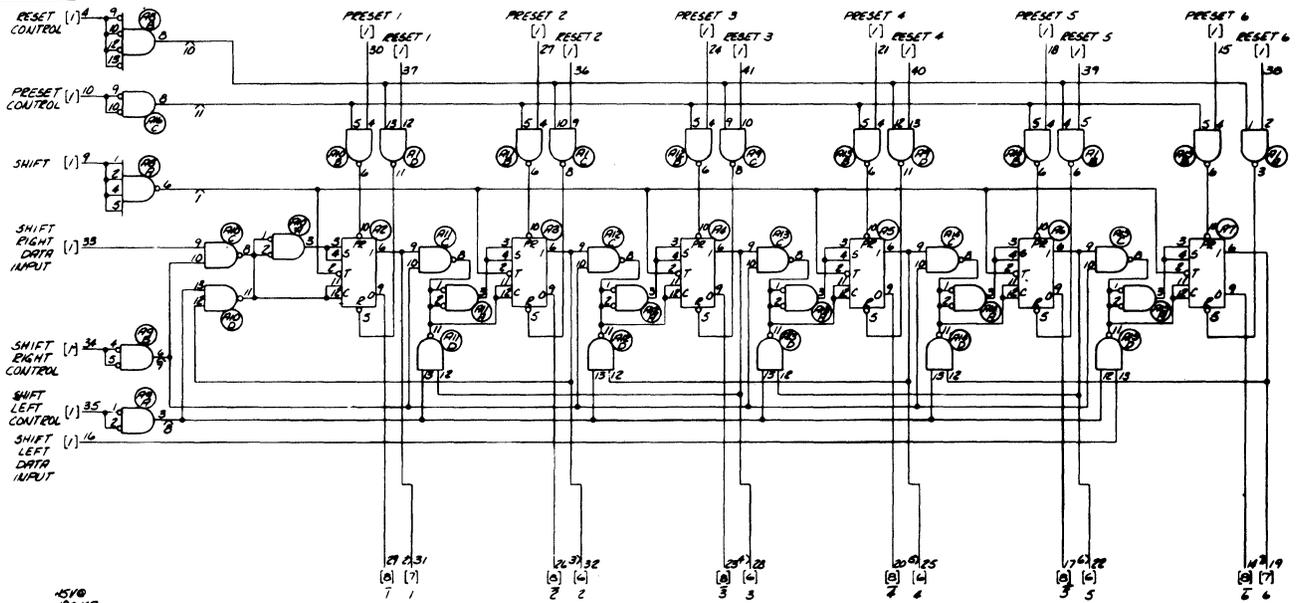


**F264**

TYPICAL  
(CIRCUITS 1 AND 2)



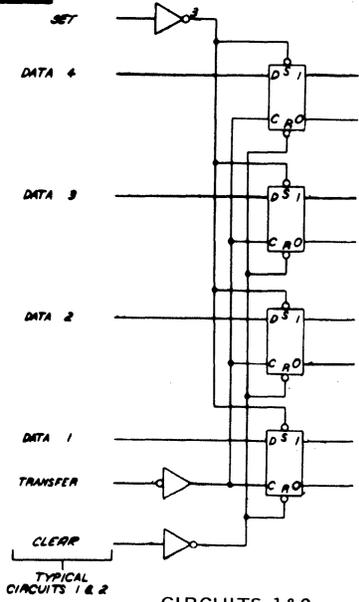
**F265**



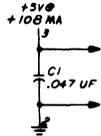
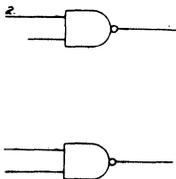
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

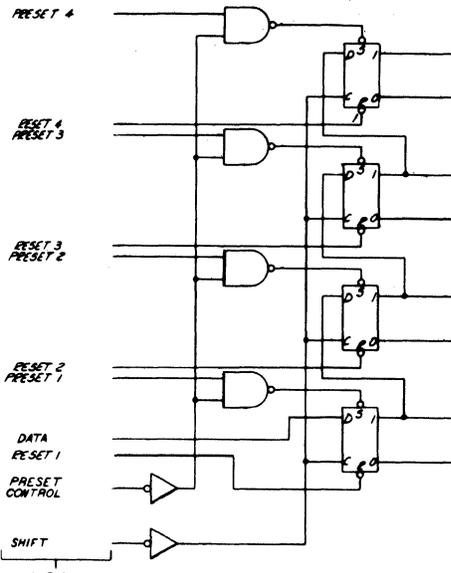
**F266**



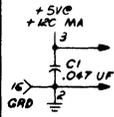
CIRCUITS 1 & 2



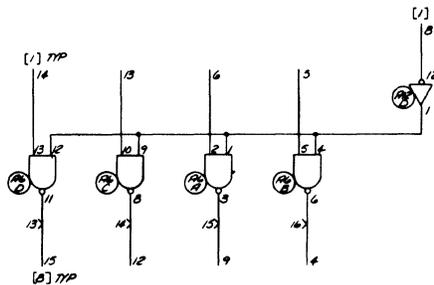
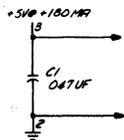
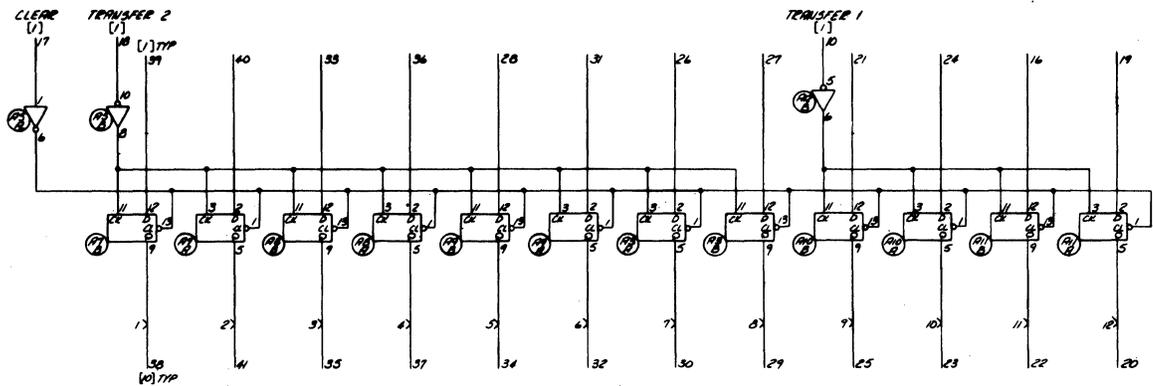
**F267**



CIRCUITS 1 & 2



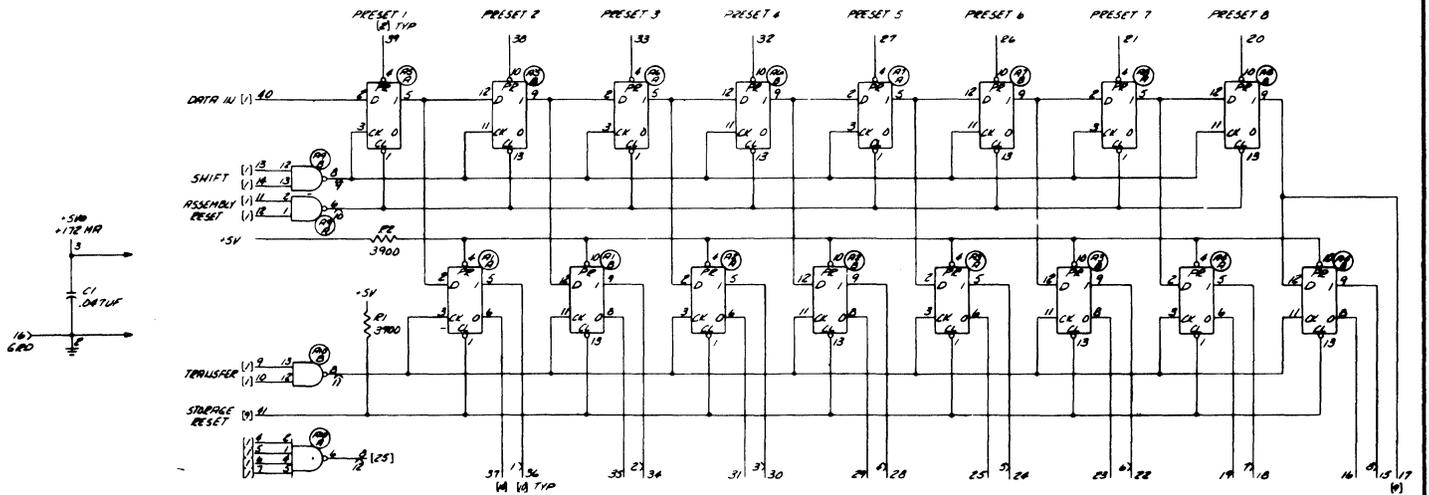
**F268**



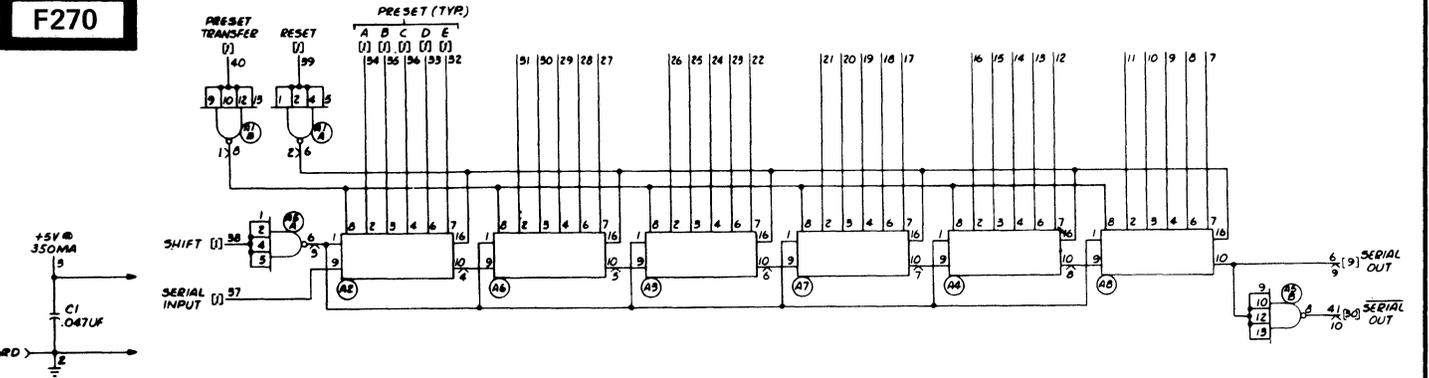
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

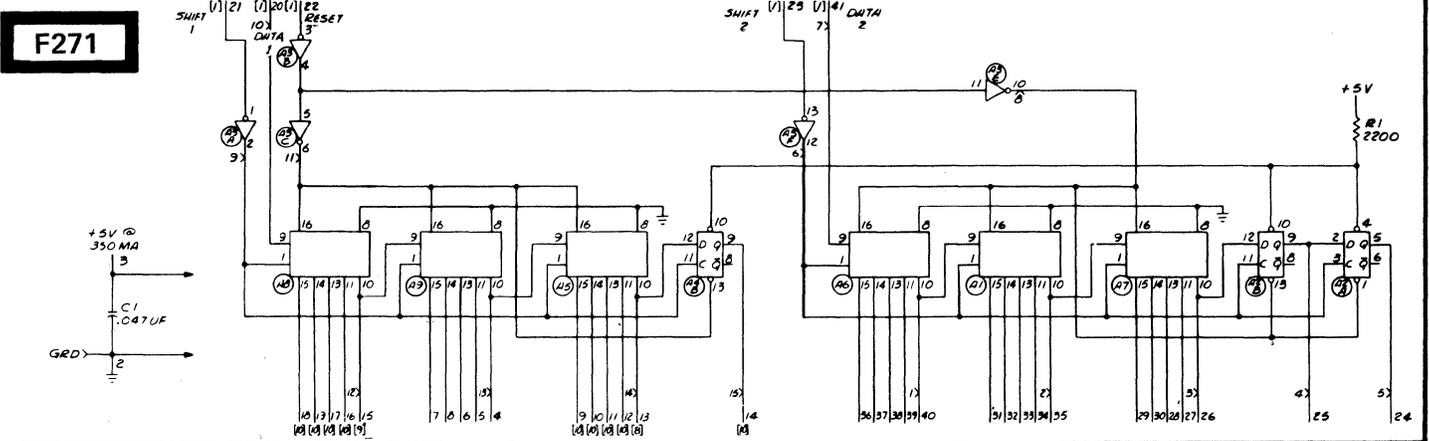
**F269**



**F270**



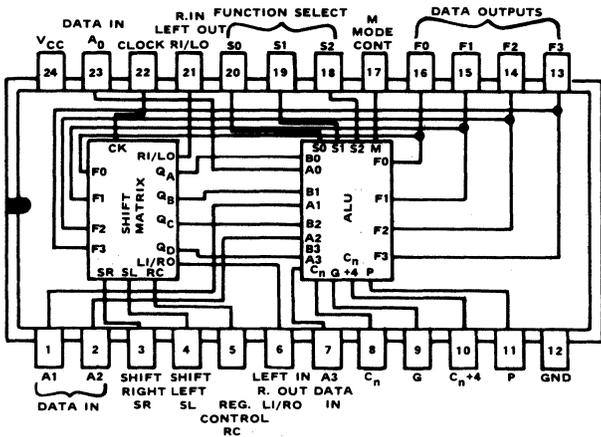
**F271**



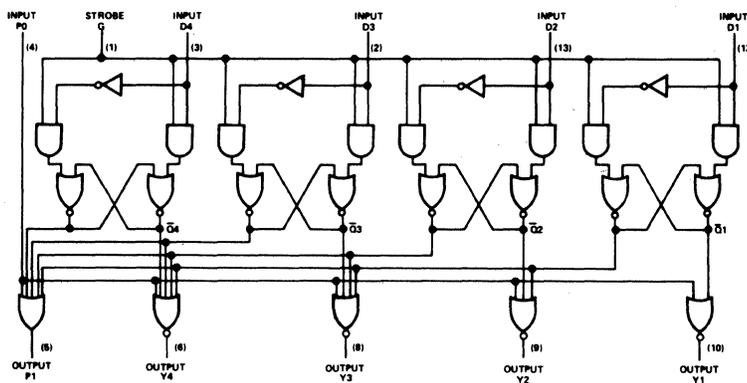
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

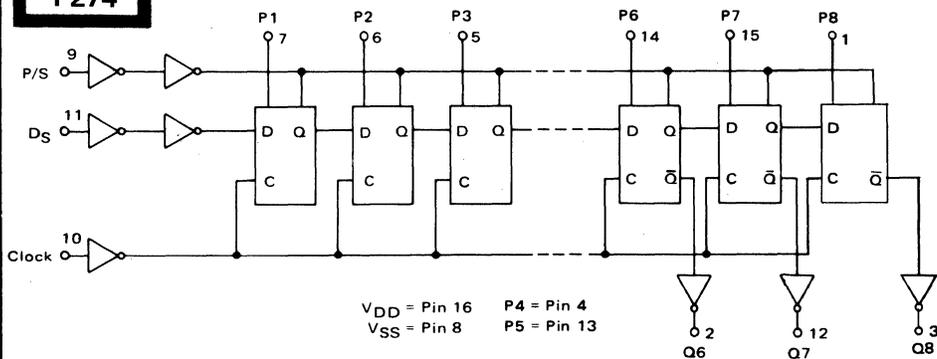
**F272**



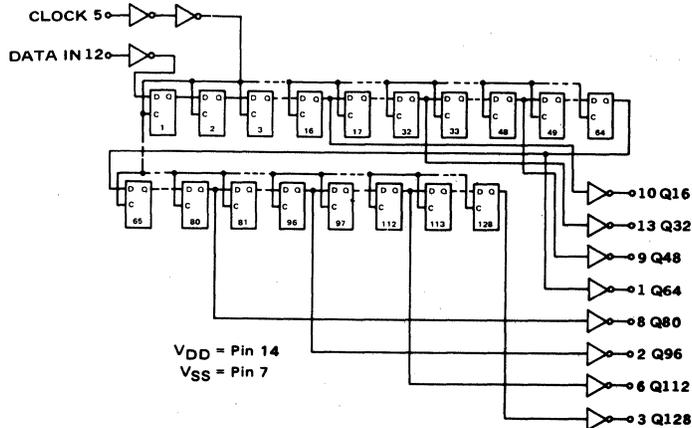
**F273**



**F274**



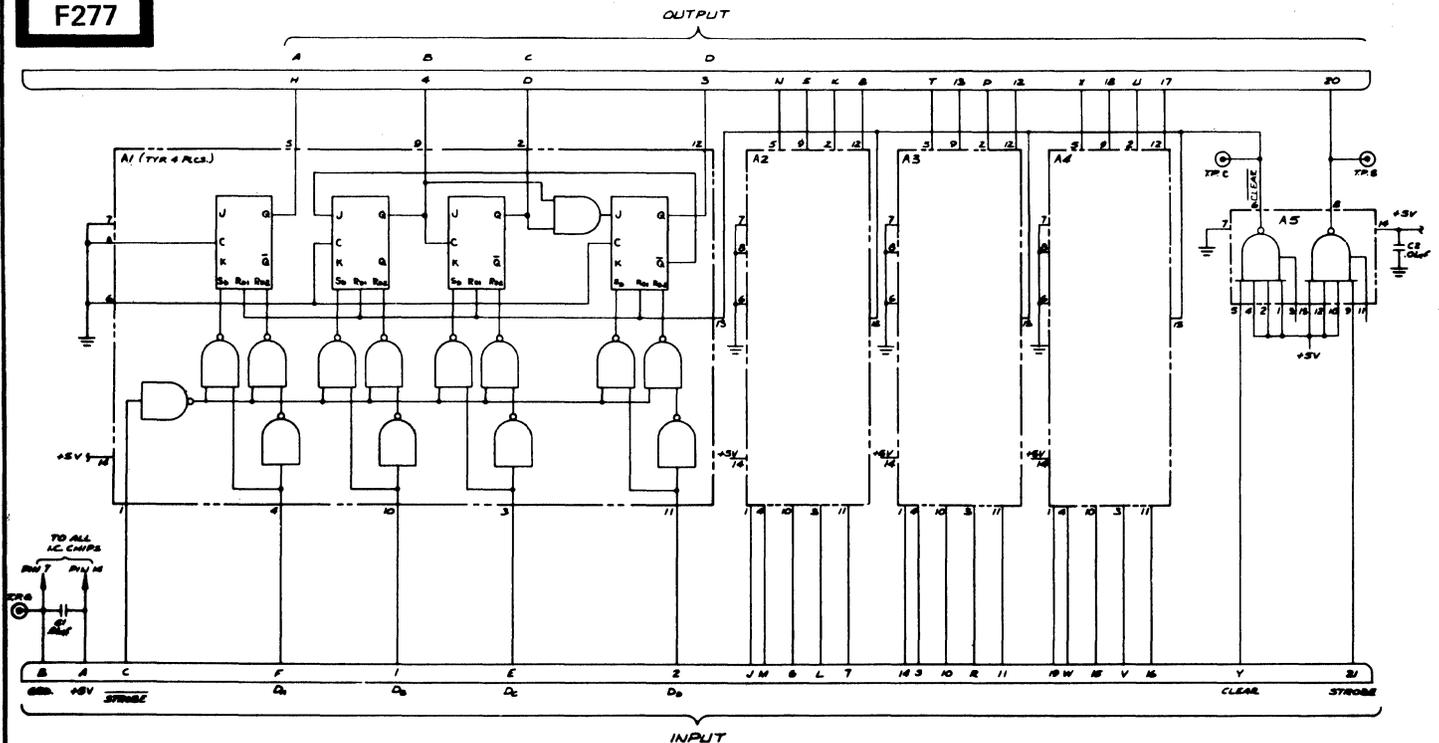
**F275**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**F277**



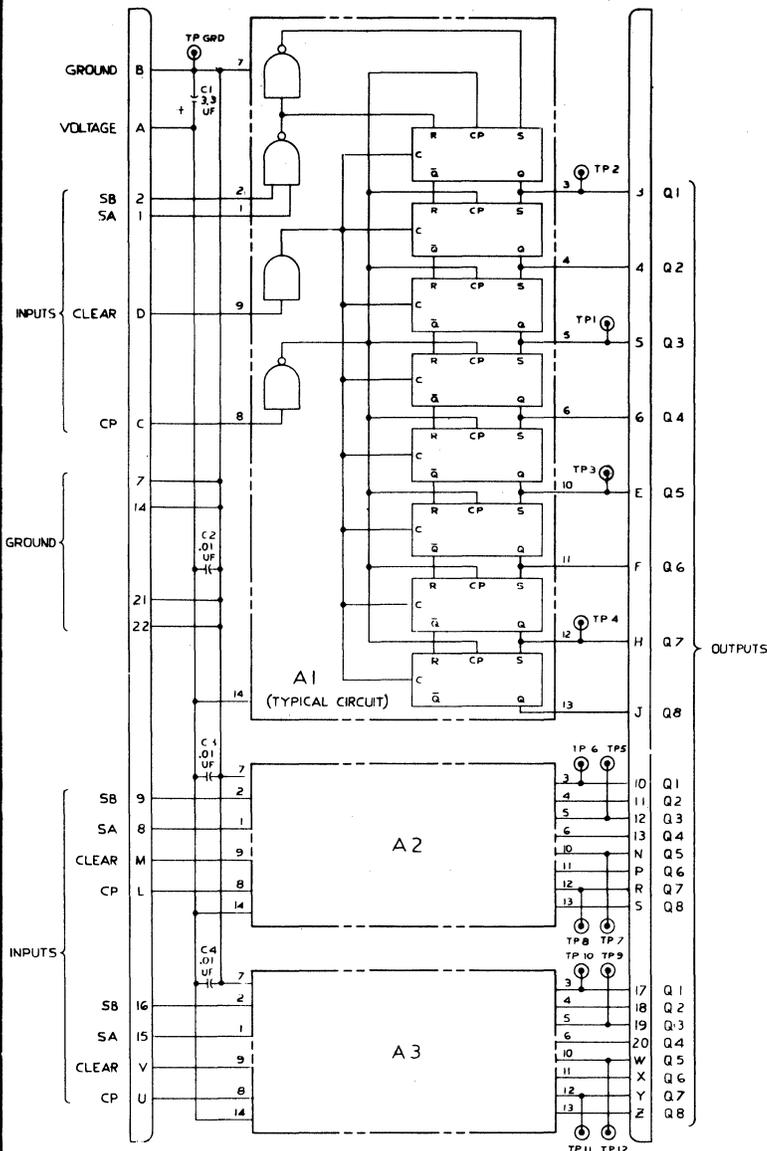
NOTES:  
 1- I.C. CHIPS A1 THRU A4 ARE BINARIES  
 N 8250A (DECADE COUNTER/STORAGE REGISTER).  
 2- I.C. CHIP A5 IS D.T.C. NO. 81452-010 (DUAL 4 POWER NAND)

<div style="border: 2px solid black; width: 50px; height: 20px; margin: 5px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin: 5px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin: 5px;"></div>
<div style="border: 2px solid black; width: 50px; height: 20px; margin: 5px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin: 5px;"></div>	<div style="border: 2px solid black; width: 50px; height: 20px; margin: 5px;"></div>

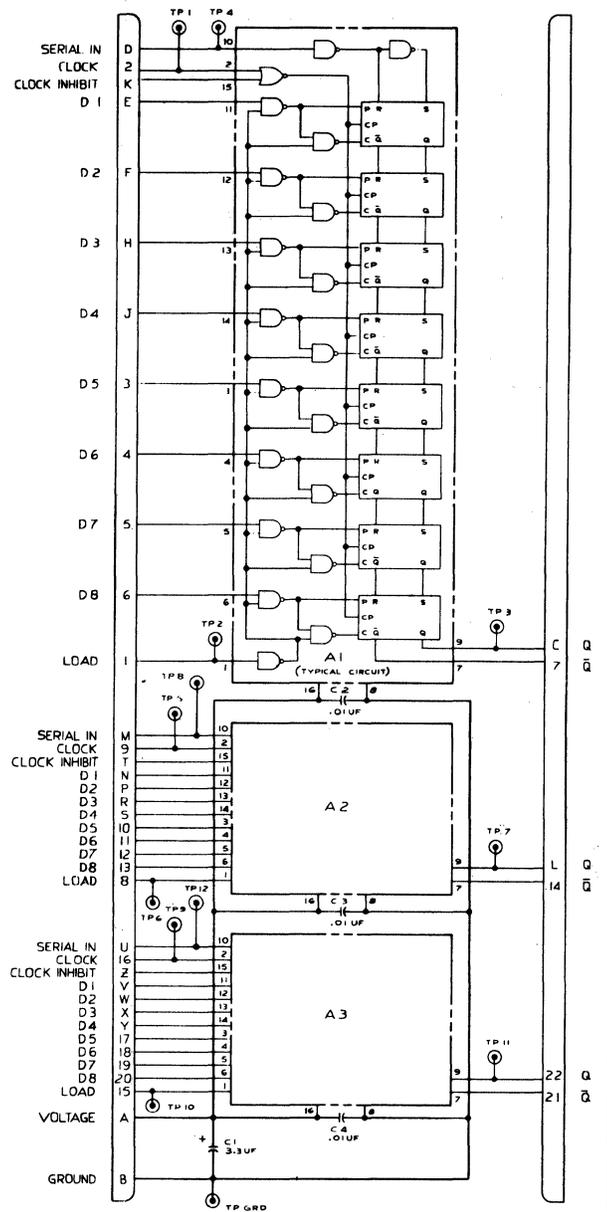
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

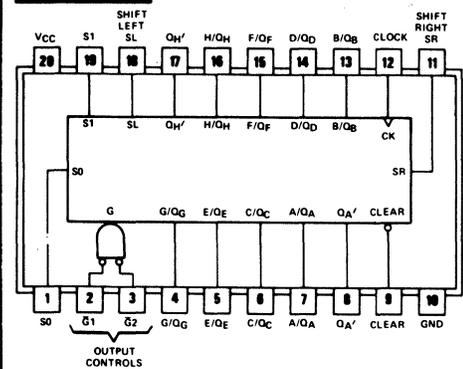
**F278**



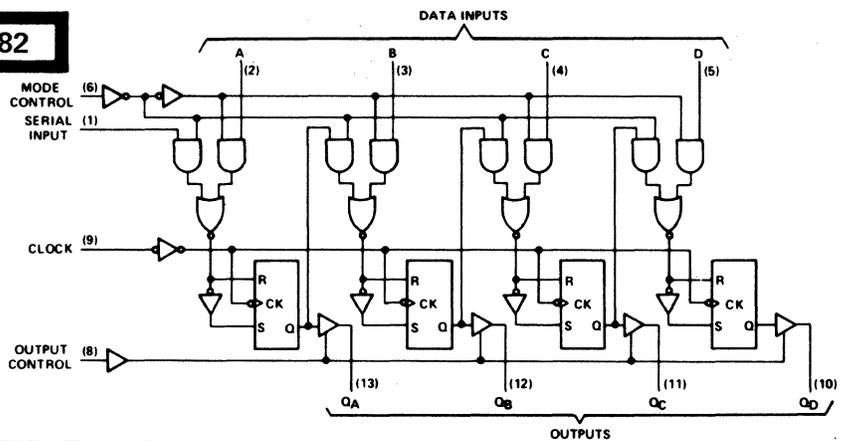
**F279**



**F281**



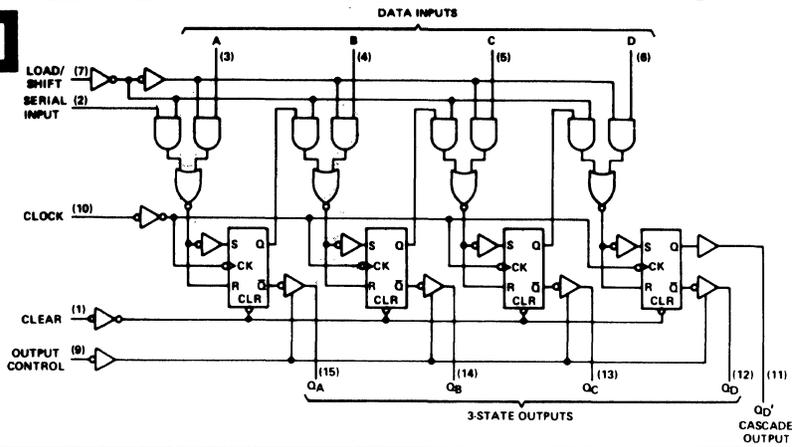
**F282**



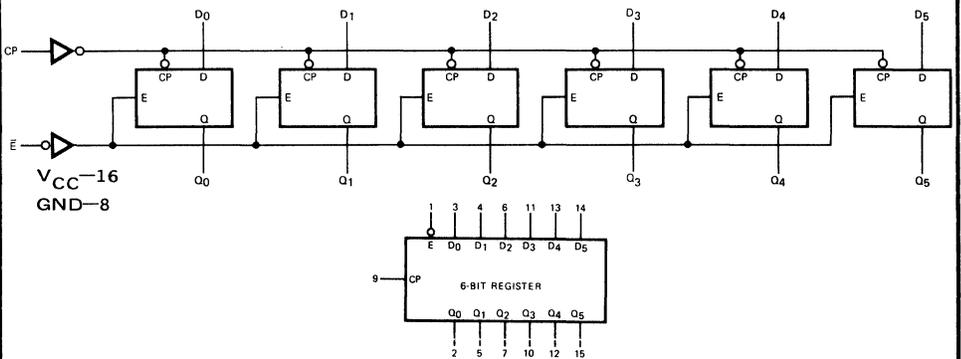
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

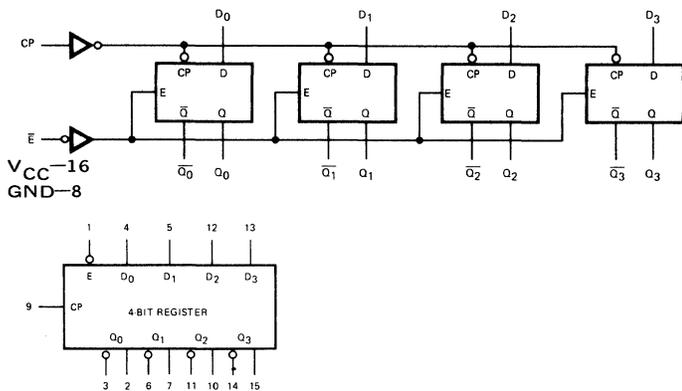
**F283**



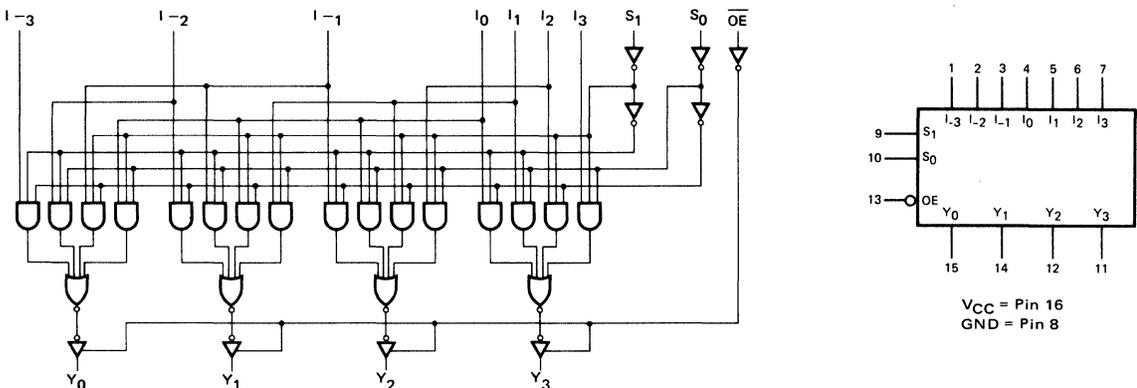
**F284**



**F285**



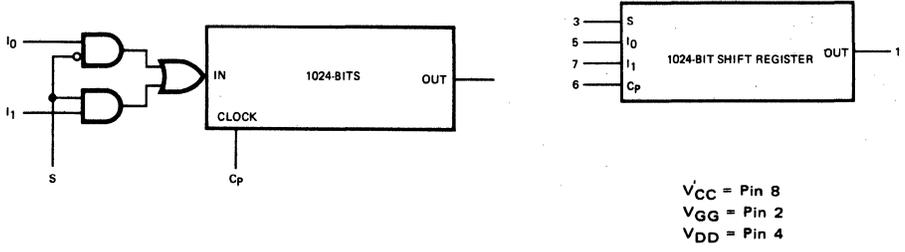
**F286**



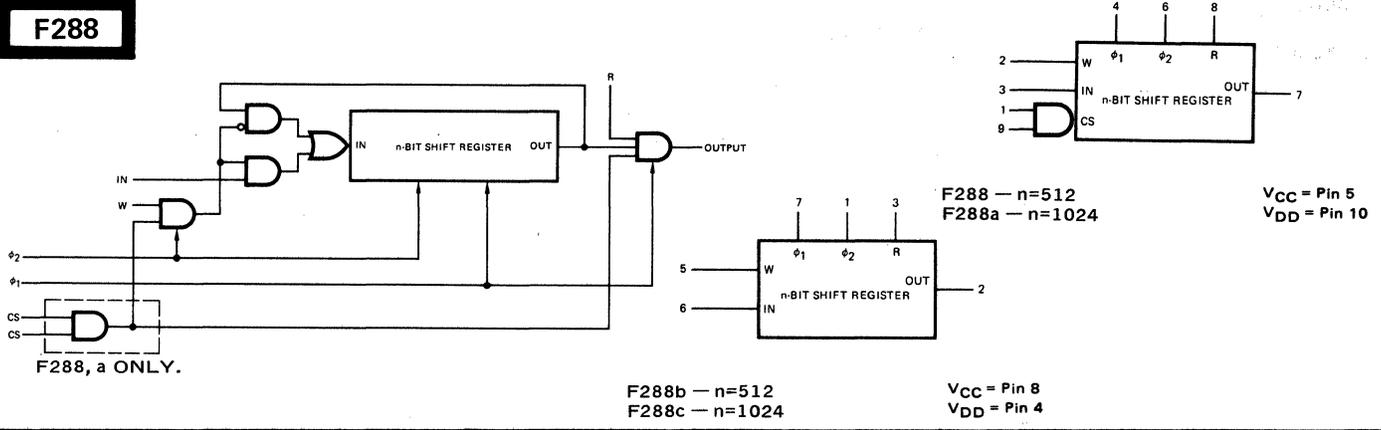
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

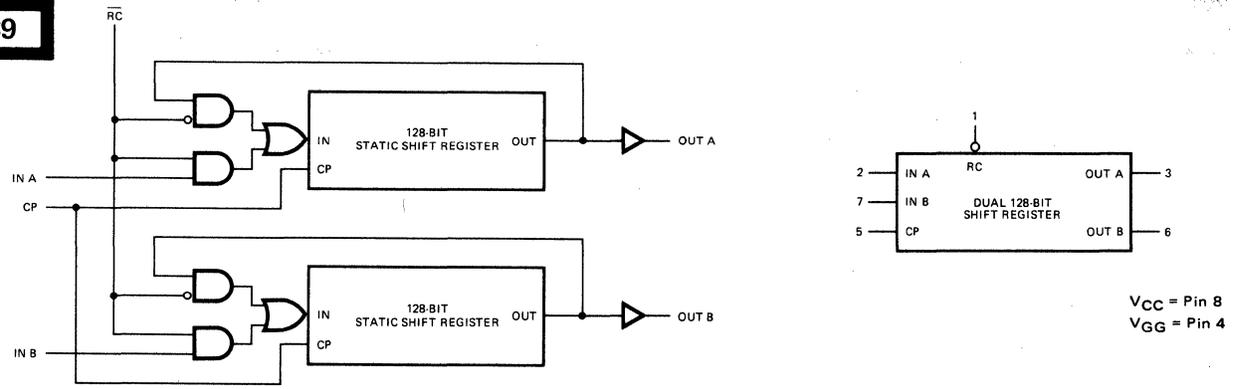
**F287**



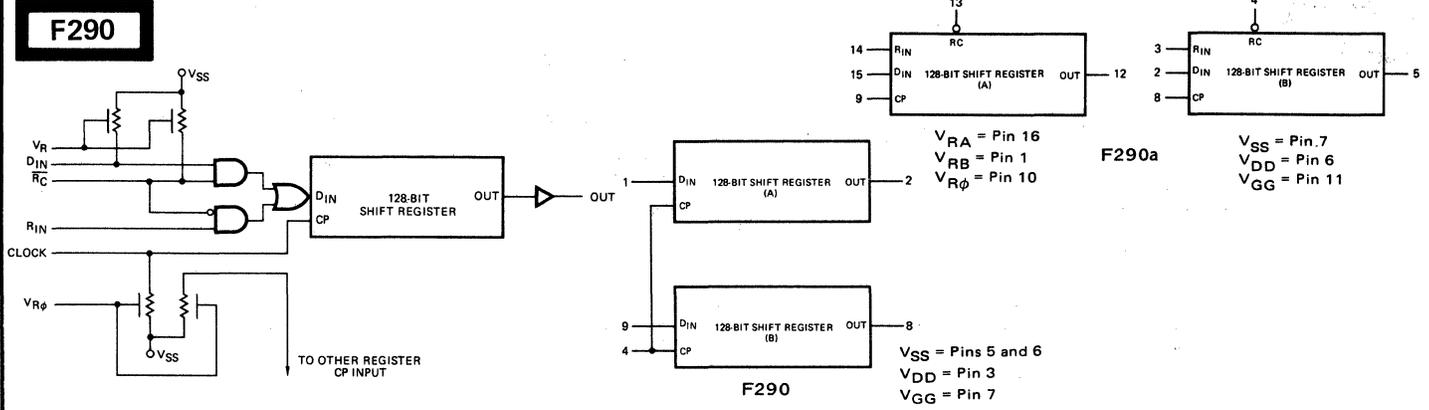
**F288**



**F289**



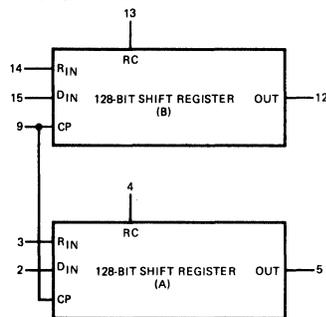
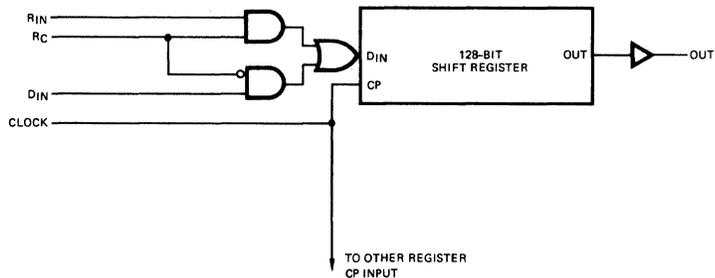
**F290**



# 22. LOGIC/BLOCK DRAWINGS

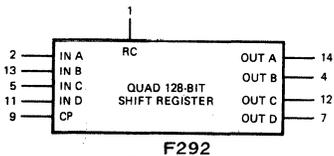
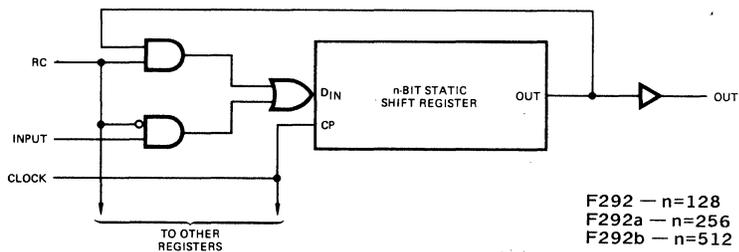
IN DRAWING NUMBER SEQUENCE

**F291**

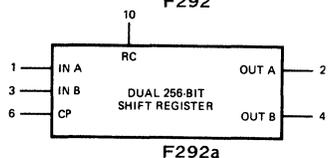


VSS = Pin 7  
VDD = Pin 6  
VGG = Pin 11

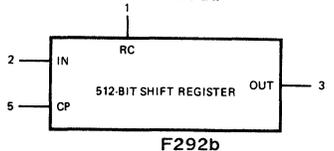
**F292**



VSS = Pin 8  
VDD = Pin 16  
VGG = Pin 10

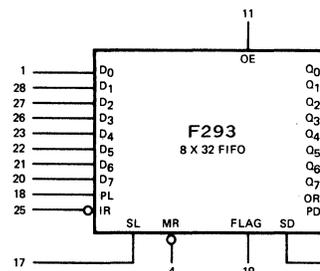


VSS = Pin 5  
VDD = Pin 9  
VGG = Pin 7

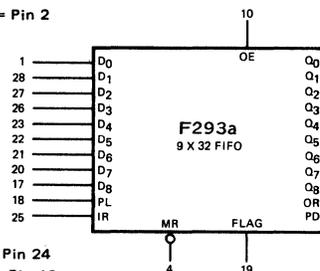


VSS = Pin 4  
VDD = Pin 8  
VGG = Pin 6

**F293**

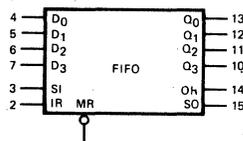


VSS = Pin 24  
VDD = Pin 16  
VGG = Pin 2

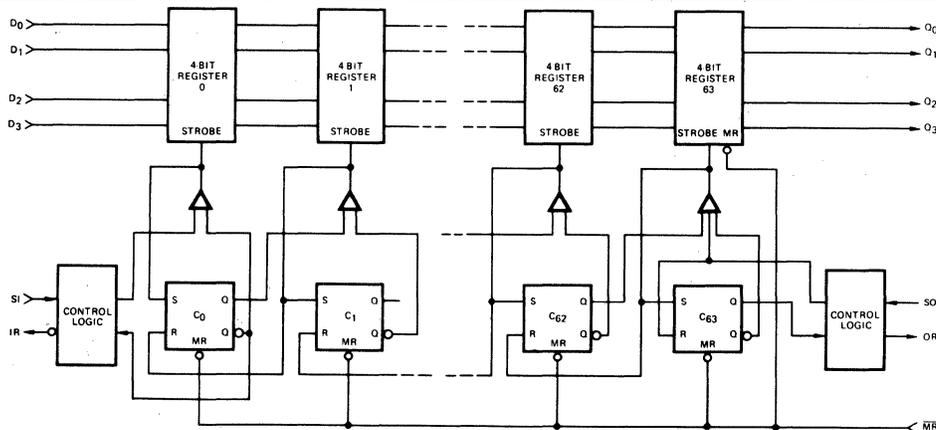


VSS = Pin 24  
VDD = Pin 16  
VGG = Pin 2

**F294**



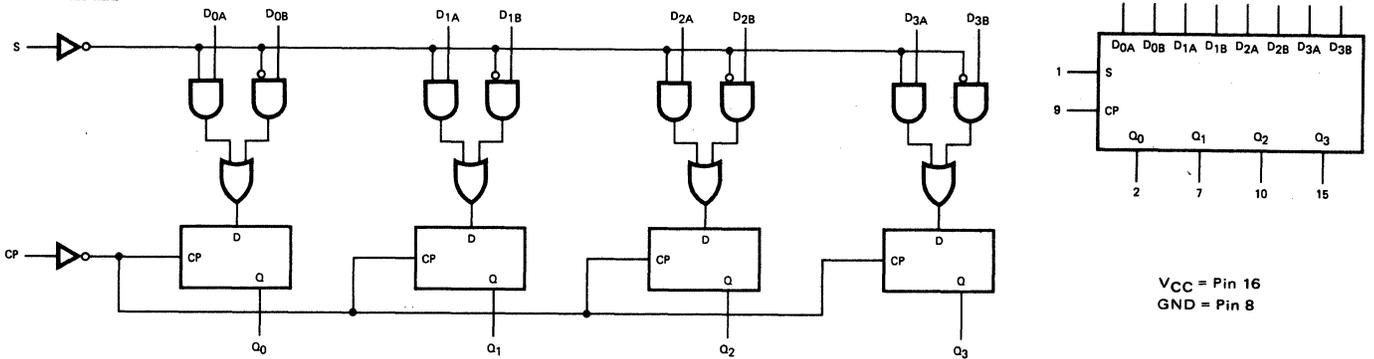
VSS = Pin 16  
VGG = Pin 1  
VDD = GND = Pin 8



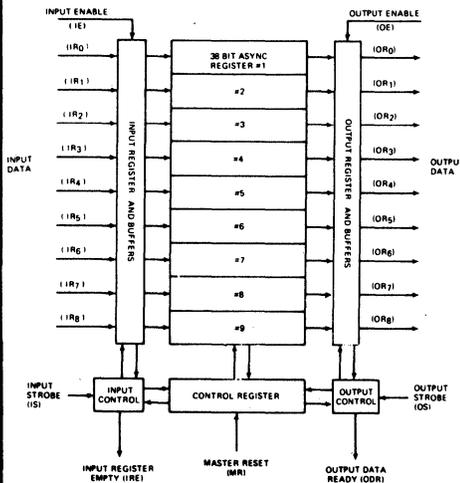
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

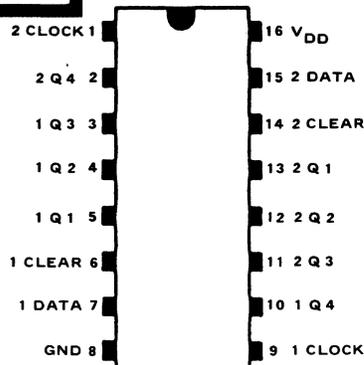
**F295**



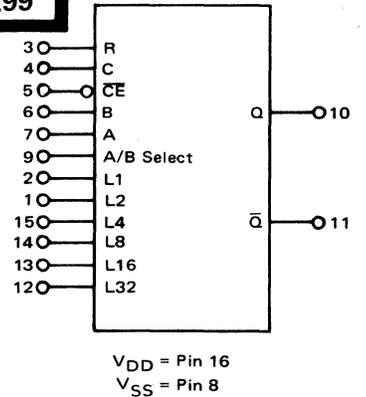
**F297**



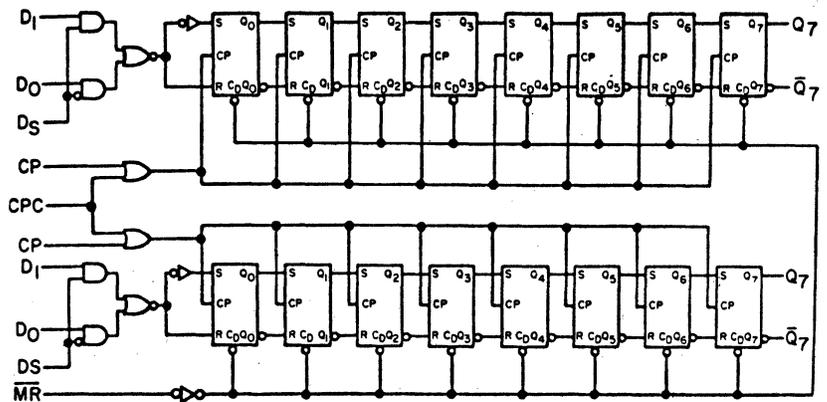
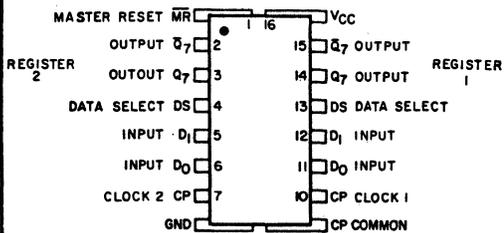
**F298**



**F299**



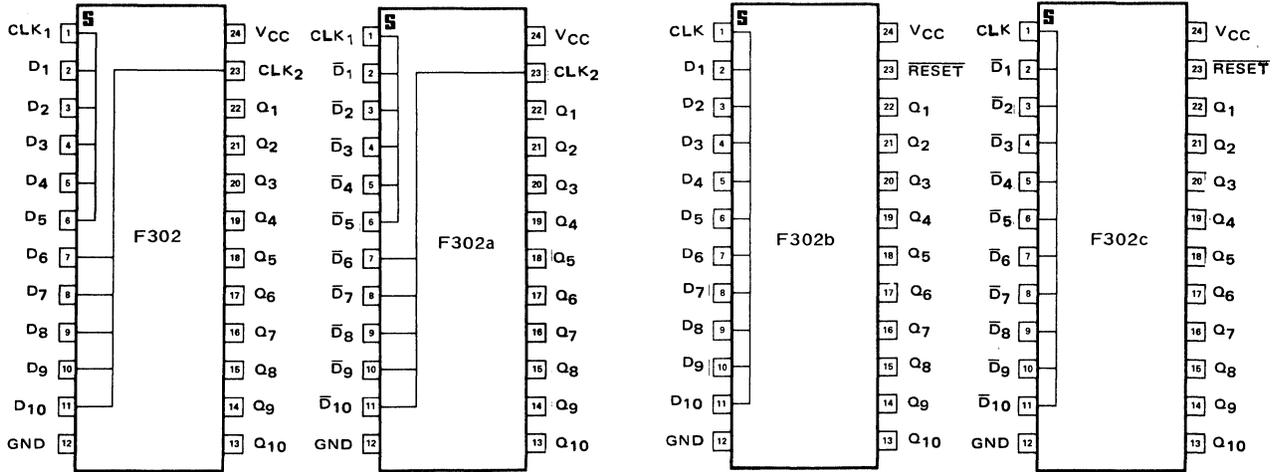
**F301**



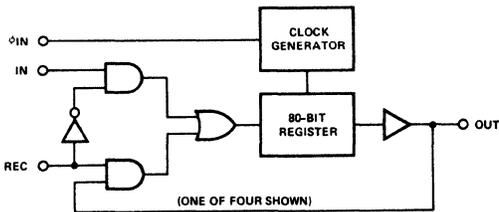
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**F302**

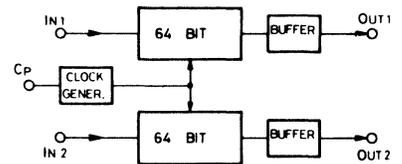


**F303**

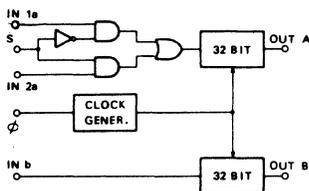


- |                  |                   |
|------------------|-------------------|
| 1. OUT 1         | 16. VCC           |
| 2. Recirculate 1 | 15. IN 4          |
| 3. IN 1          | 14. Recirculate 4 |
| 4. OUT 2         | 13. OUT 4         |
| 5. Recirculate 2 | 12. VGG           |
| 6. IN 2          | 11. phi IN        |
| 7. OUT 3         | 10. IN 3          |
| 8. VDD (Ground)  | 9. Recirculate 3  |

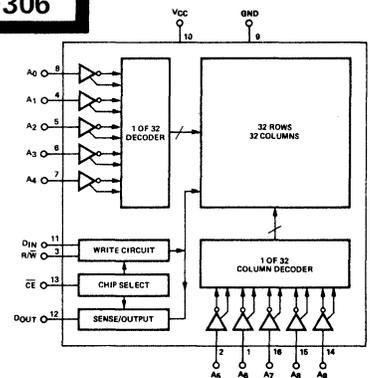
**F304**



**F305**



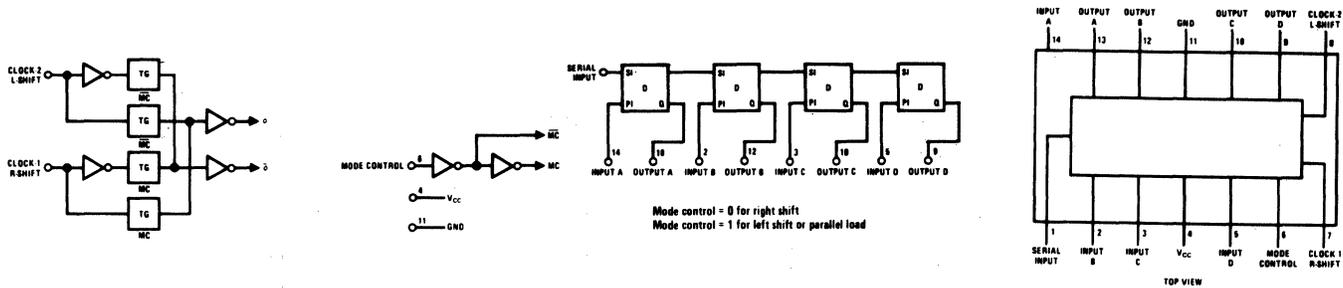
**F306**



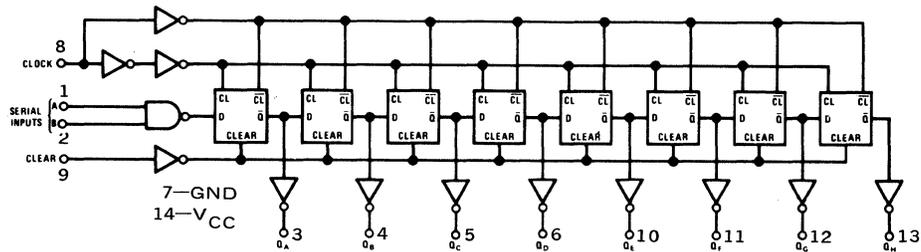
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

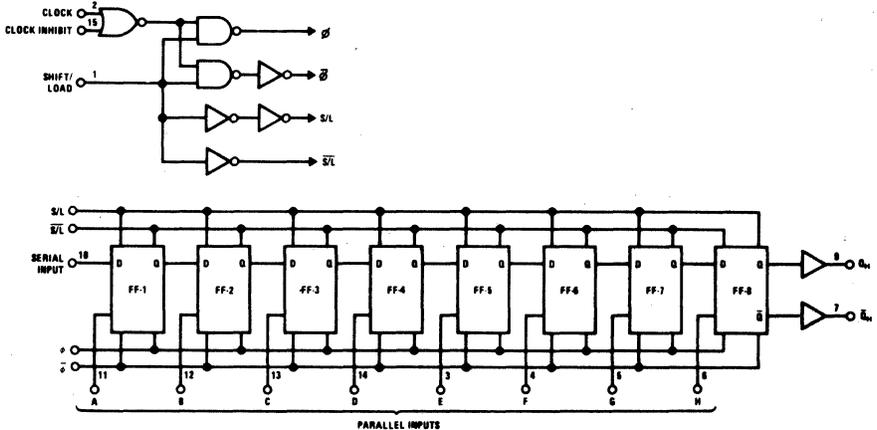
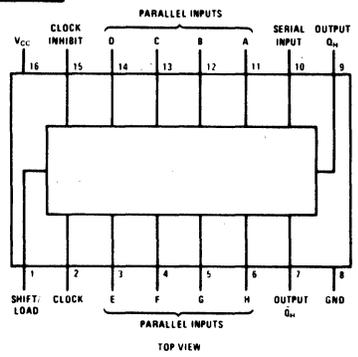
**F309**



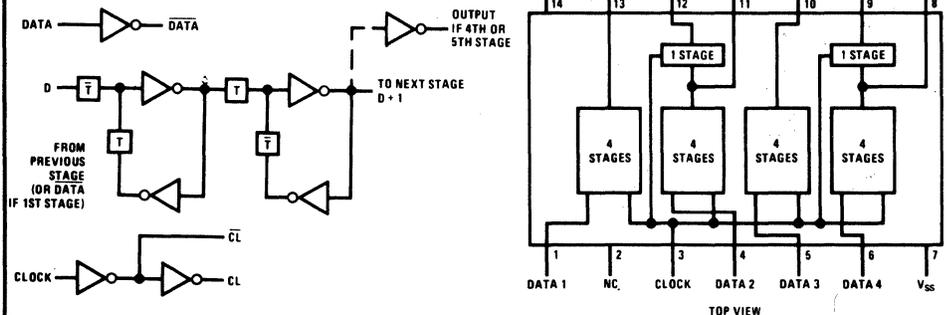
**F310**



**F311**



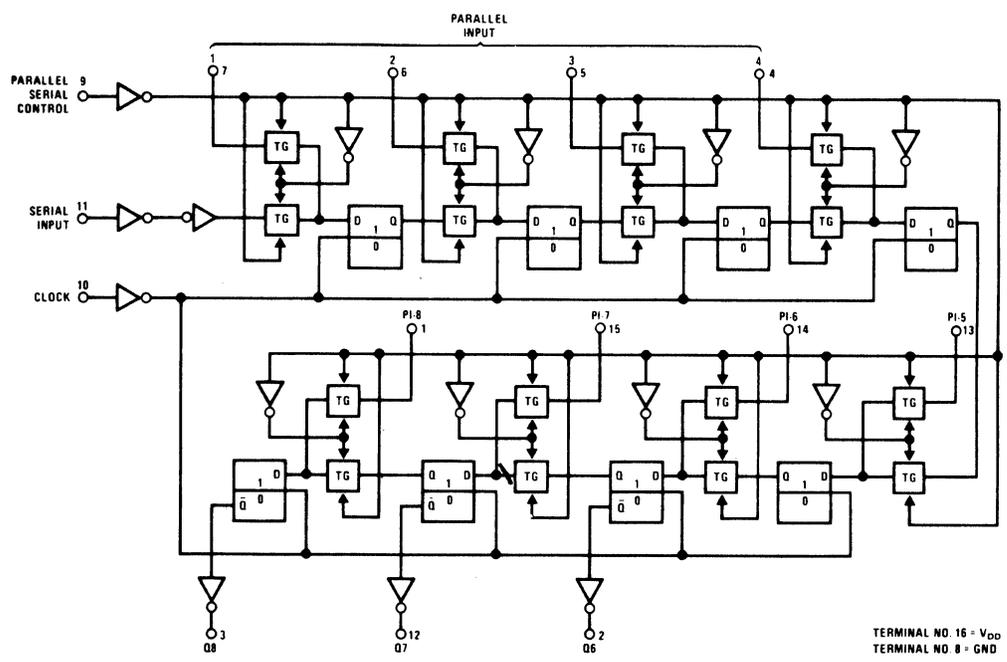
**F312**



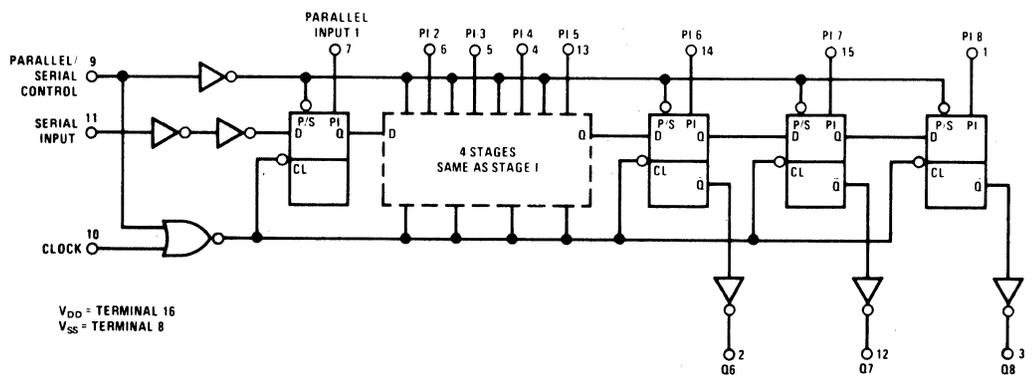
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

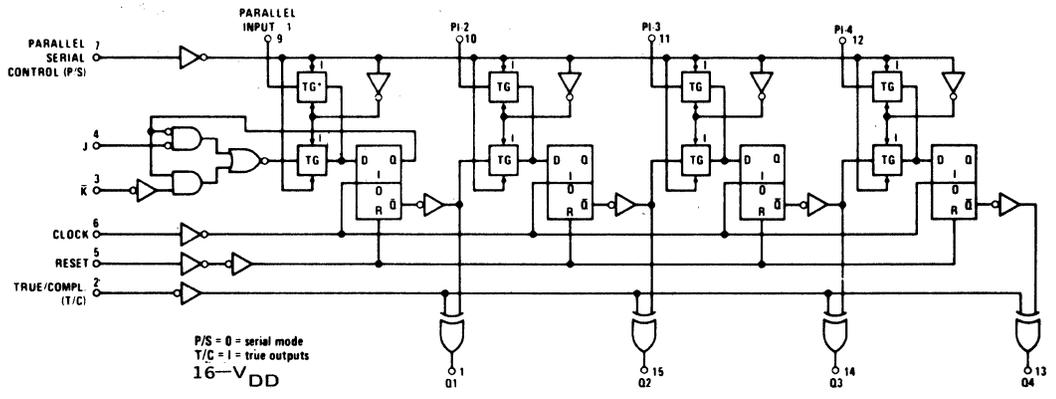
**F313**



**F314**



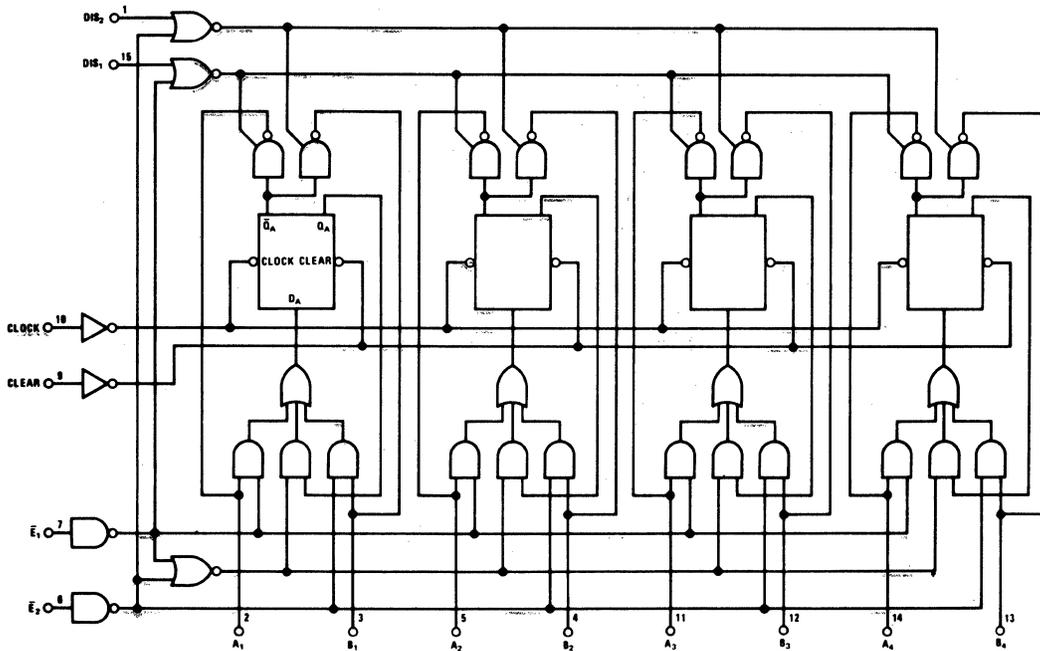
**F315**



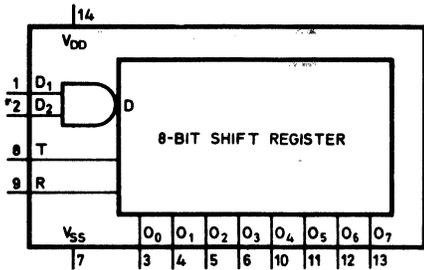
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

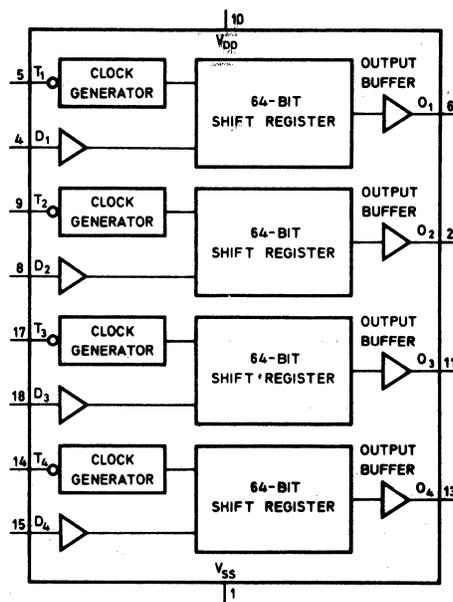
F316



F317



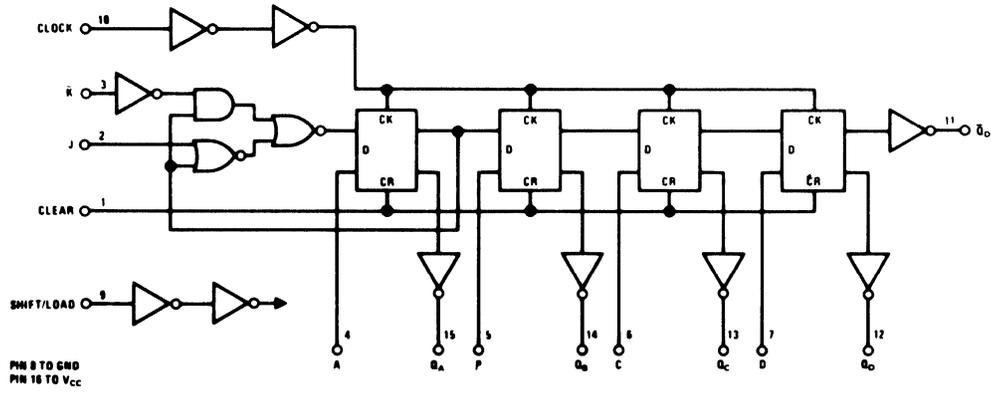
F318



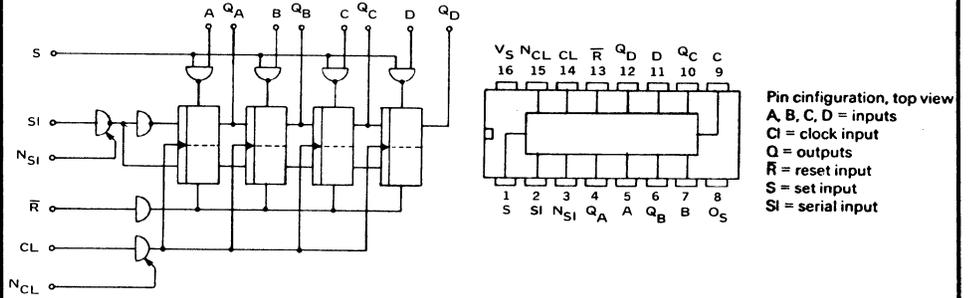
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

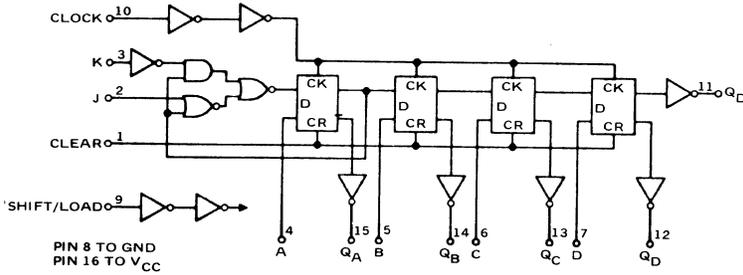
**F320**



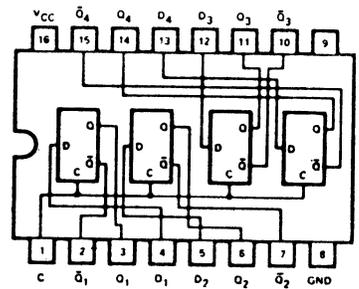
**F321**



**F323**



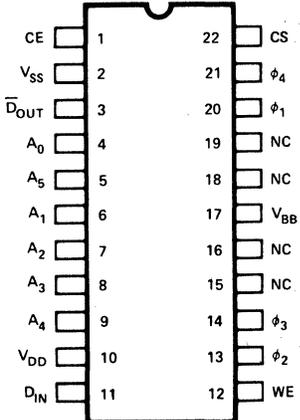
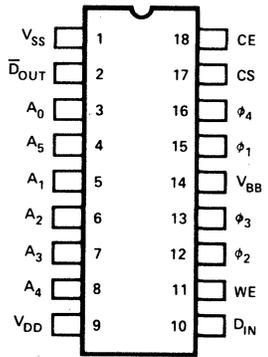
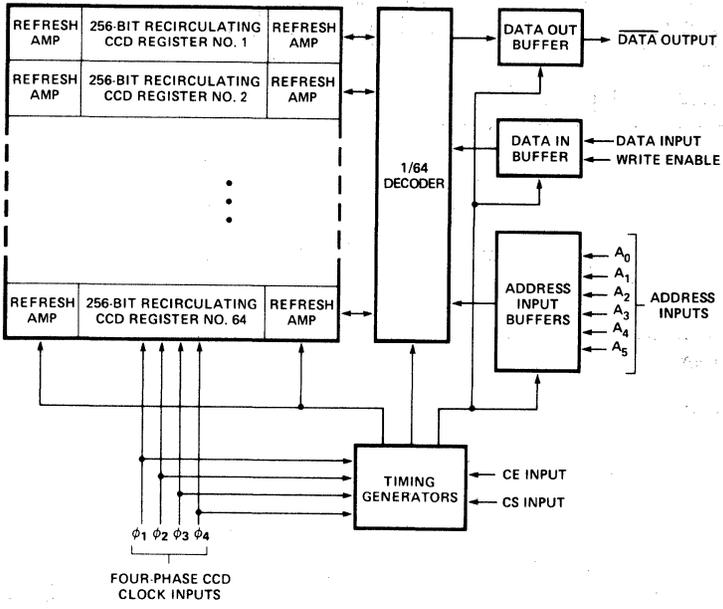
**F324**



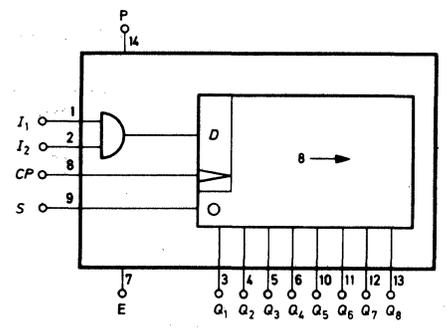
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

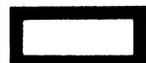
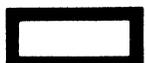
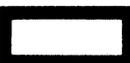
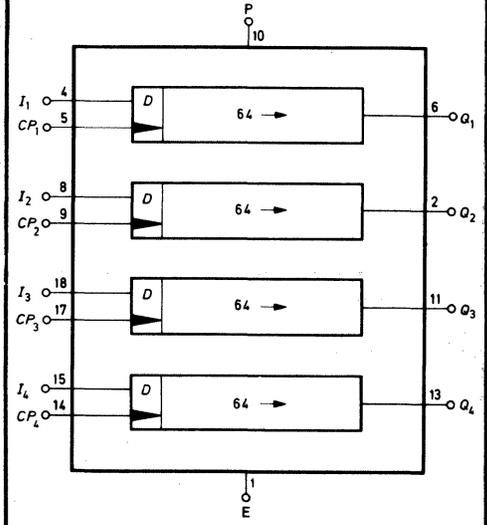
**F325**



**F236**



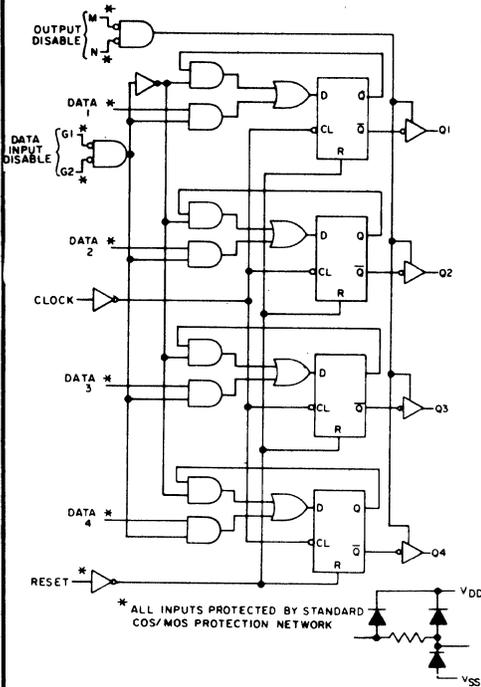
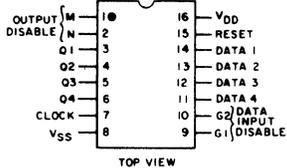
**F327**



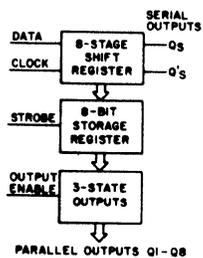
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

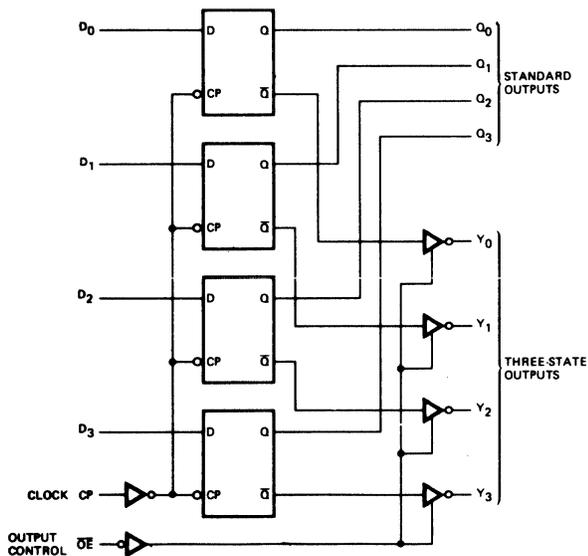
**F329**



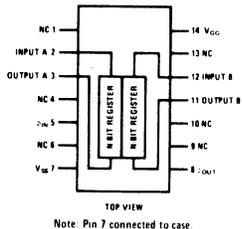
**F330**



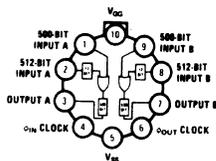
**F331**



**F332**



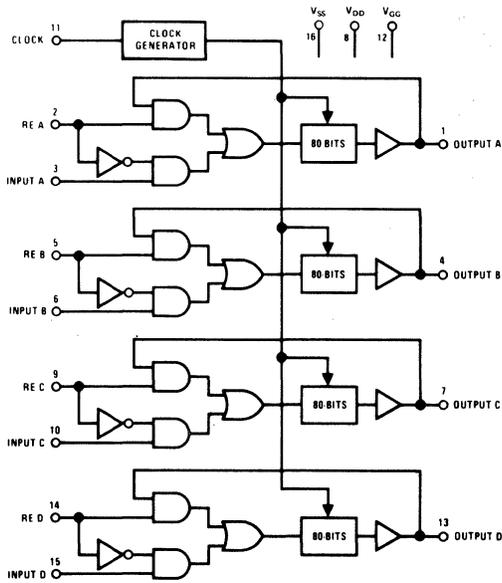
**F333**



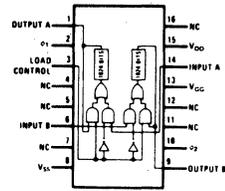
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

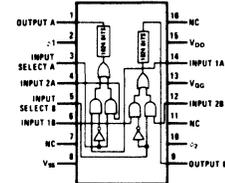
**F334**



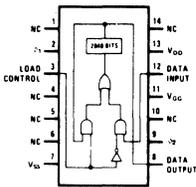
**F335**



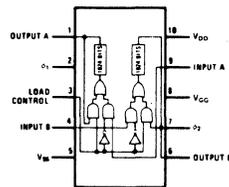
**F336**



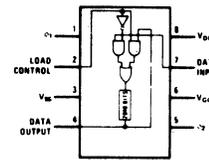
**F337**



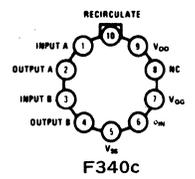
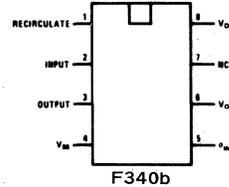
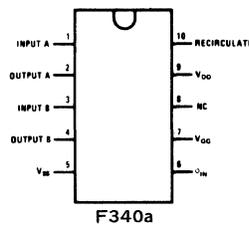
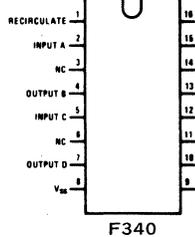
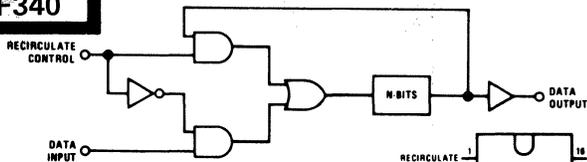
**F338**



**F339**



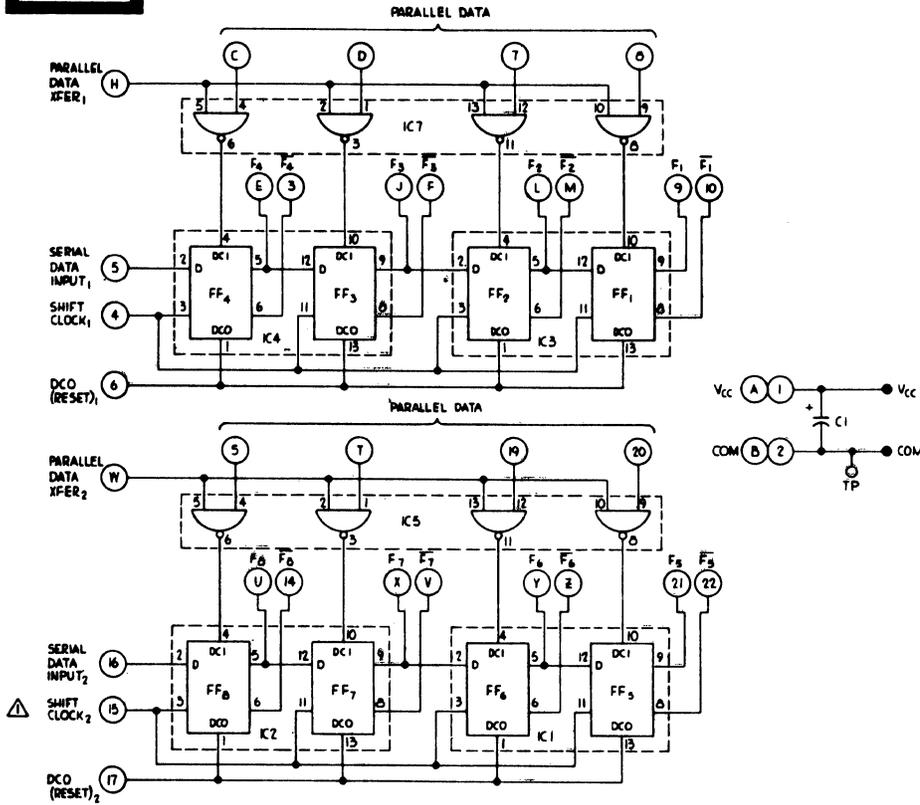
**F340**



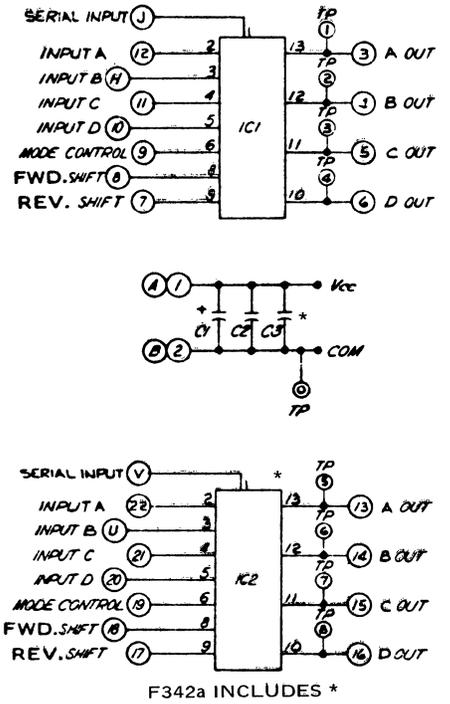
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

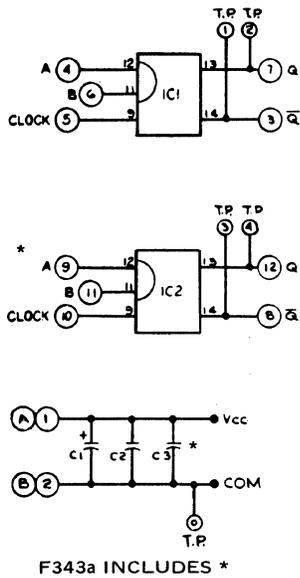
**F341**



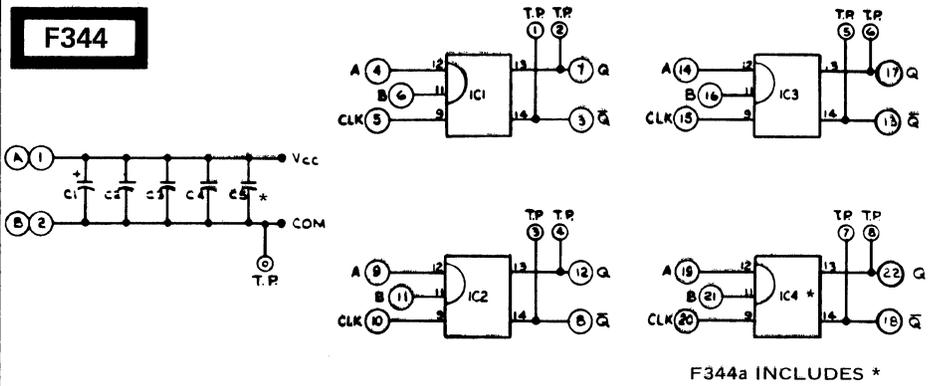
**F342**



**F343**



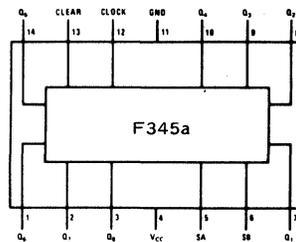
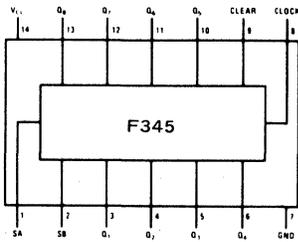
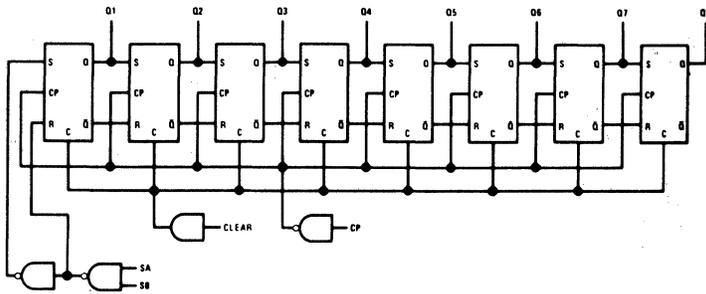
**F344**



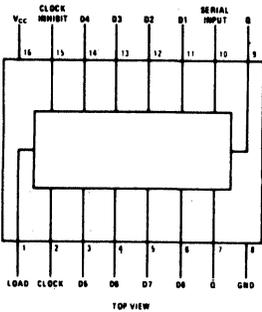
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

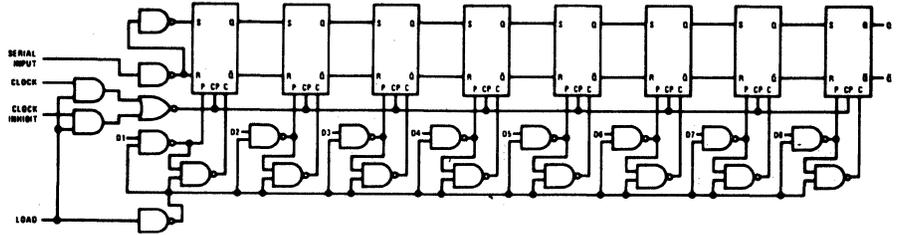
**F345**



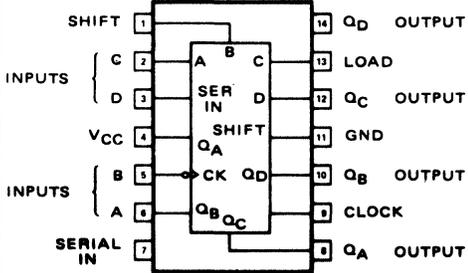
**F346**



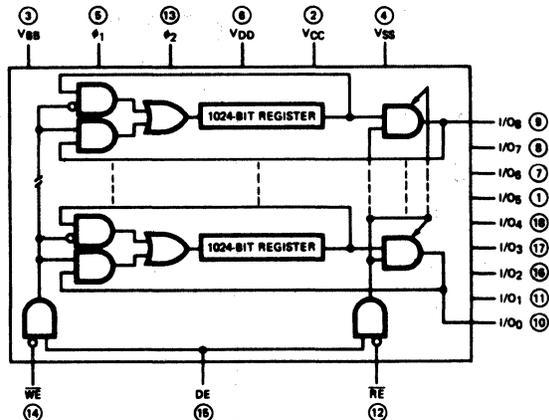
TOP VIEW



**F347**



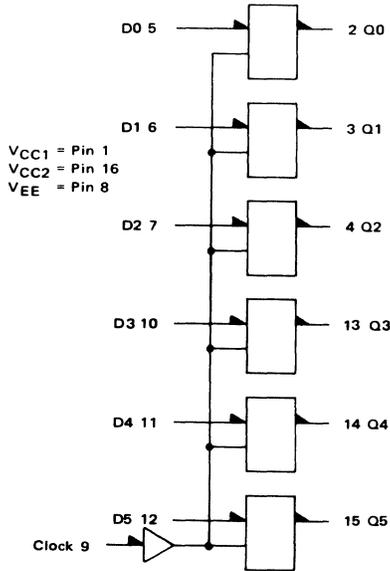
**F348**



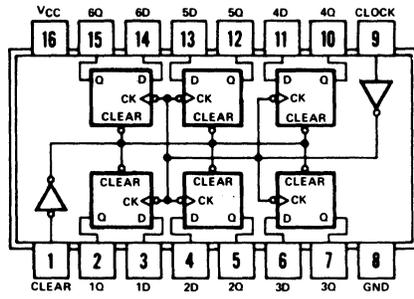
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

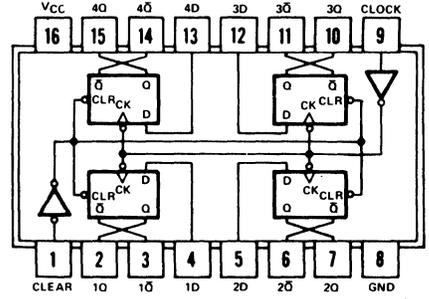
**F349**



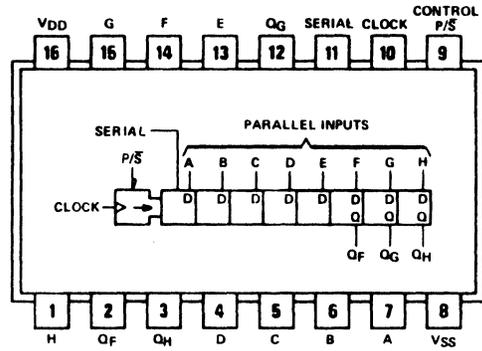
**F350**



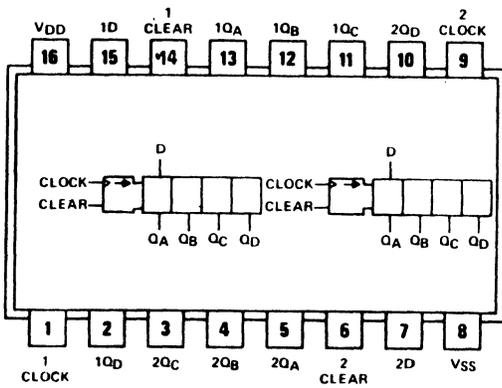
**F351**



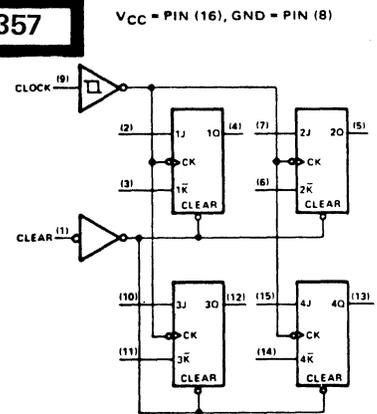
**F354**



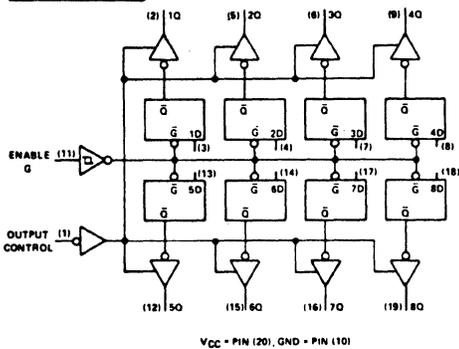
**F355**



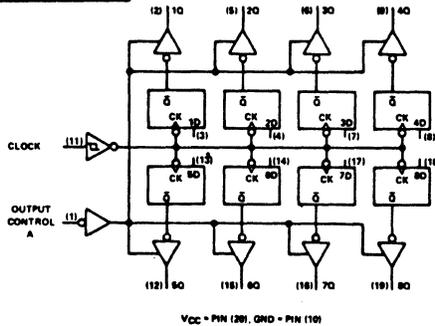
**F357**



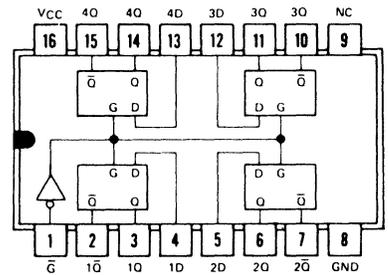
**F358**



**F359**



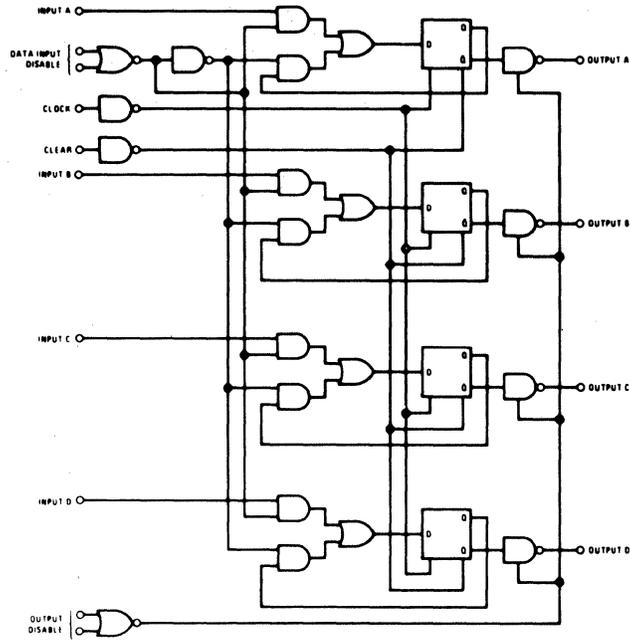
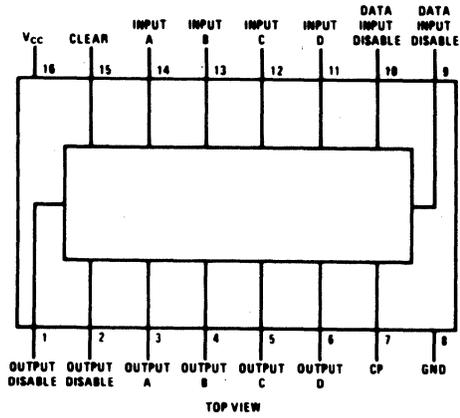
**F360**



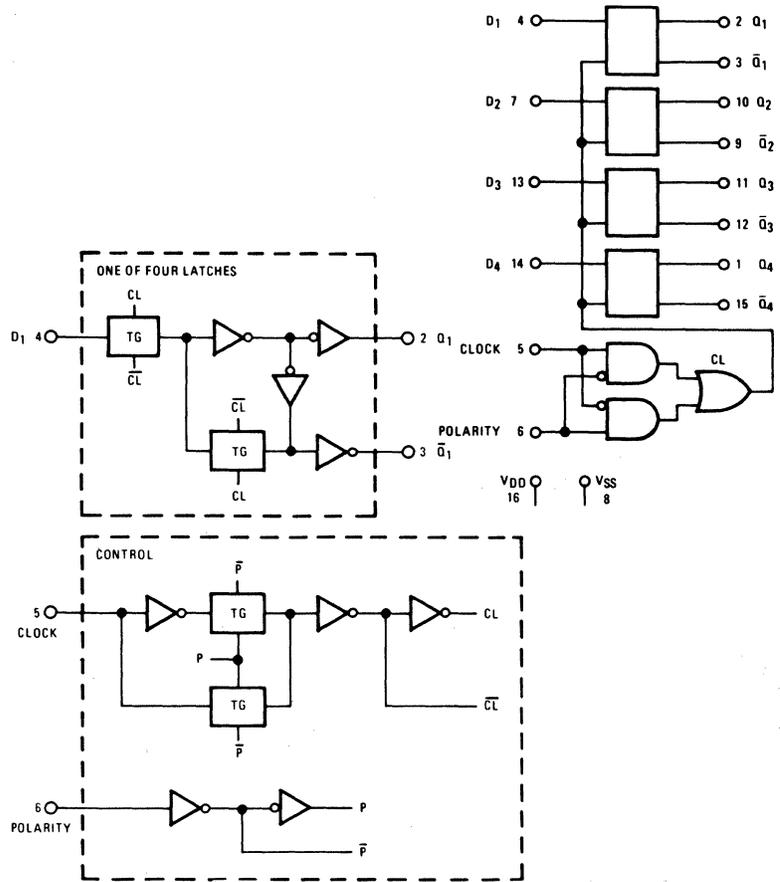
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F361**



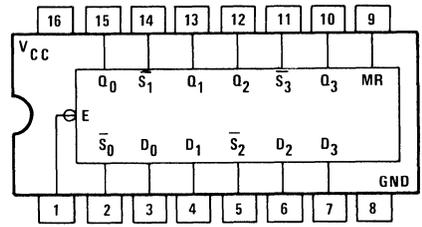
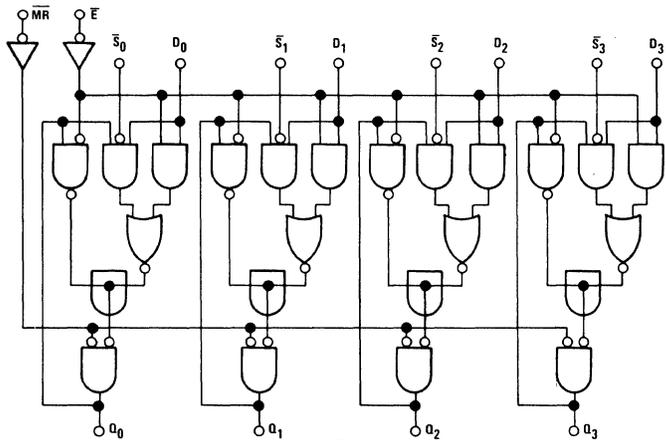
**F362**



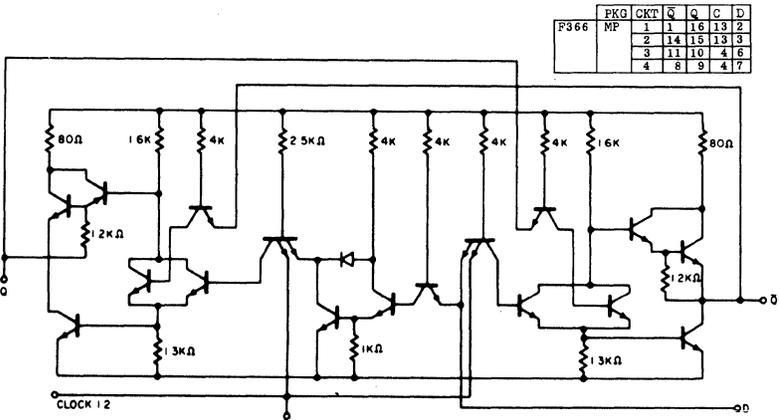
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

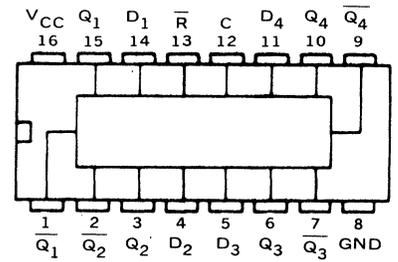
**F363**



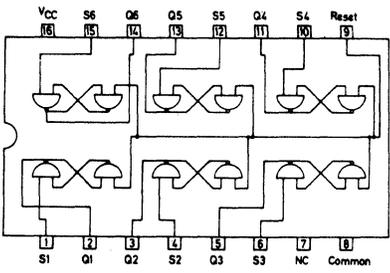
**F366**



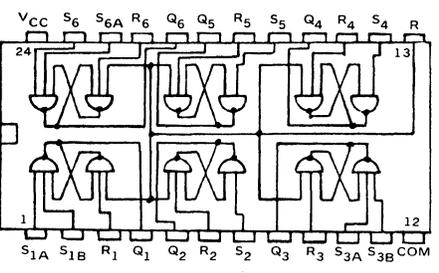
**F367**



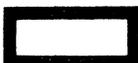
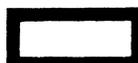
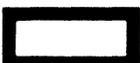
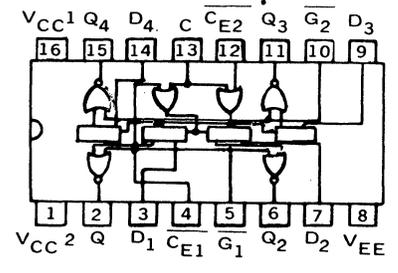
**F368**



**F369**



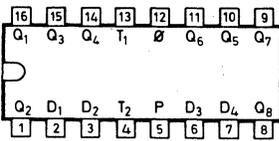
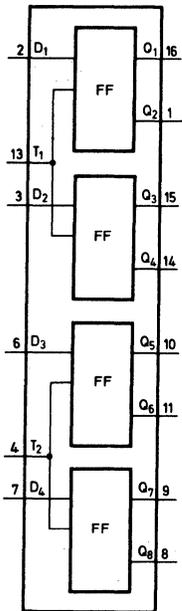
**F370**



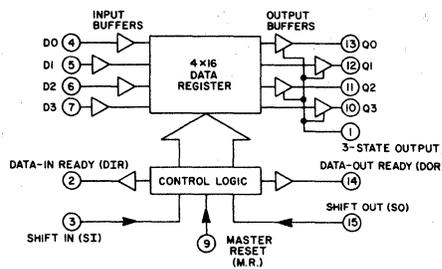
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

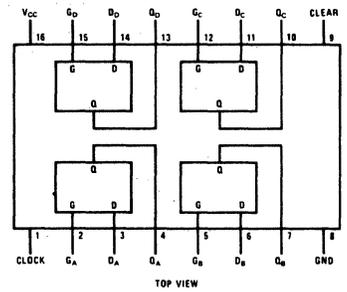
**F372**



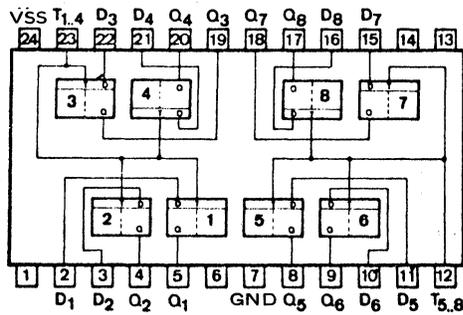
**F373**



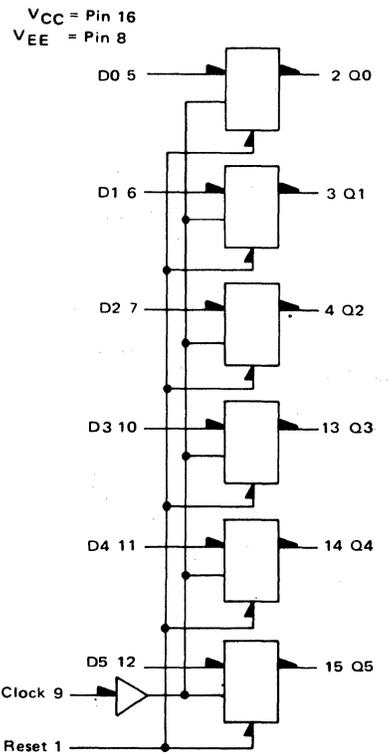
**F374**



**F375**

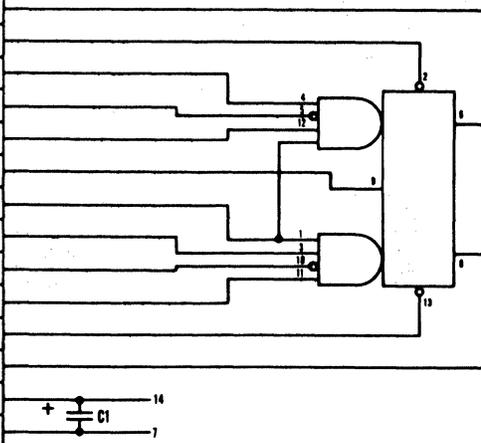


**F378**

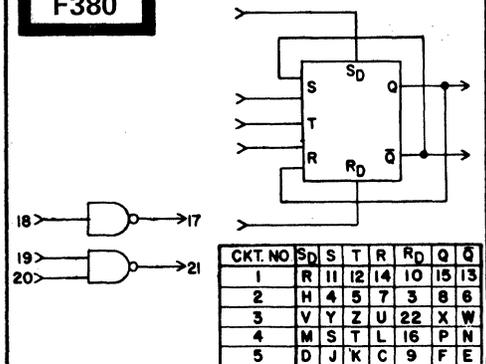


**F379**

FUNCTION	CONNECTOR PINS			
	IC1	IC2	IC3	IC4
Q	50	38	26	14
SD	46	34	22	10
J1	48	36	24	12
J	47	35	23	11
J2	40	28	16	4
CP	39	27	15	8
JK	44	32	20	3
K1	49	37	25	13
K	42	30	18	6
K2	41	29	17	5
CD	45	33	21	9
Q̄	43	31	19	7
Vcc	51			
GRD	1	52		



**F380**



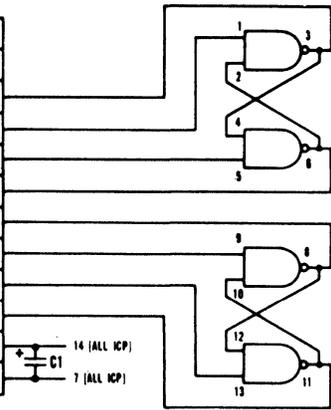
CKT. NO	S <sub>D</sub>	S	T	R	R̄ <sub>D</sub>	Q	Q̄
1	R	11	12	14	10	15	13
2	H	4	5	7	3	8	6
3	V	Y	Z	U	22	X	W
4	M	S	T	L	16	P	N
5	D	J	K	C	9	F	E

# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

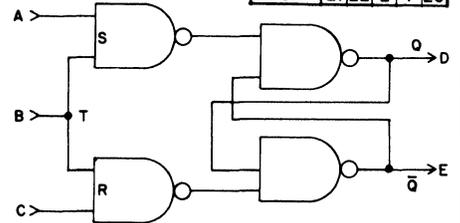
**F381**

FUNCTION	CONNECTOR PINS					
	IC1	IC2	IC3	IC4	IC5	IC6
Q	45	37	29	21	13	5
S	43	35	27	19	11	3
R	47	39	31	23	15	7
$\bar{Q}$	49	41	33	25	17	9
Q	44	36	28	20	12	4
S	46	38	30	22	14	6
R	50	42	34	26	18	10
$\bar{Q}$	48	40	32	24	16	8
Vcc	51					
GRD	1					52



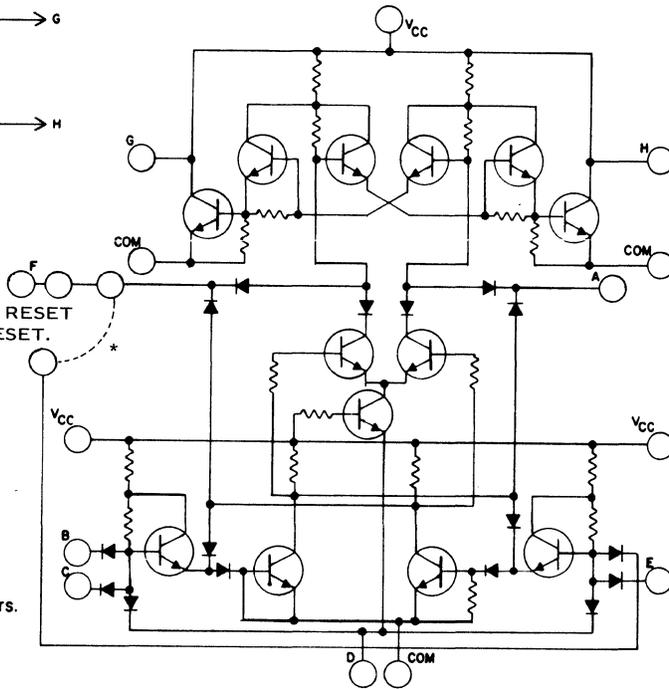
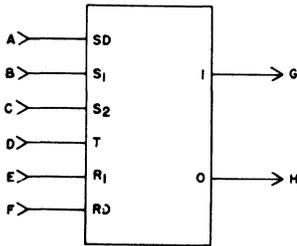
**F383**

CKT. NO	A	B	C	D	E
1	C	3	4	E	D
2	6	7	H	F	5
3	J	8	9	L	K
4	I	12	N	M	10
5	P	13	14	S	R
6	16	17	U	T	15
7	V	18	19	X	W
8	21	22	Z	Y	20



**F384**

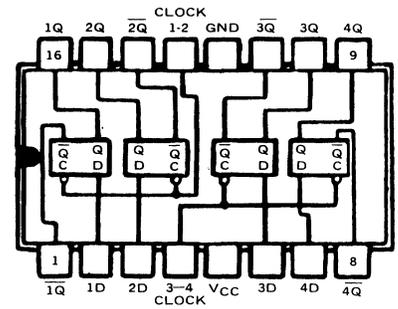
CKT. NO.	A	B	C	D	E	F	G	H	Vcc	COM
1	2	6	5	7	9	8	1	10	34	3
2	17	13	12	14	15	16	11	18		
3	25	21	20	22	23	24	19	26		
4	31	29	28	30	33	32	27	38		



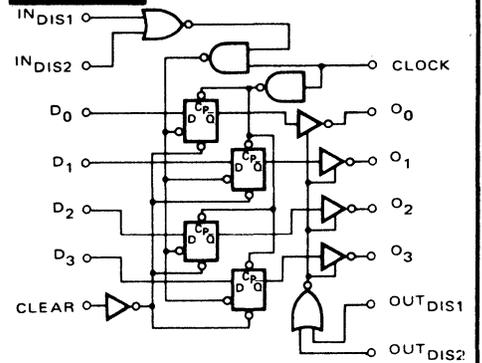
\* NOTE: THE DIRECT RESET WIRED AS SECOND RESET.

4 CKTS.

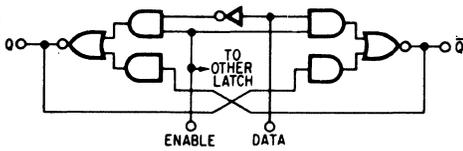
**F386**



**F387**

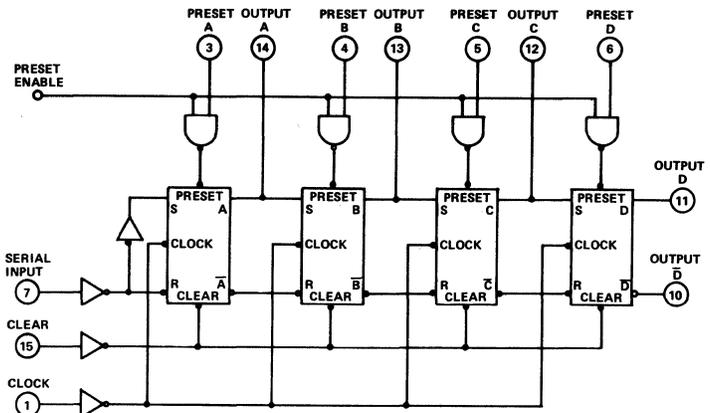


**F388**

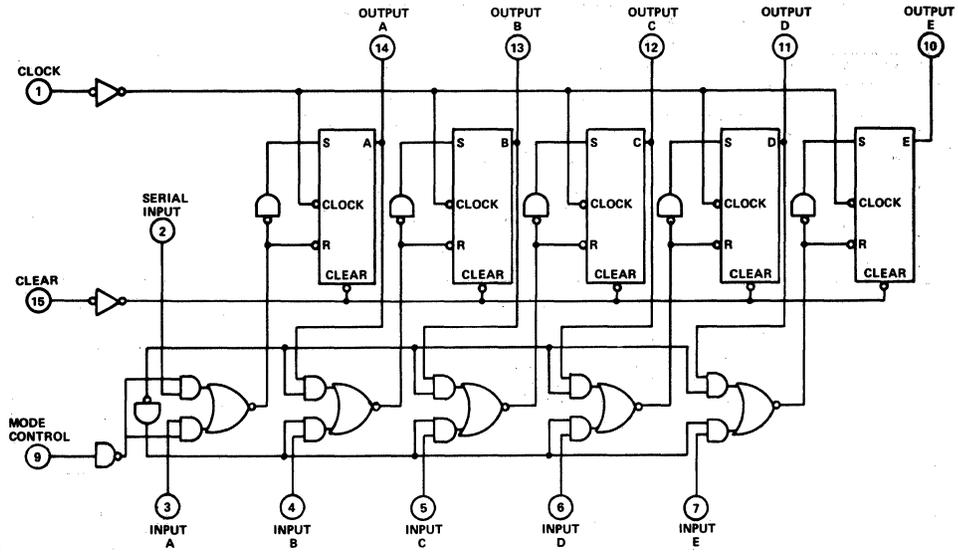


CKT	Q	E	D	Q	Vcc	GND
1	4	16	2	3	16	8
2	5	15	1	6	16	8
3	11	7	13	12	16	8
4	10	7	14	9	16	8

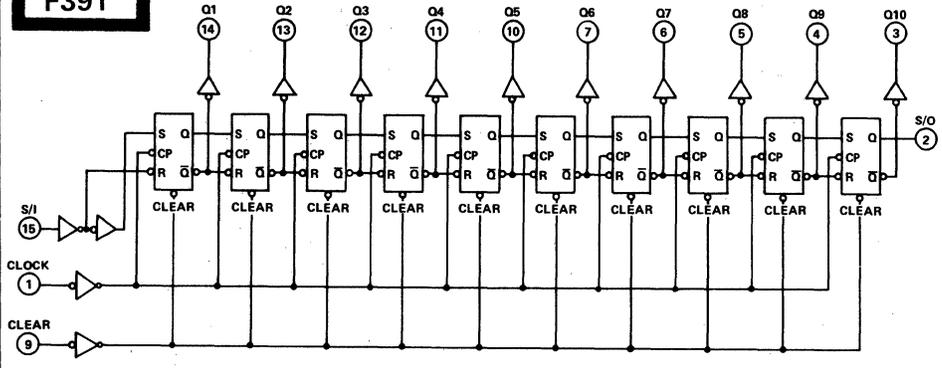
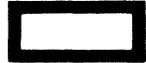
**F389**



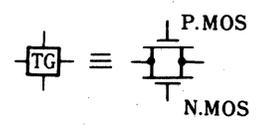
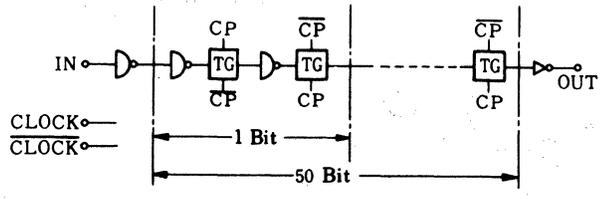
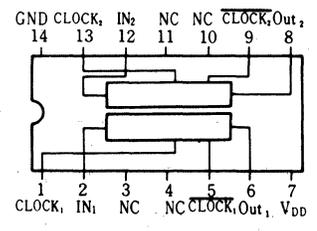
**F390**



**F391**



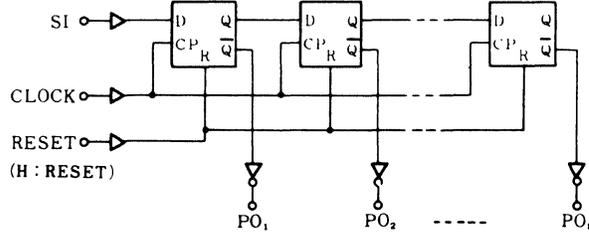
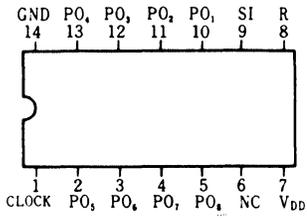
**F392**



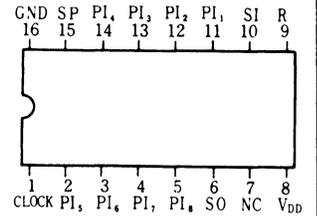
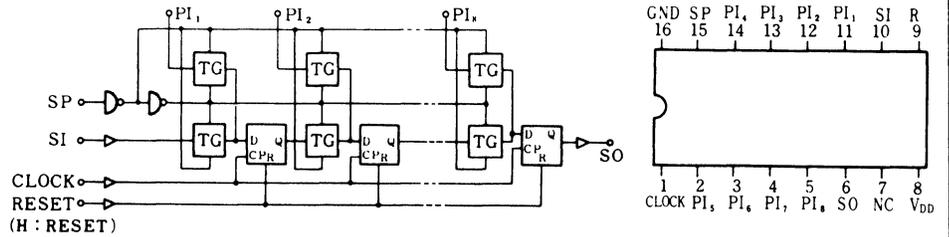
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

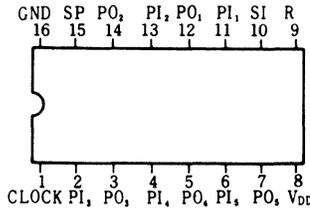
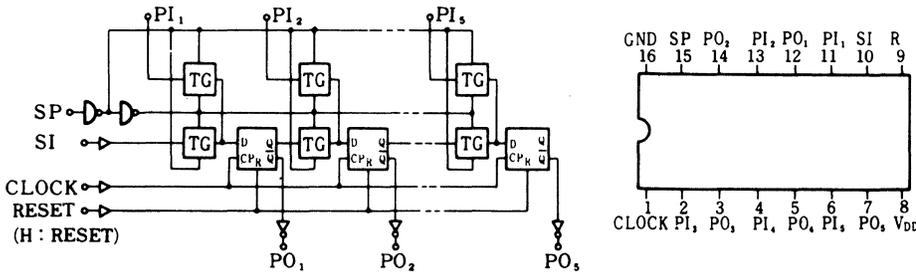
**F393**



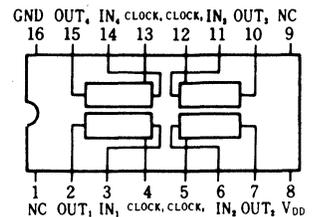
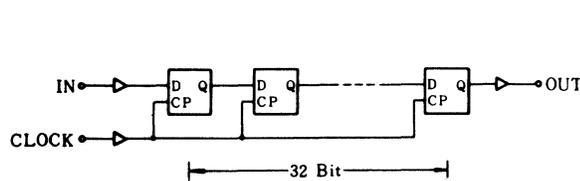
**F394**



**F395**



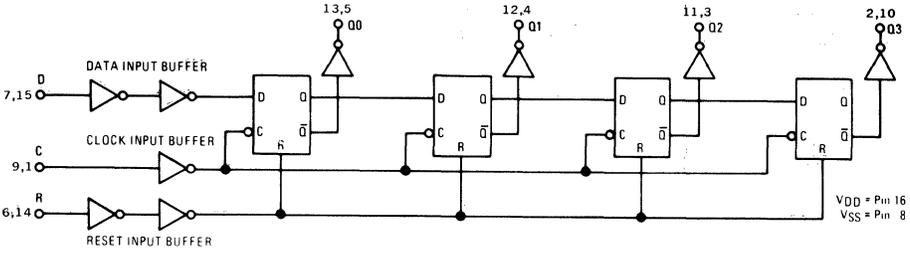
**F396**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

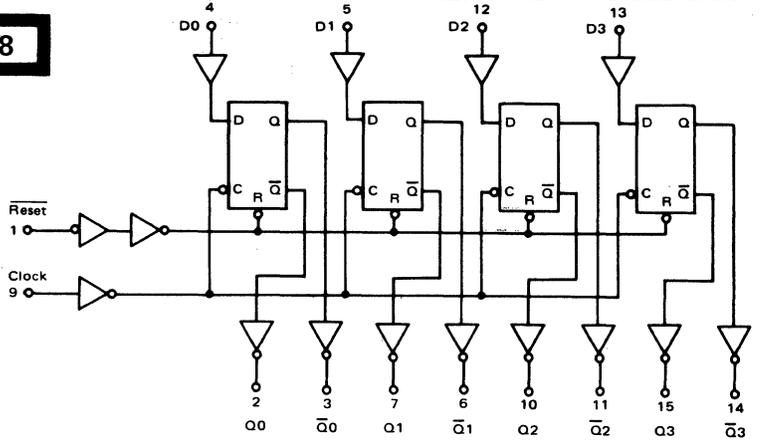
**F397**



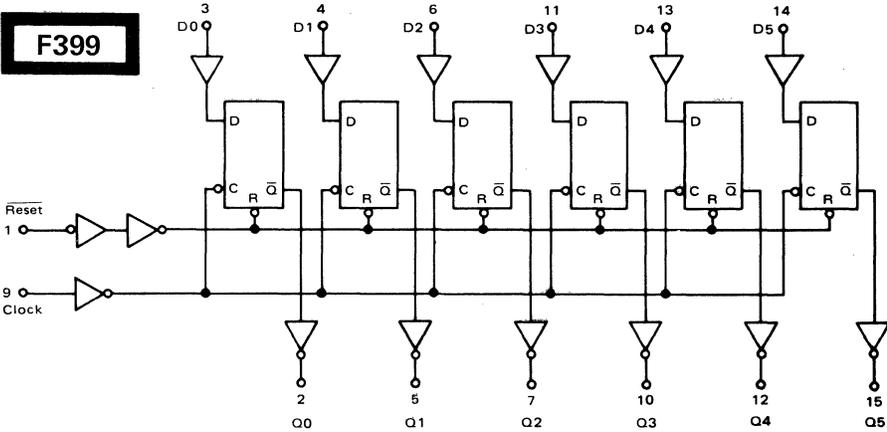
[Empty box]

[Empty box]

**F398**

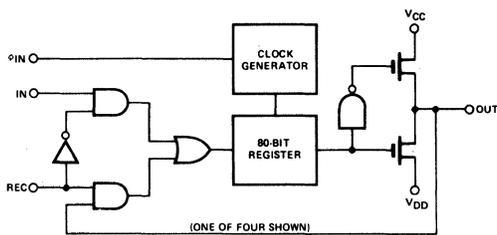


**F399**

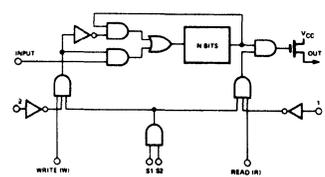


[Empty box]

**F400**



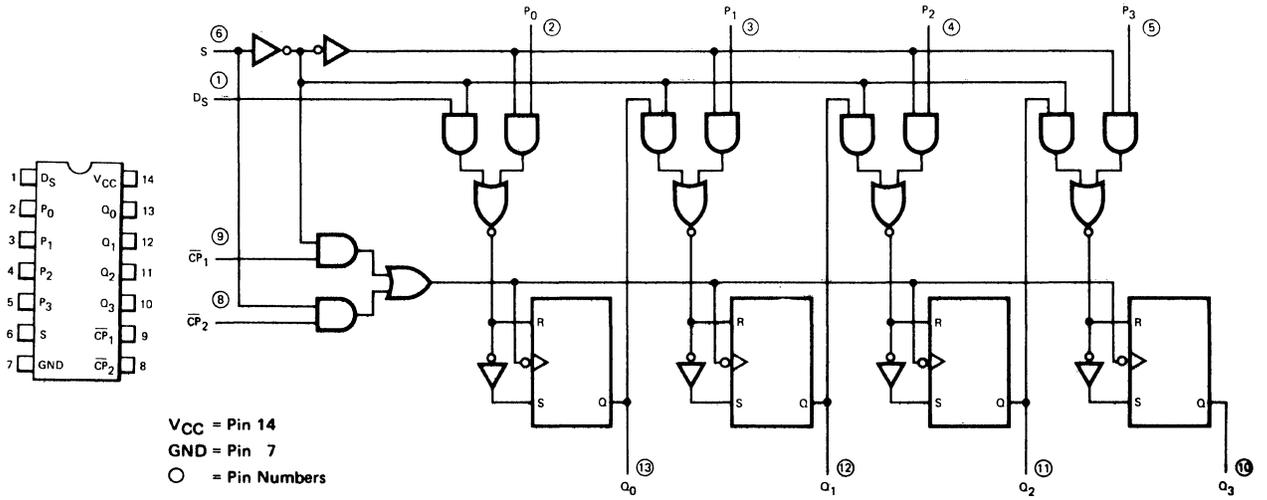
**F401**



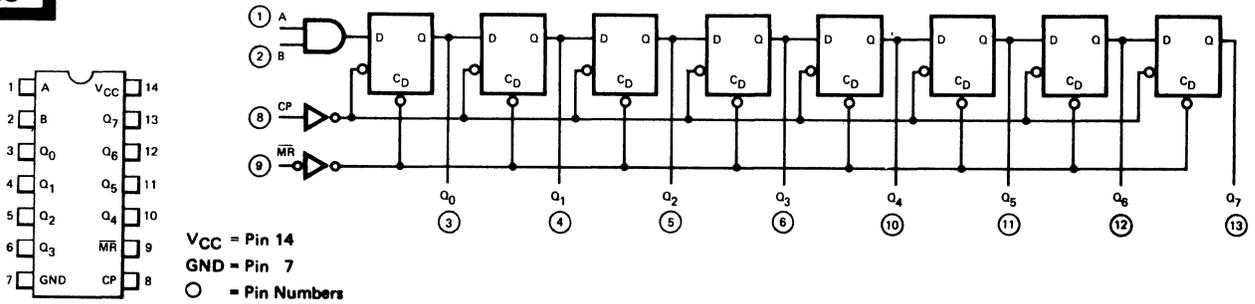
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

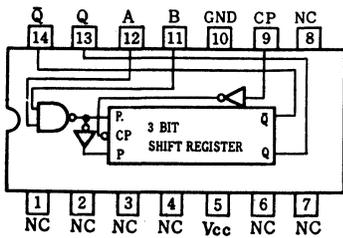
**F402**



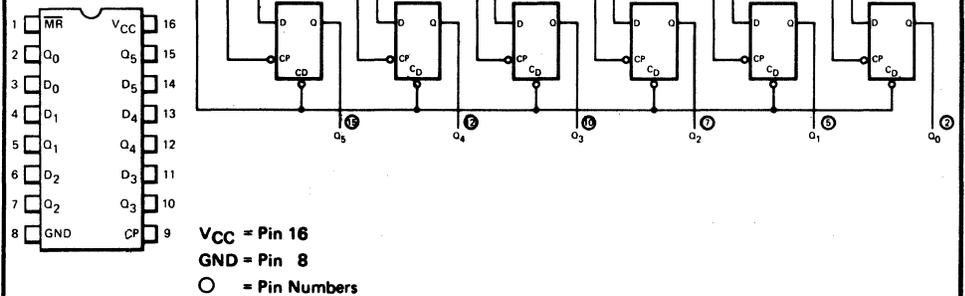
**F403**



**F404**



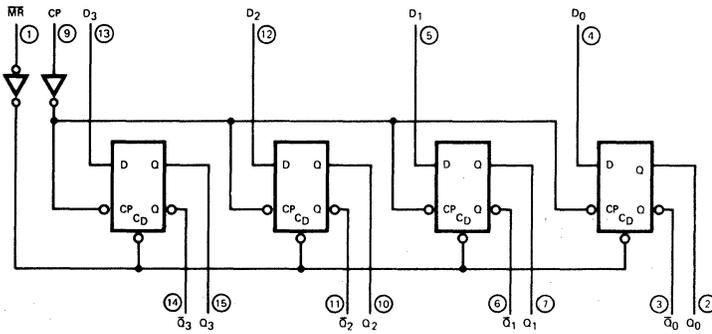
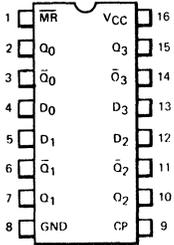
**F405**



# 22. LOGIC/BLOCK DRAWINGS

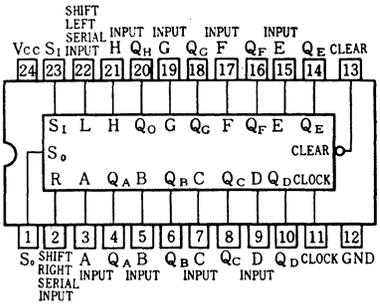
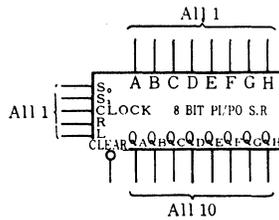
IN DRAWING NUMBER  
SEQUENCE

**F406**

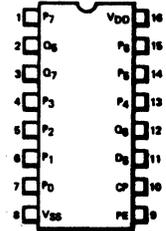
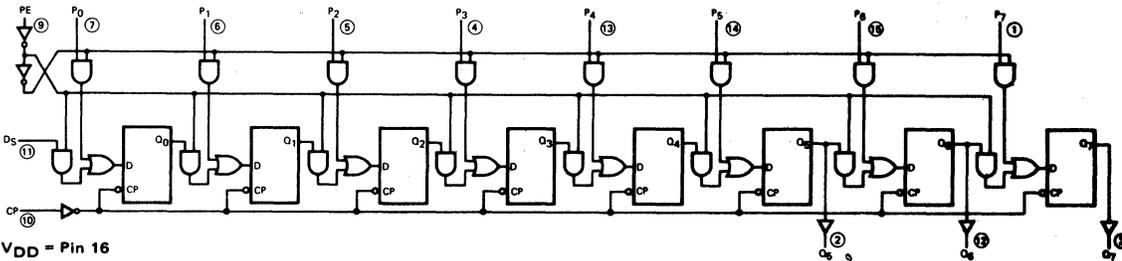


V<sub>CC</sub> = Pin 16  
GND = Pin 8  
○ = Pin Numbers

**F407**

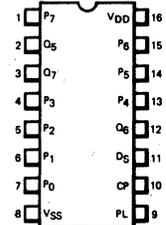
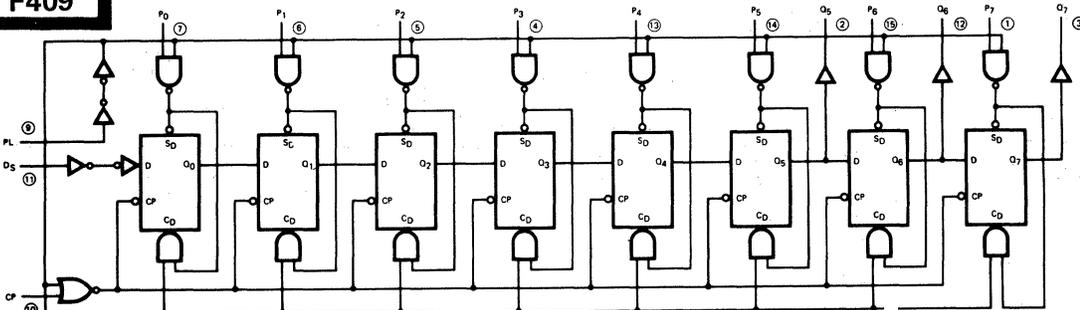


**F408**



V<sub>DD</sub> = Pin 16  
V<sub>SS</sub> = Pin 8  
○ = Pin Number

**F409**

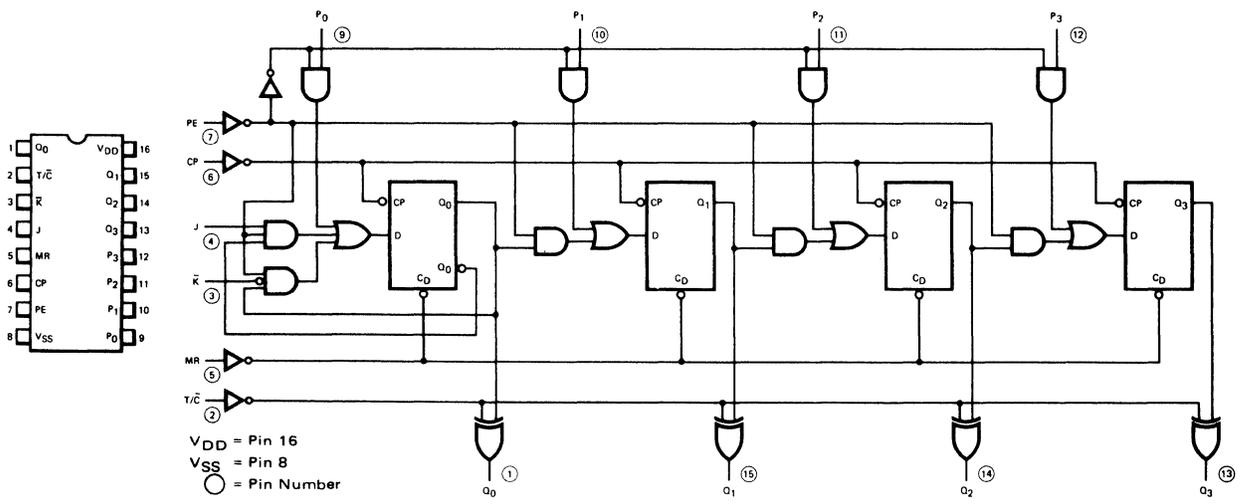


V<sub>DD</sub> = Pin 16  
V<sub>SS</sub> = Pin 8  
○ = Pin Number

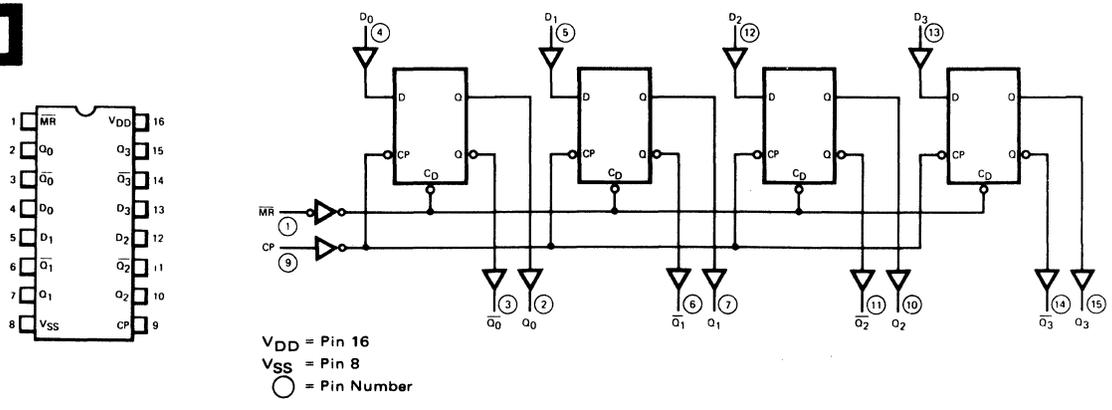
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

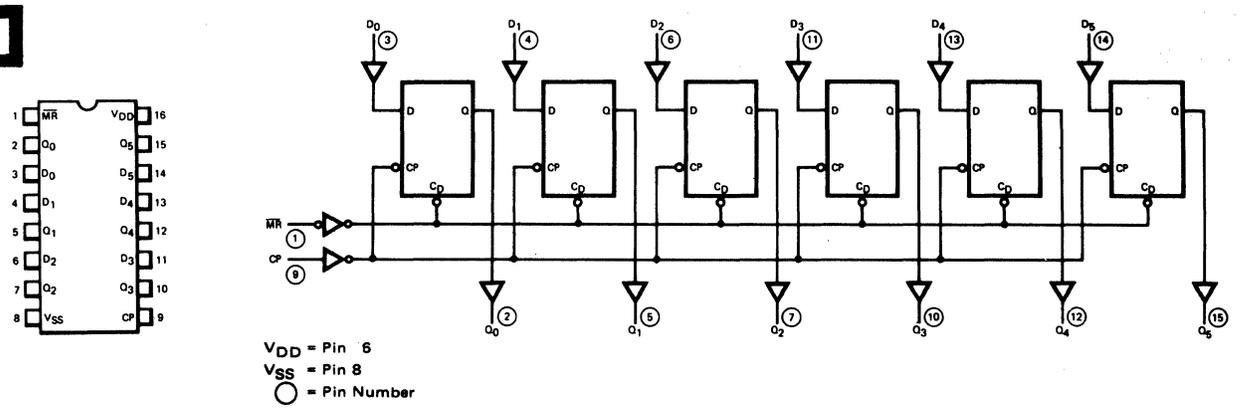
**F410**



**F411**



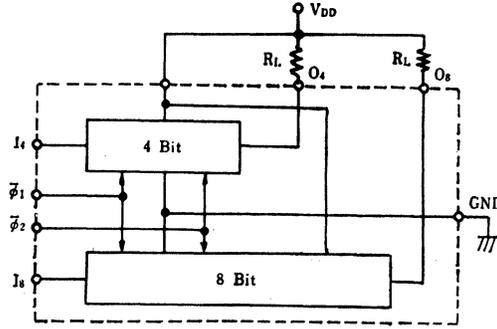
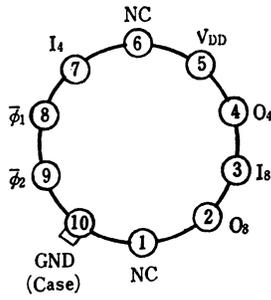
**F412**



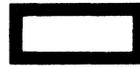
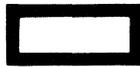
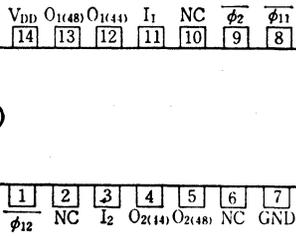
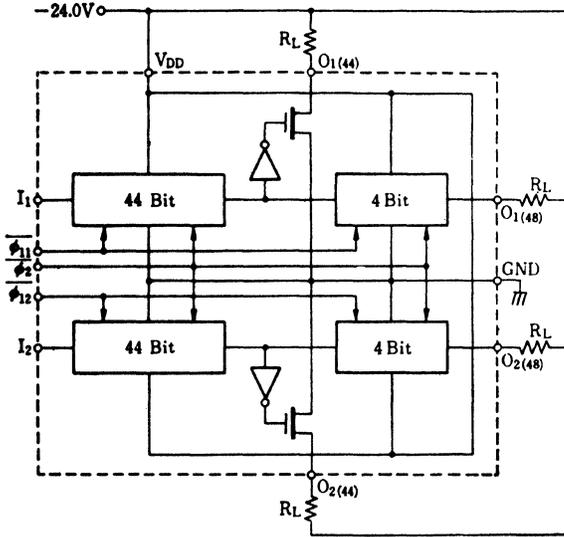
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

F413



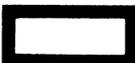
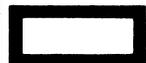
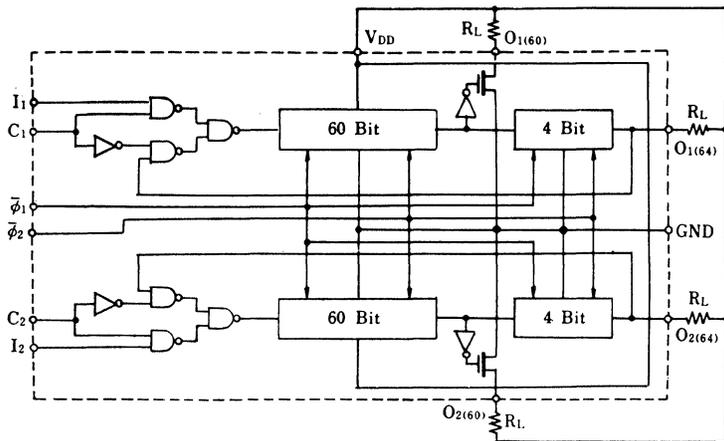
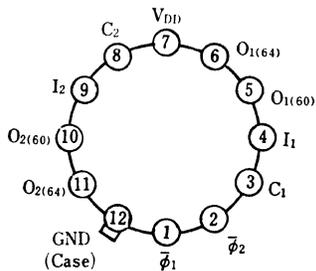
F414



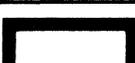
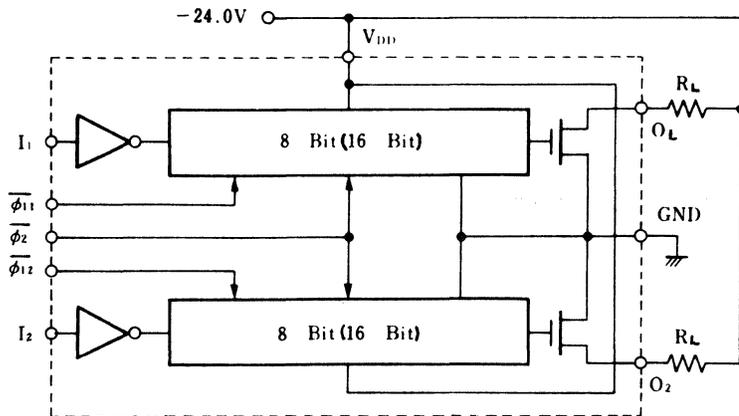
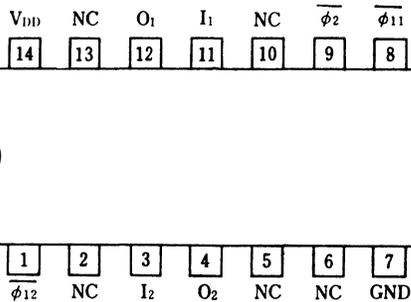
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F415**



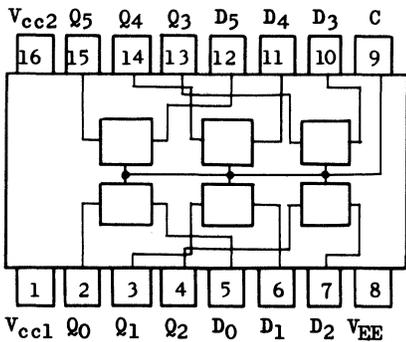
**F416**



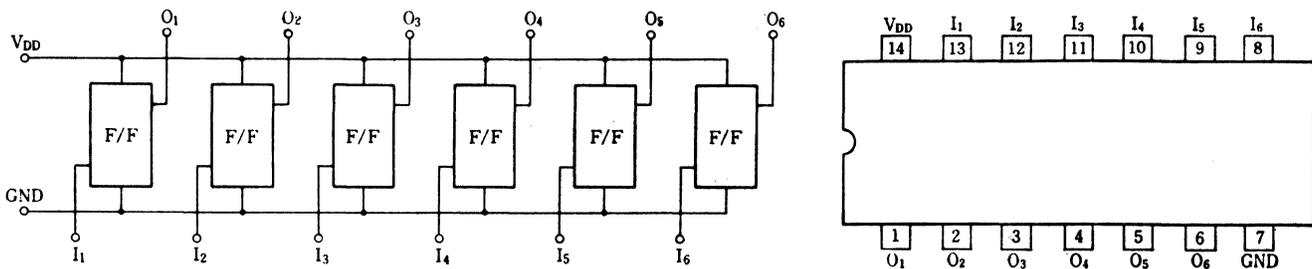
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

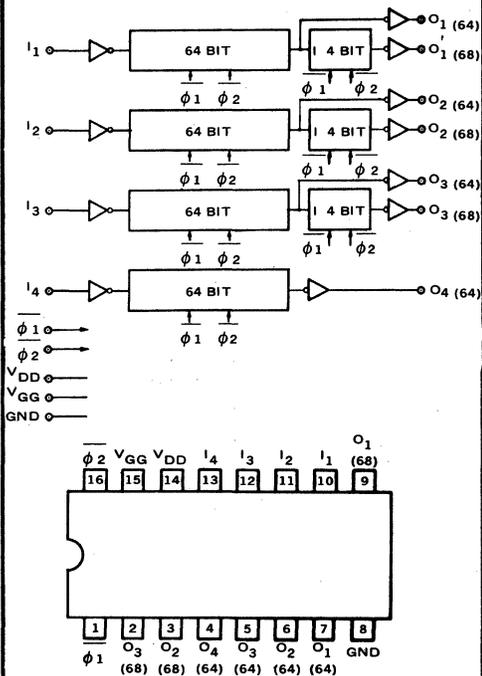
**F417**



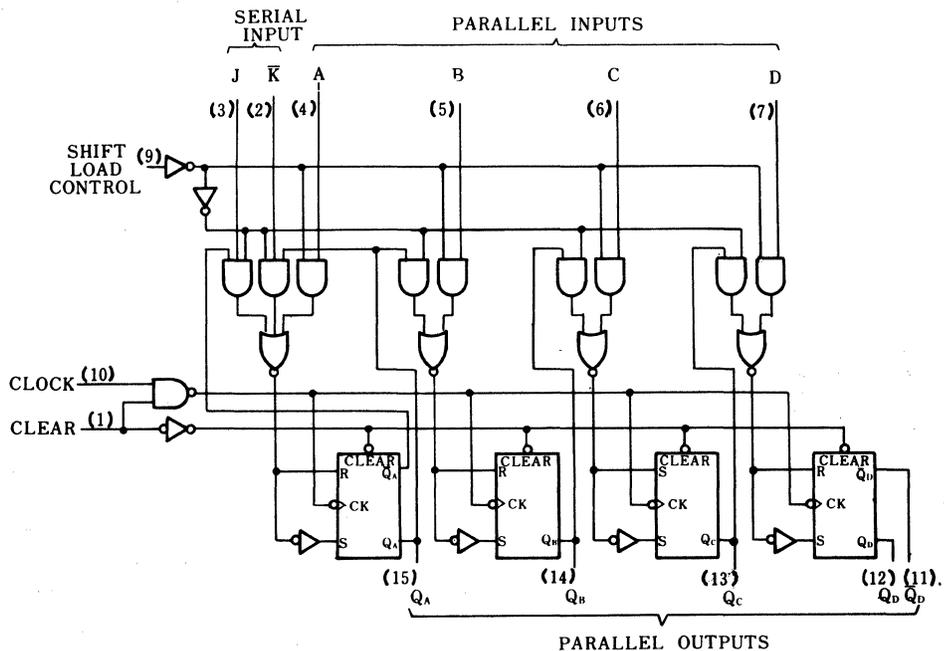
**F418**



**F420**



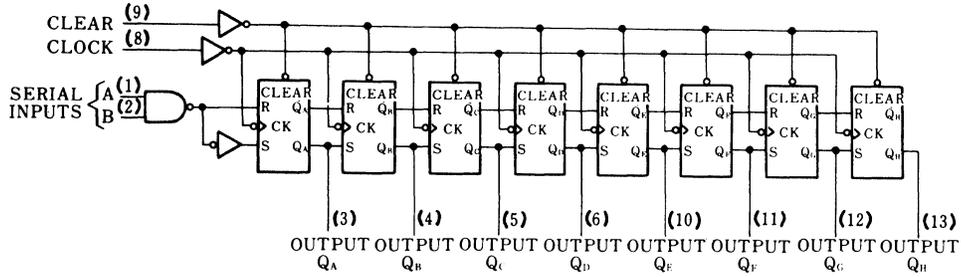
**F421**



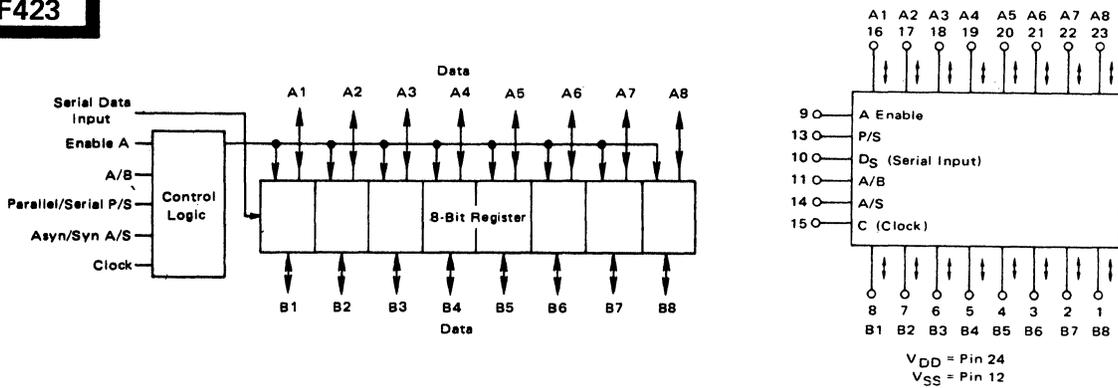
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

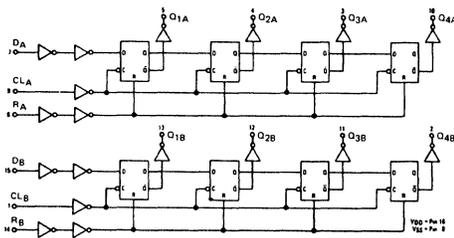
**F422**



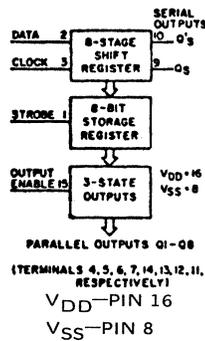
**F423**



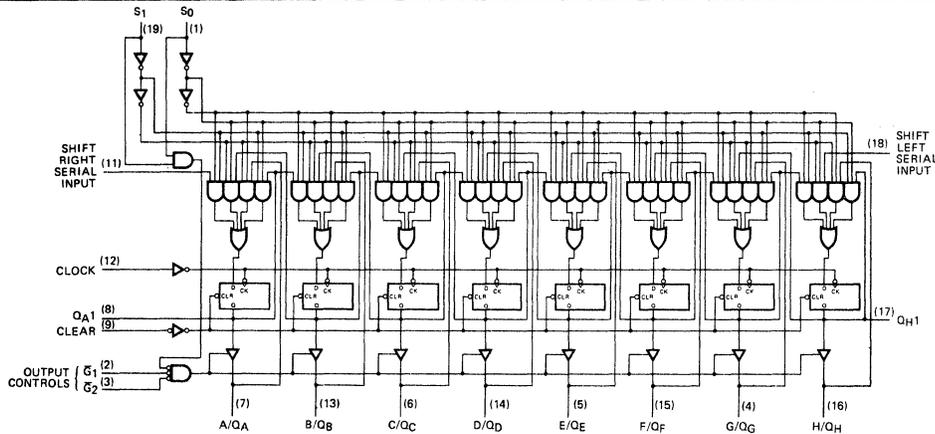
**F424**



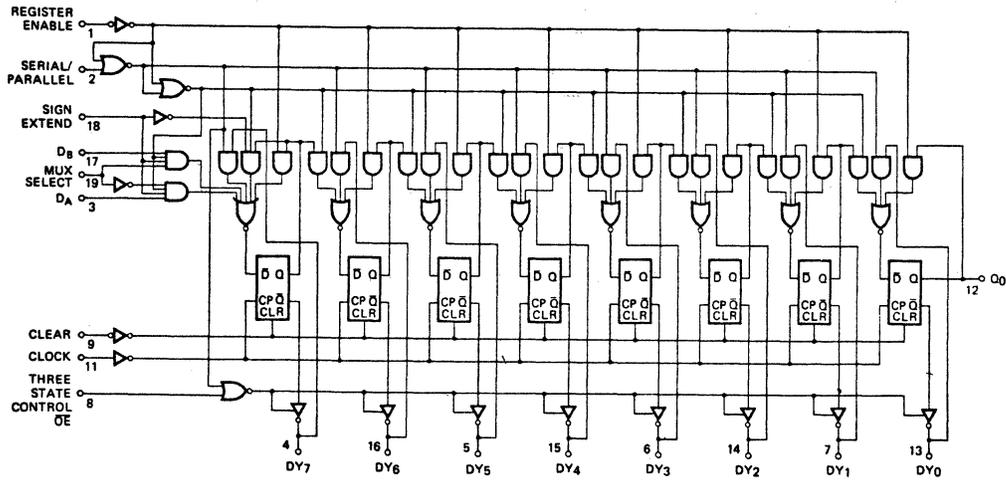
**F425**



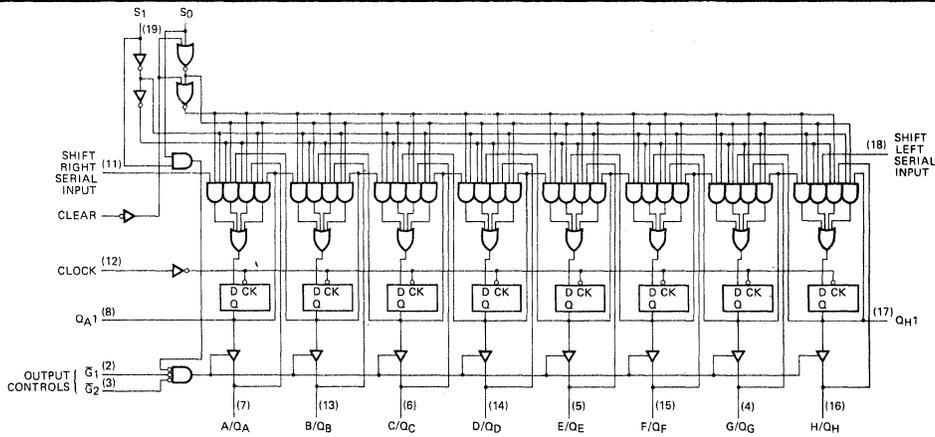
**F427**



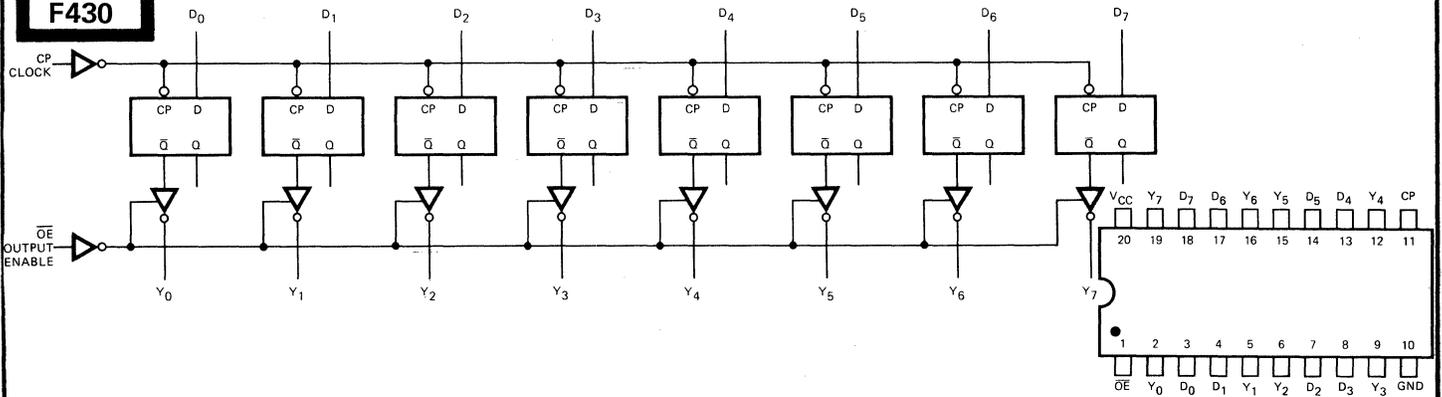
**F428**



**F429**



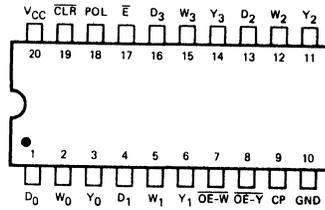
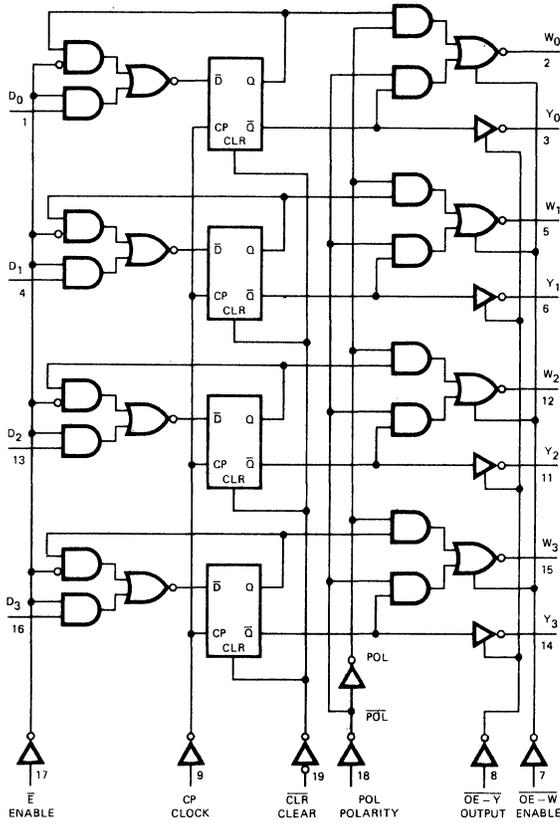
**F430**



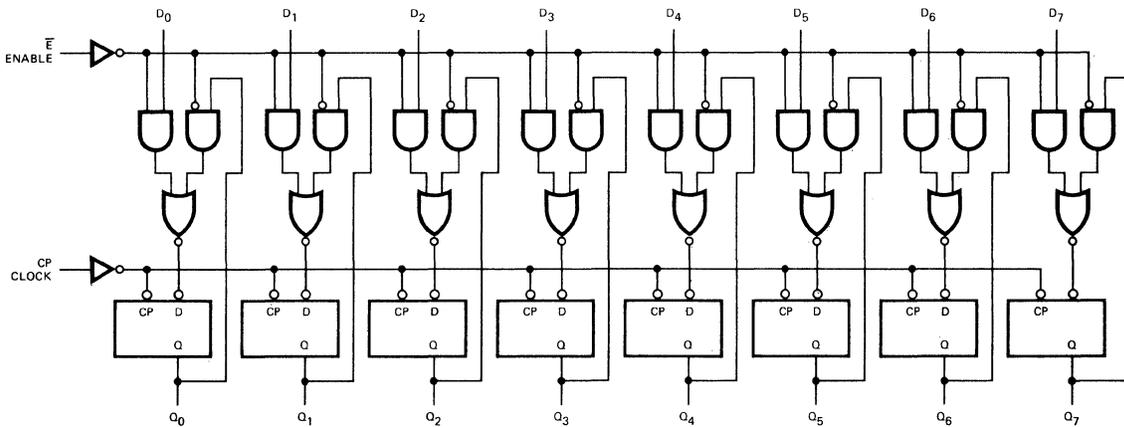
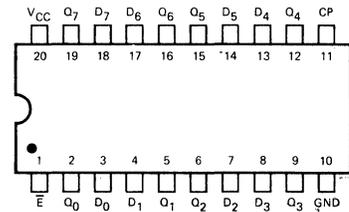
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

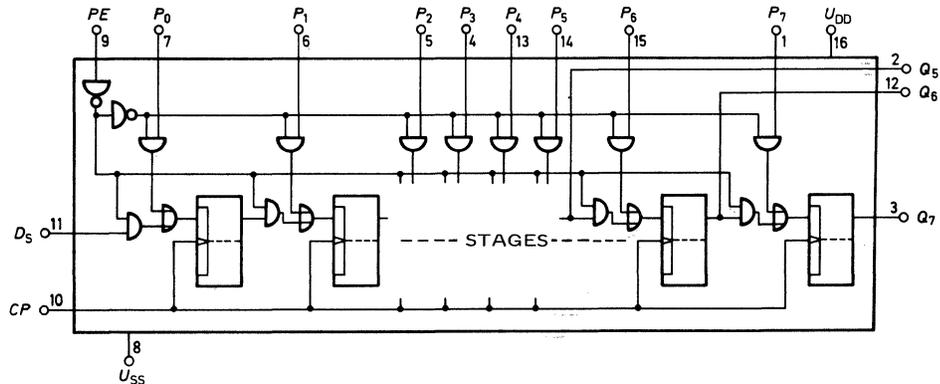
**F431**



**F432**



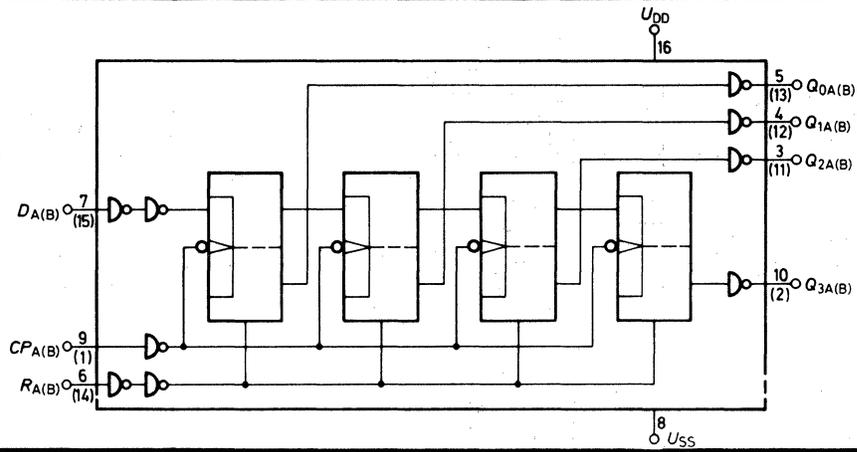
**F433**



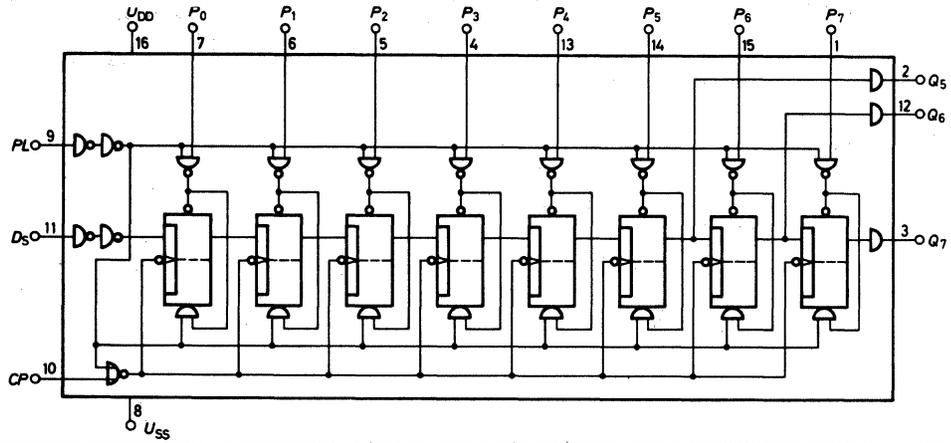
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

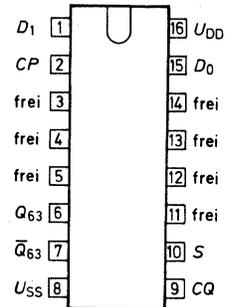
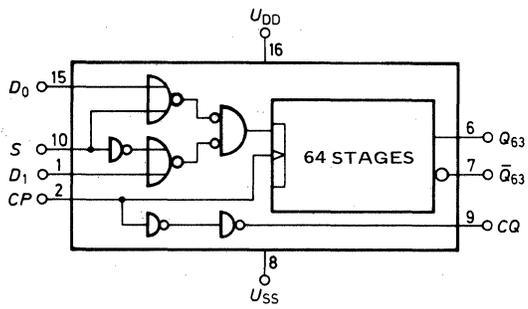
**F434**



**F435**

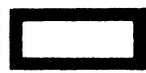
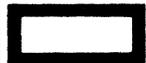
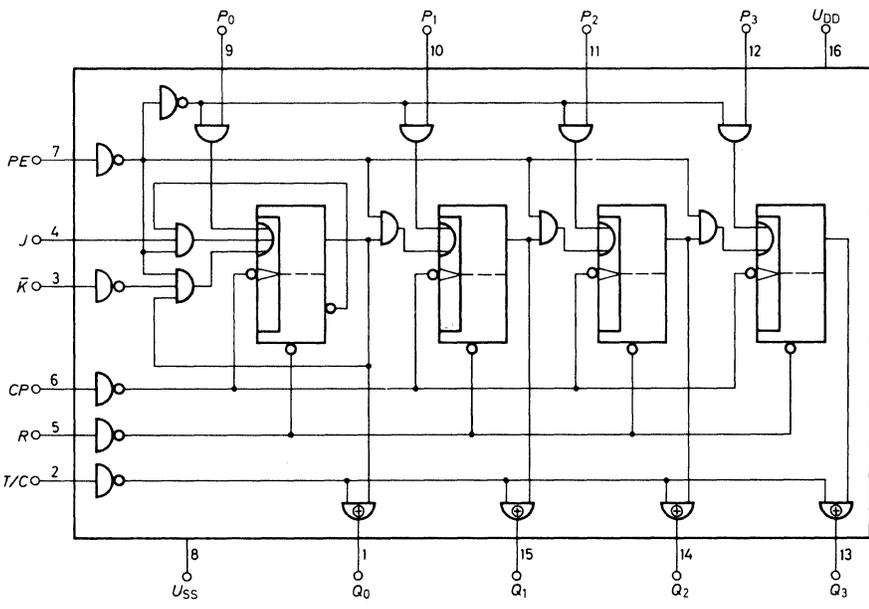


**F436**

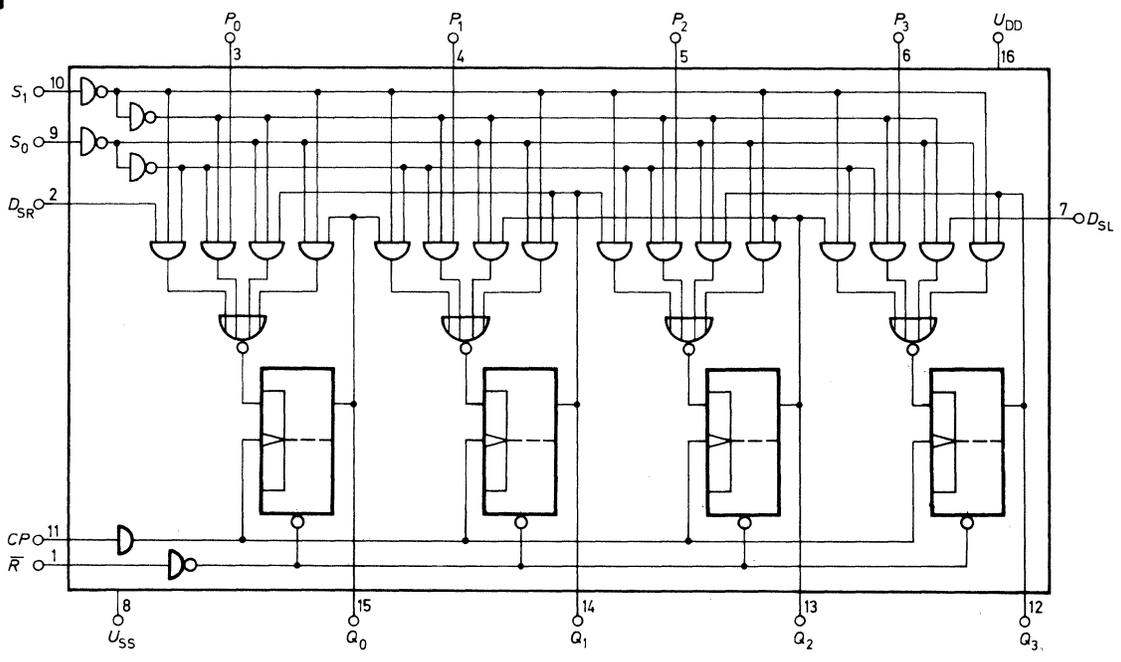


**22. LOGIC/BLOCK DRAWINGS** **IN DRAWING NUMBER SEQUENCE**

**F437**



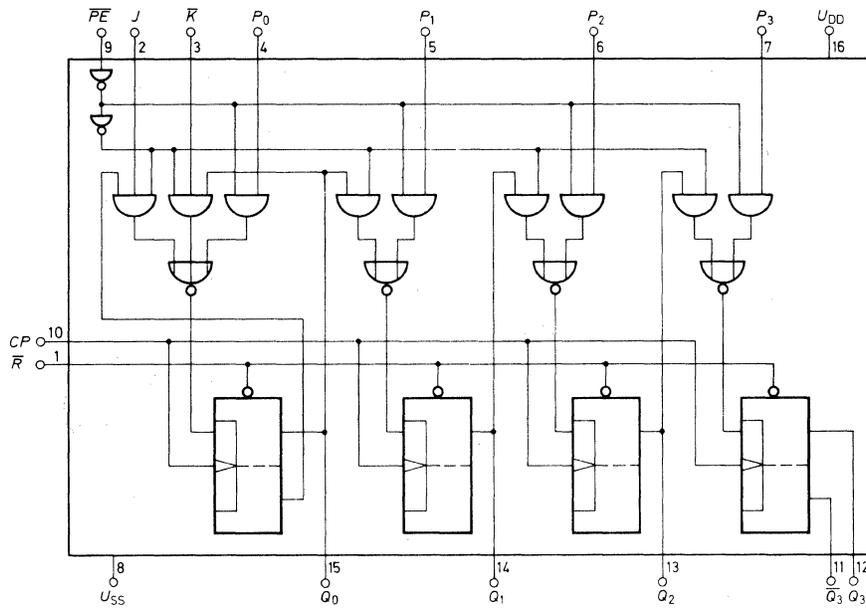
**F438**



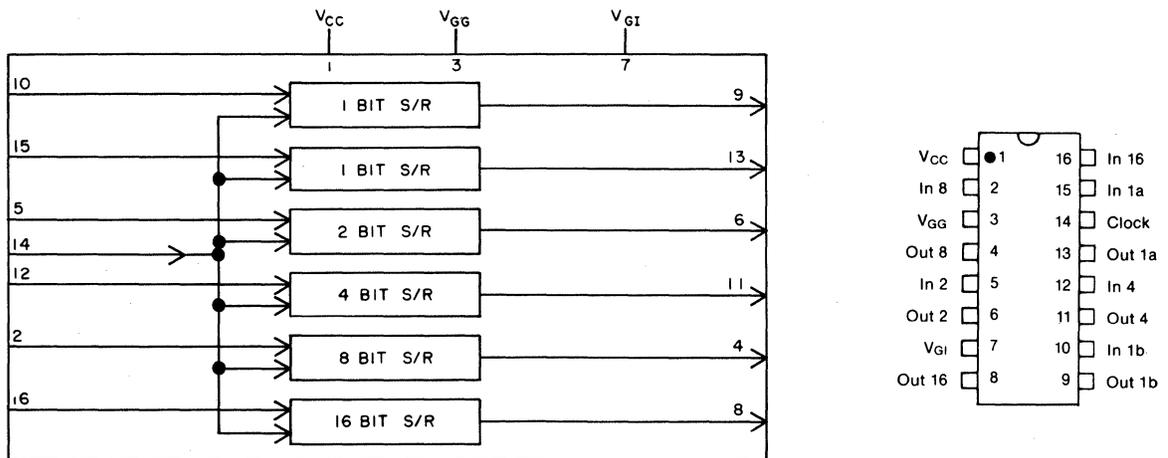
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

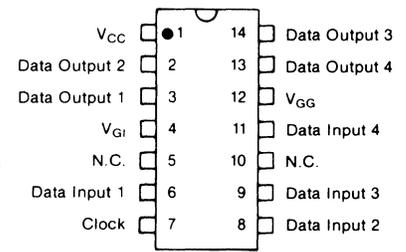
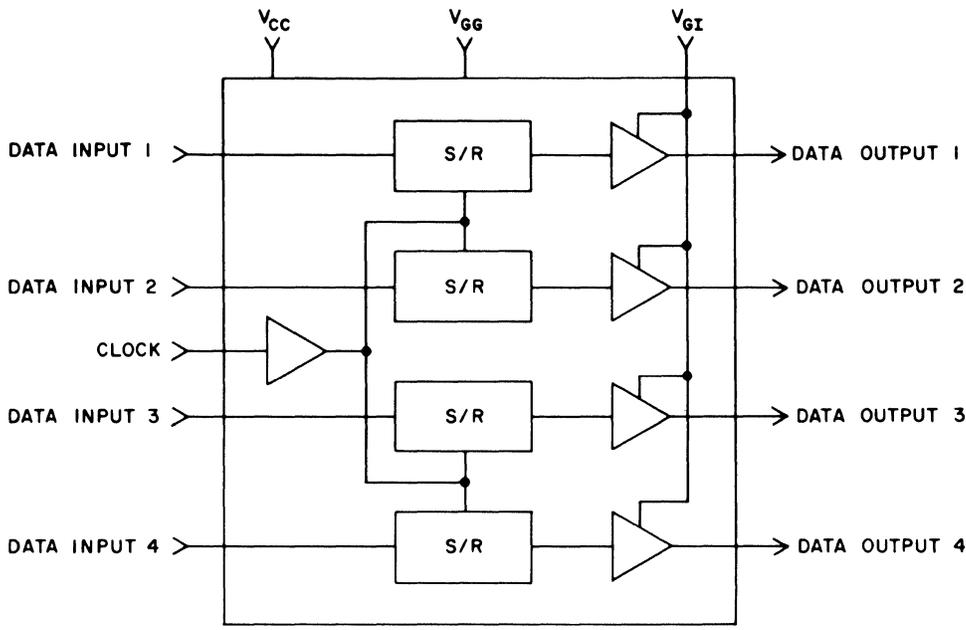
**F439**



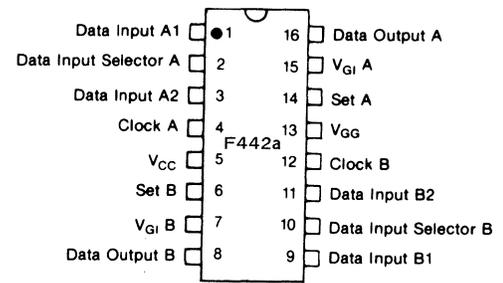
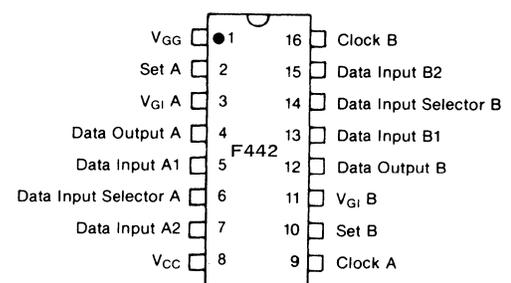
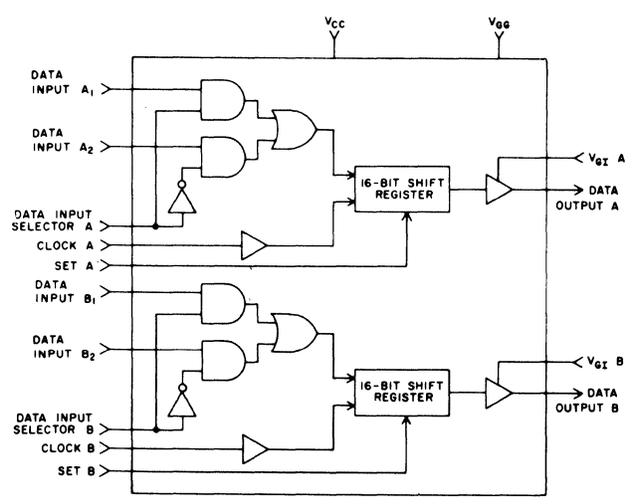
**F440**



**F441**



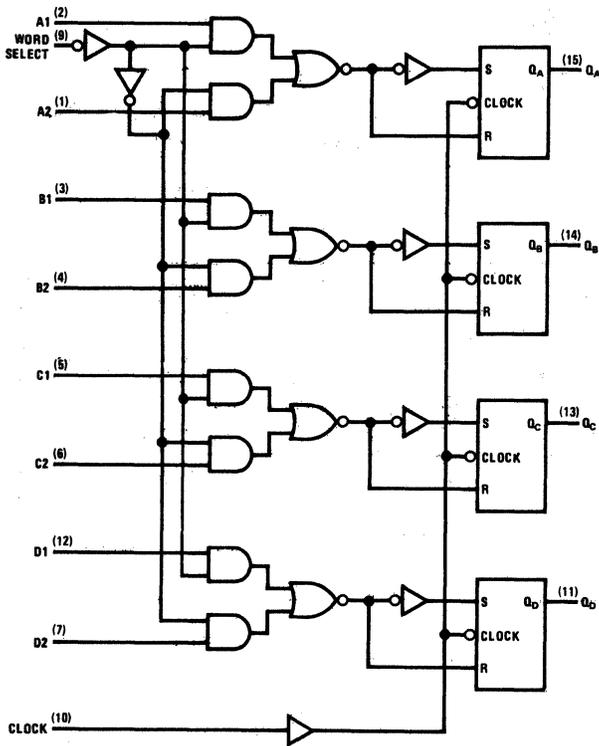
**F442**



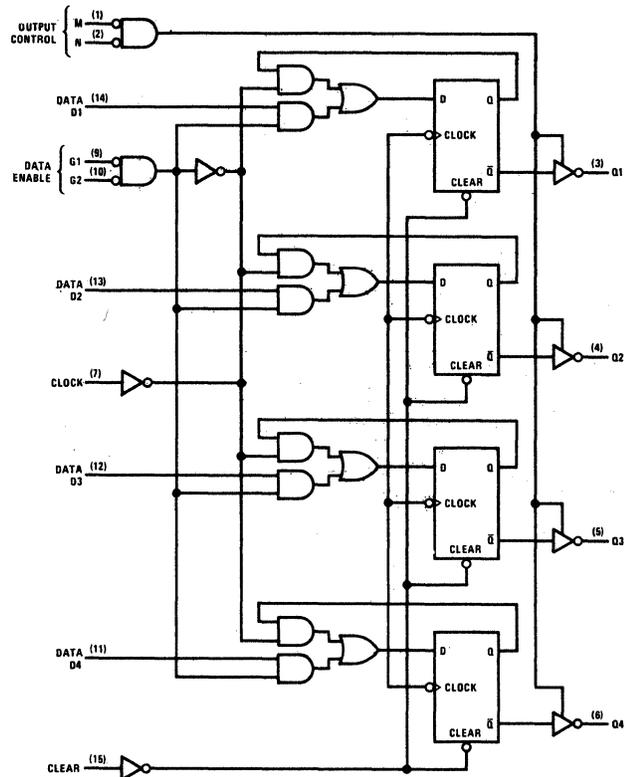
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F443**



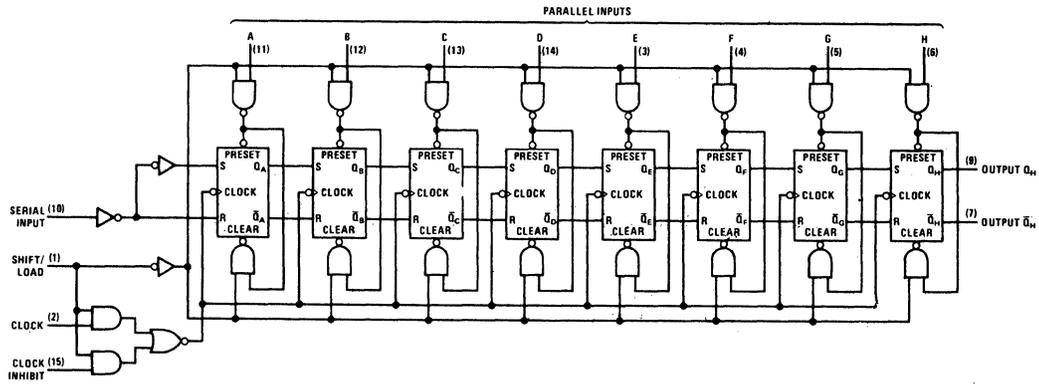
**F444**



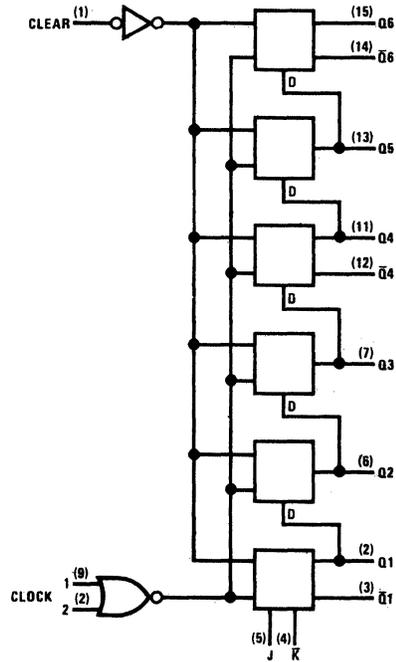
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**F445**



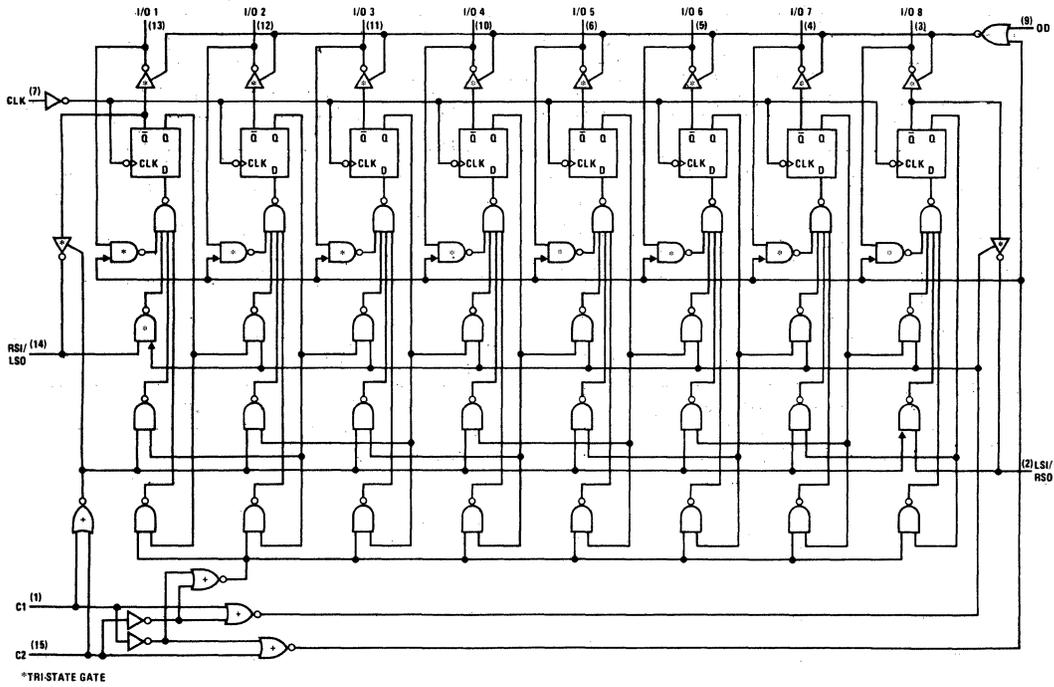
**F446**



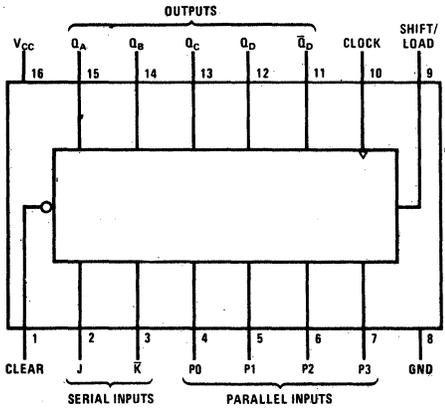
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

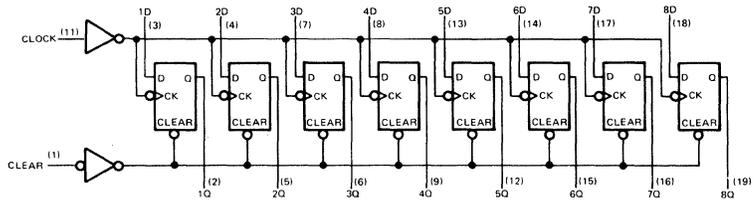
**F447**



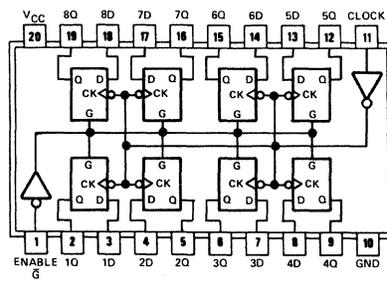
**F448**



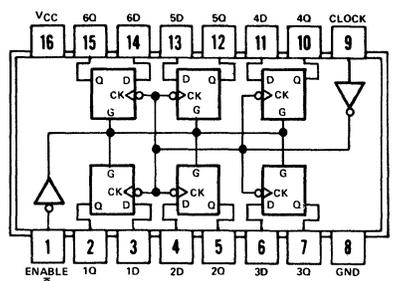
**F449**



**F450**



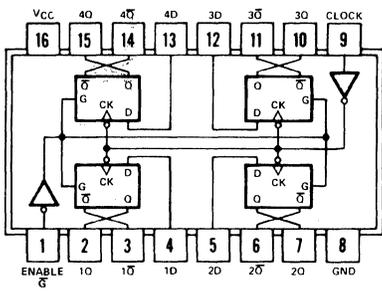
**F451**



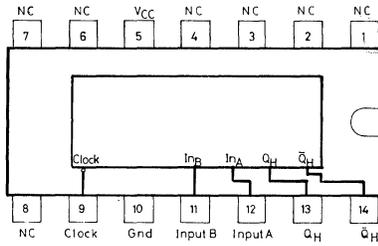
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

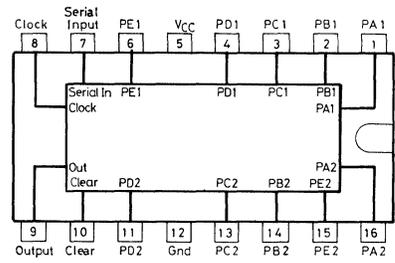
**F452**



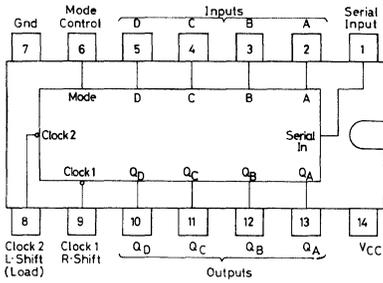
**F453**



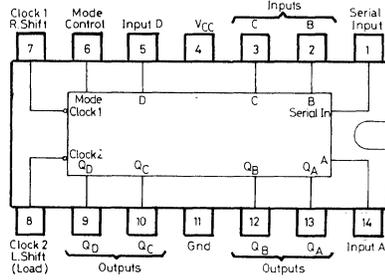
**F454**



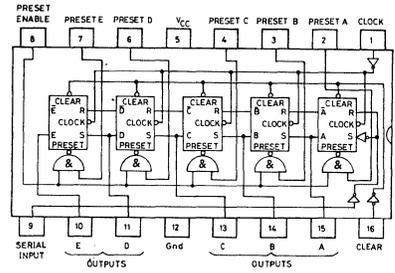
**F455**



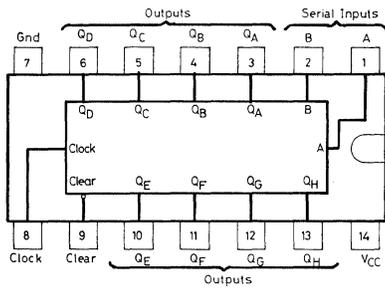
**F456**



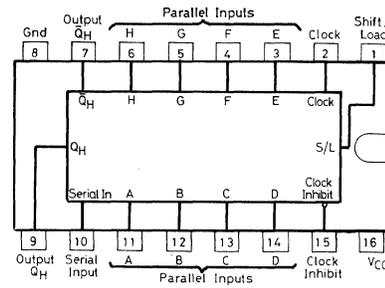
**F457**



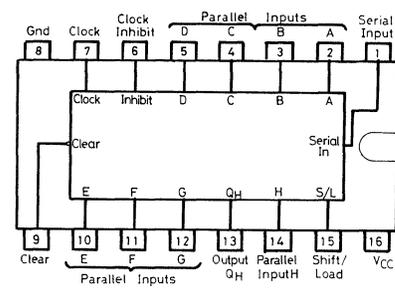
**F458**



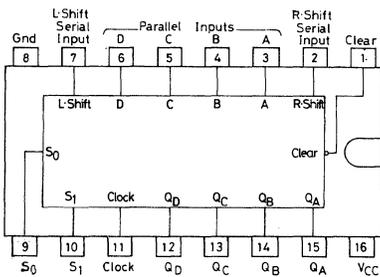
**F459**



**F460**



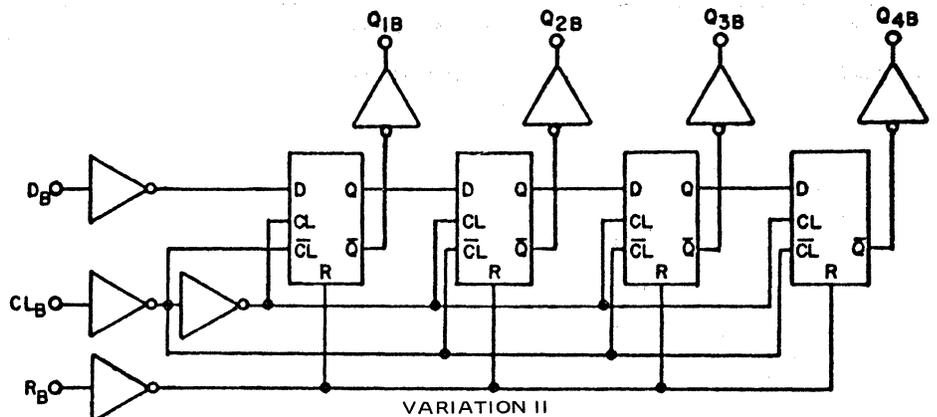
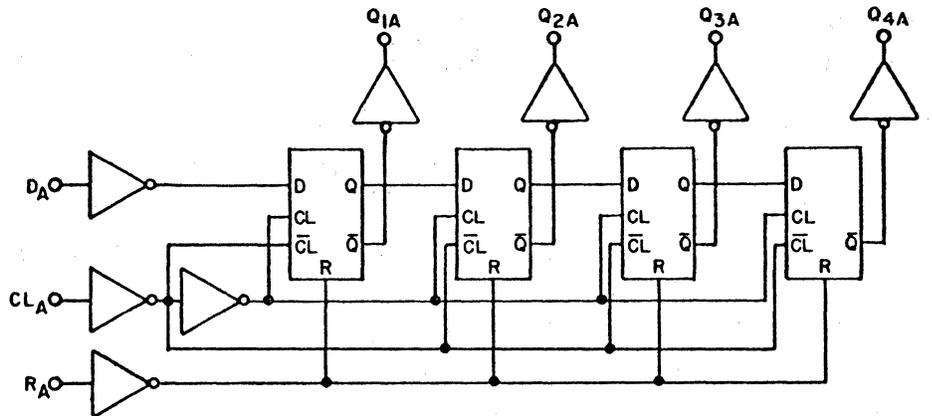
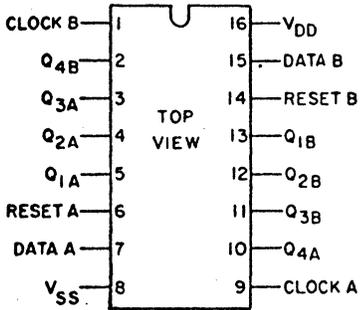
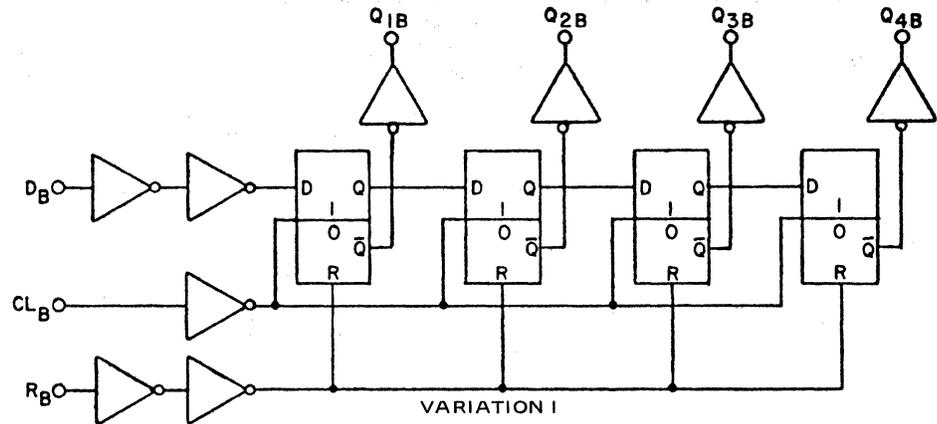
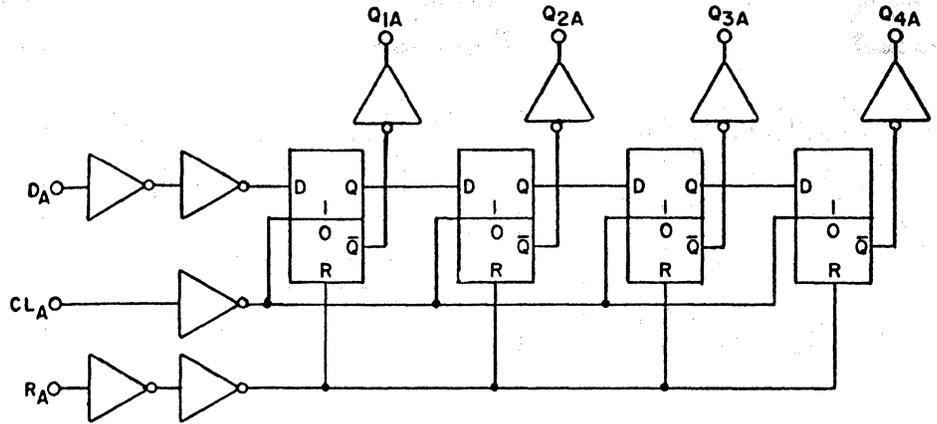
**F461**



# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

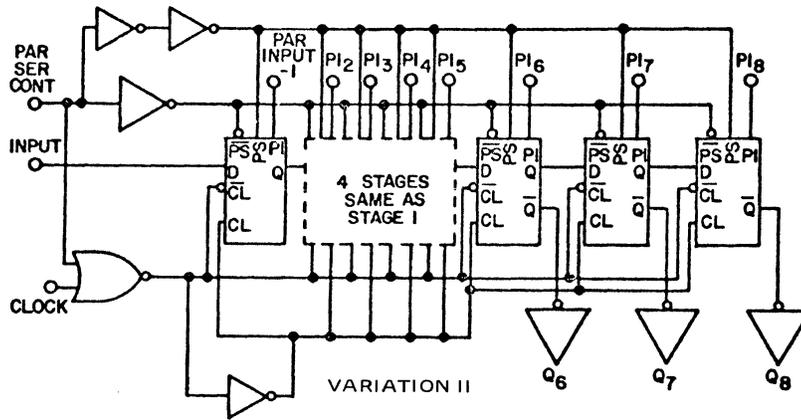
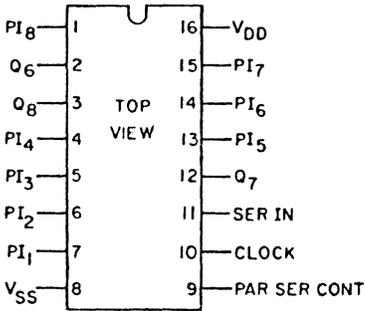
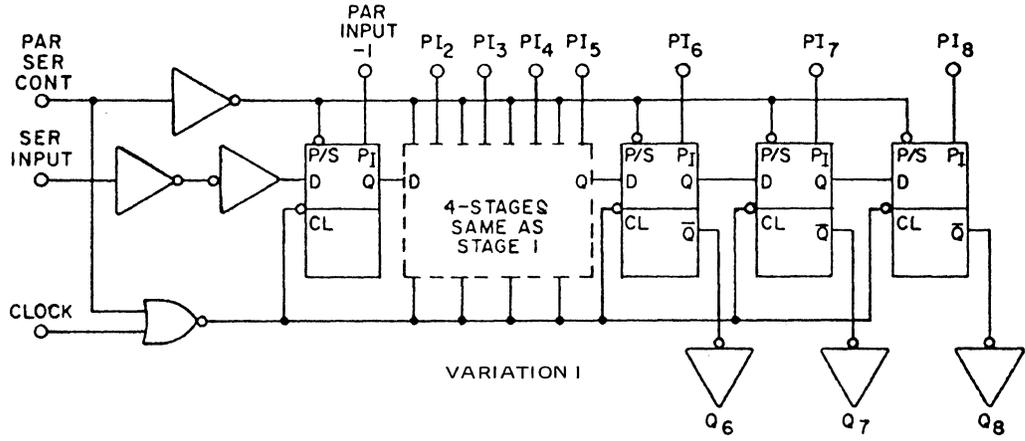
F462



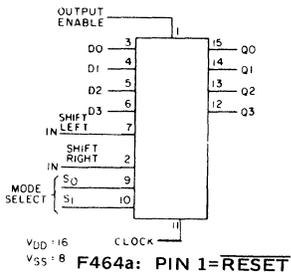
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

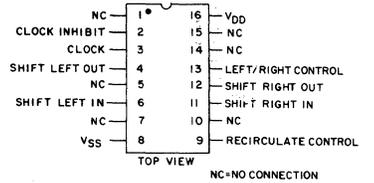
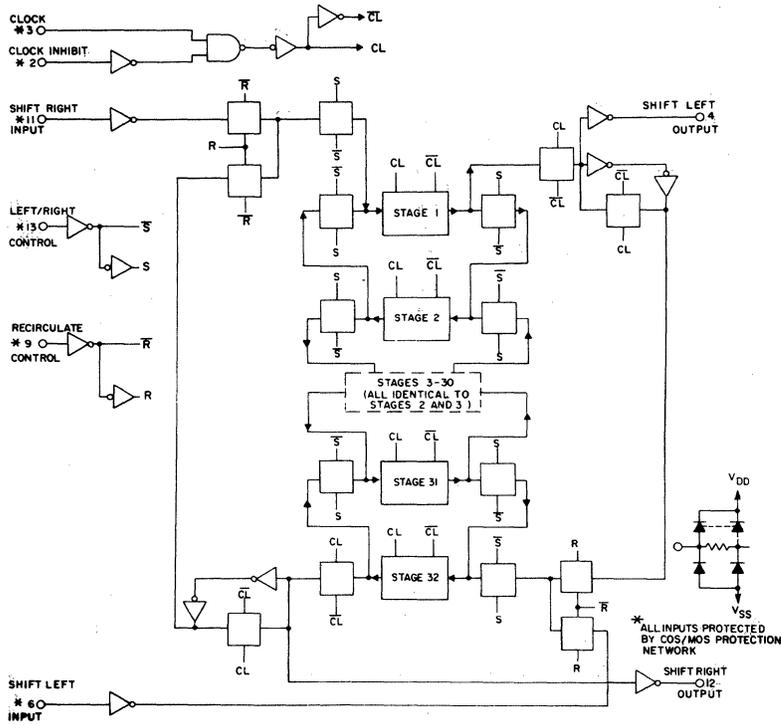
F463



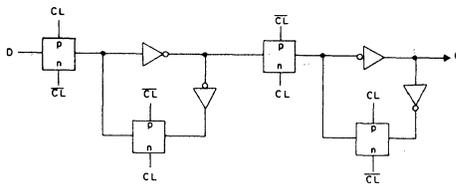
F464



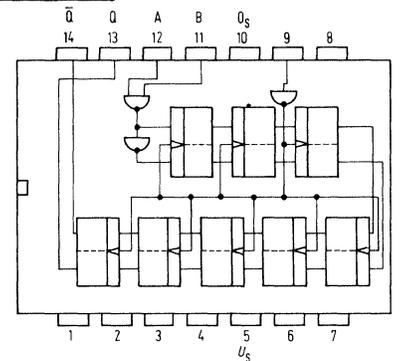
**F465**



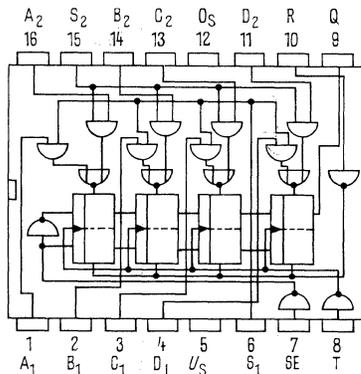
DETAIL OF TYPICAL D-TYPE M-S FLIP-FLOP



**F466**



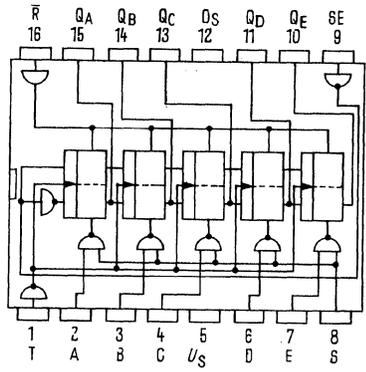
**F467**



# 22. LOGIC/BLOCK DRAWINGS

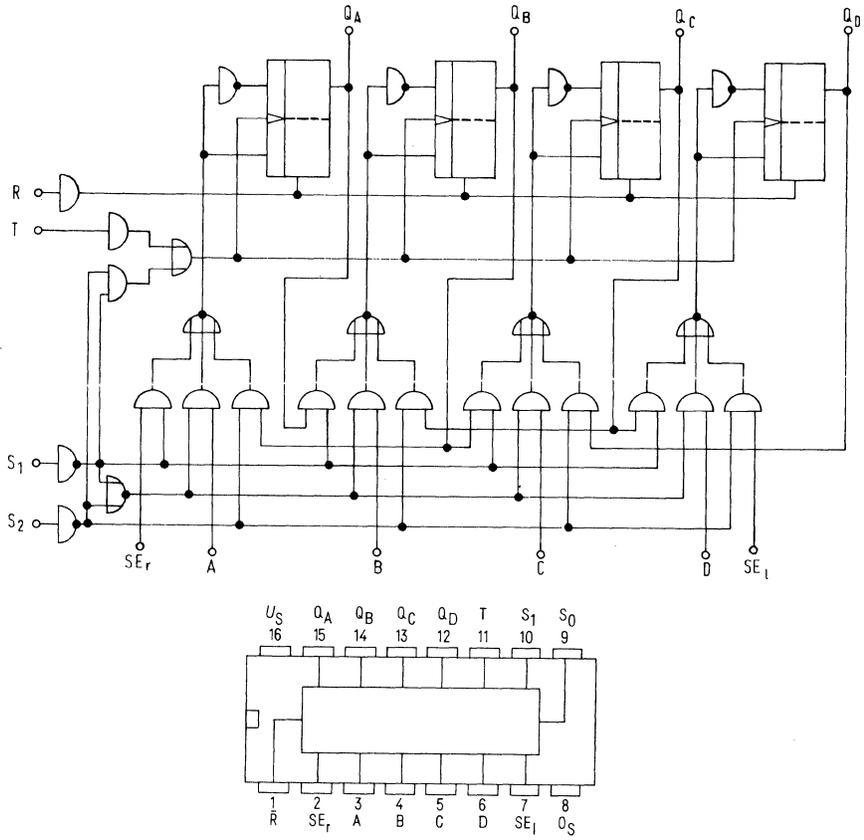
IN DRAWING NUMBER  
SEQUENCE

**F468**



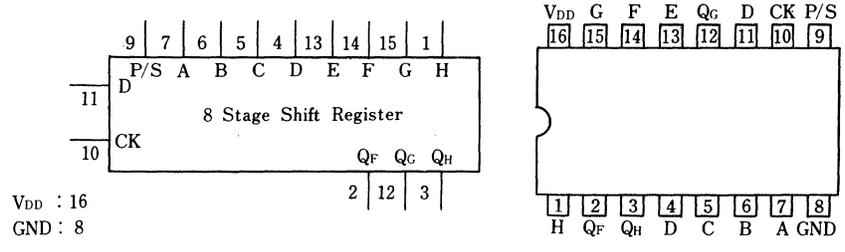
[Empty box]

**F469**

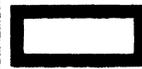
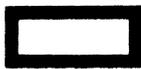
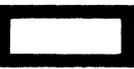
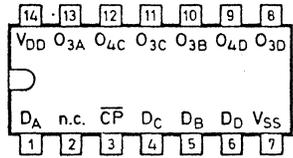
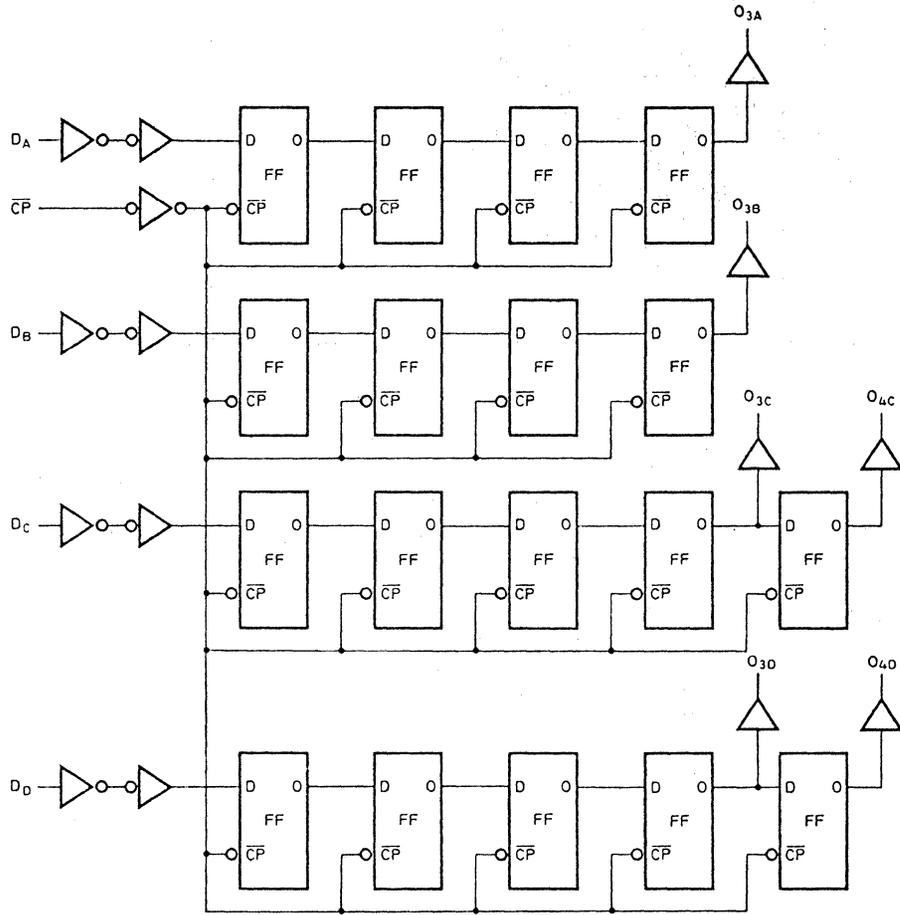


[Empty box]

**F470**



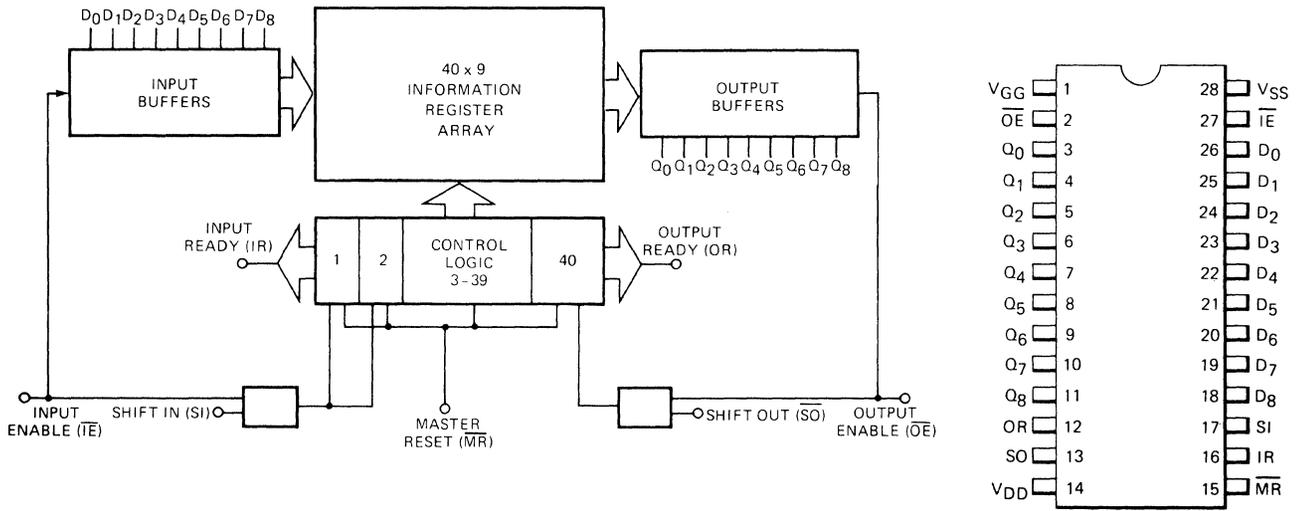
F471



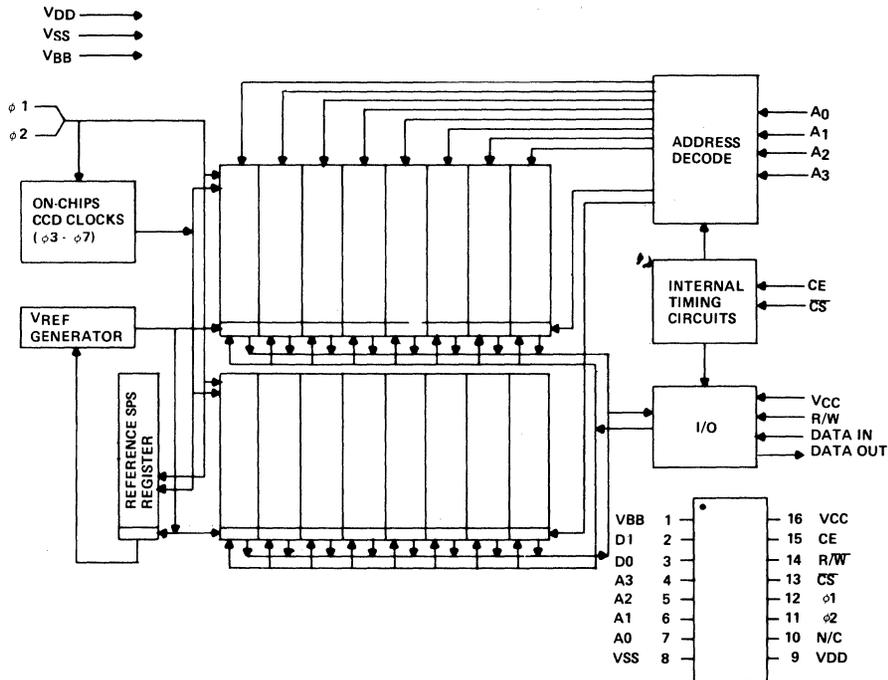
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**F472**



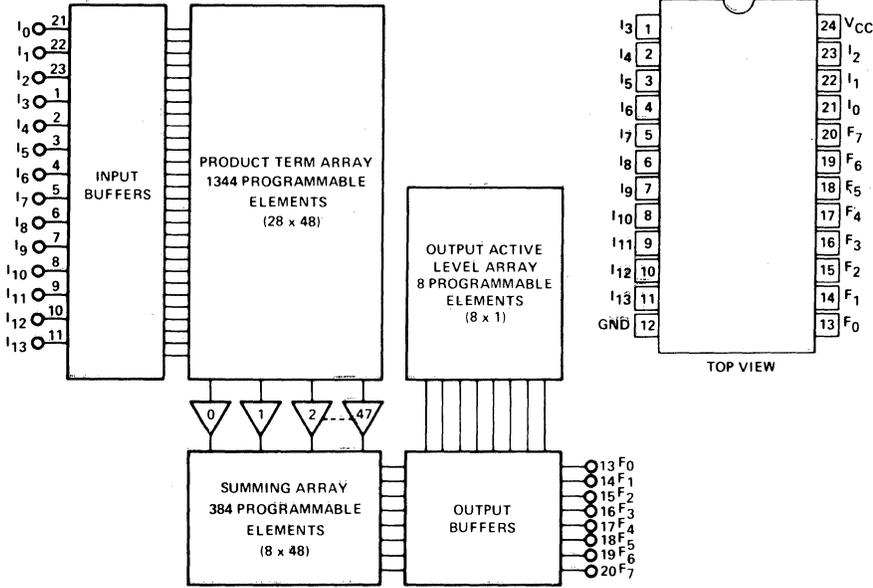
**F473**



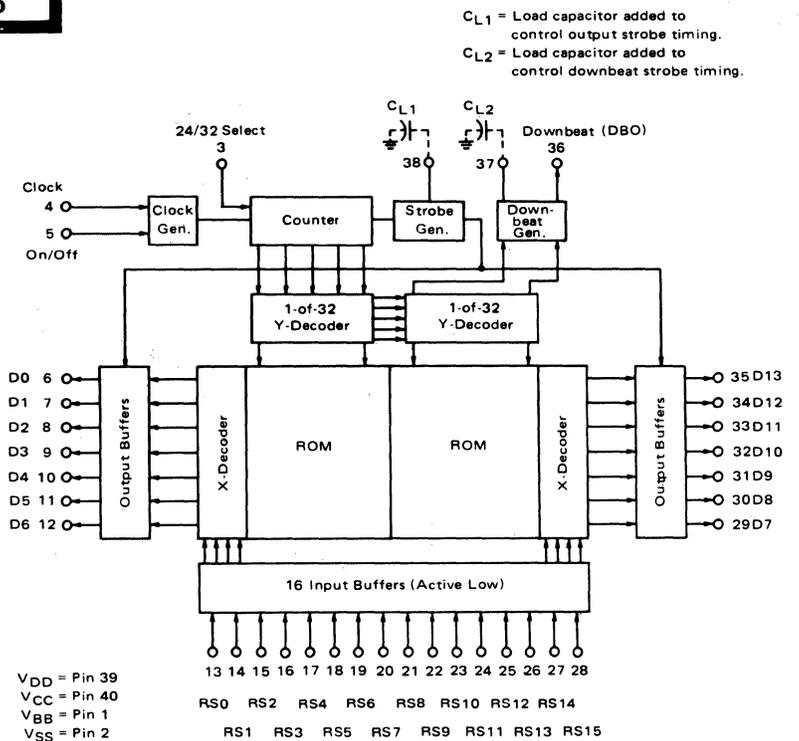
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

Z4

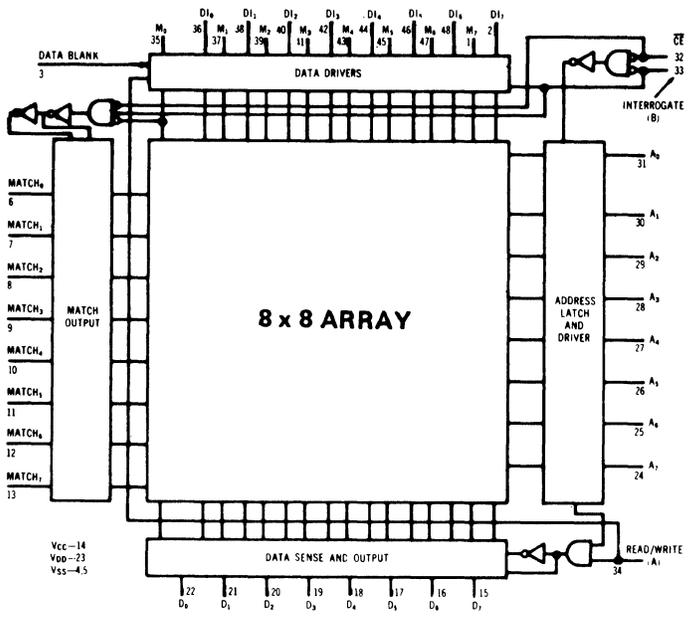


Z5

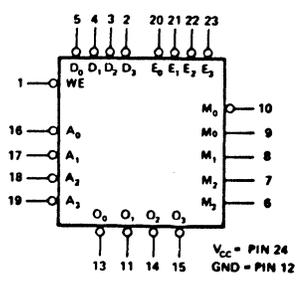


**22. LOGIC/BLOCK DRAWINGS** IN DRAWING NUMBER SEQUENCE

**Z12**

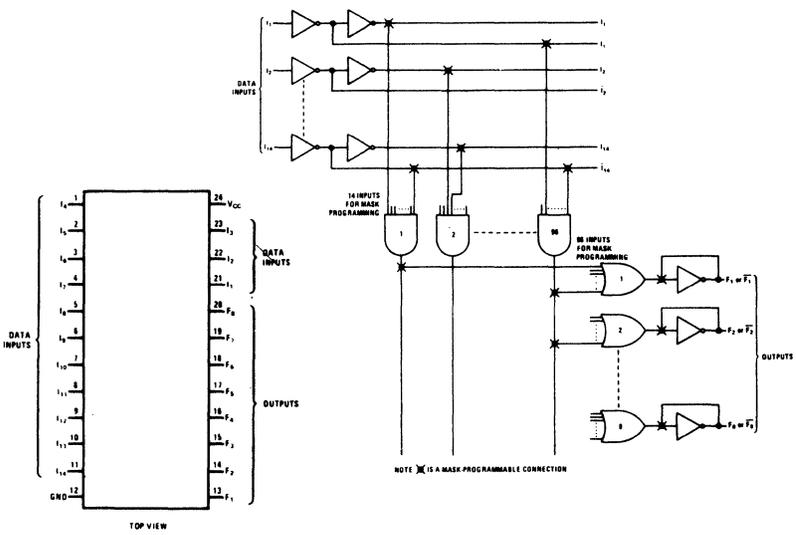


**Z13**



[Empty box]

**Z14**

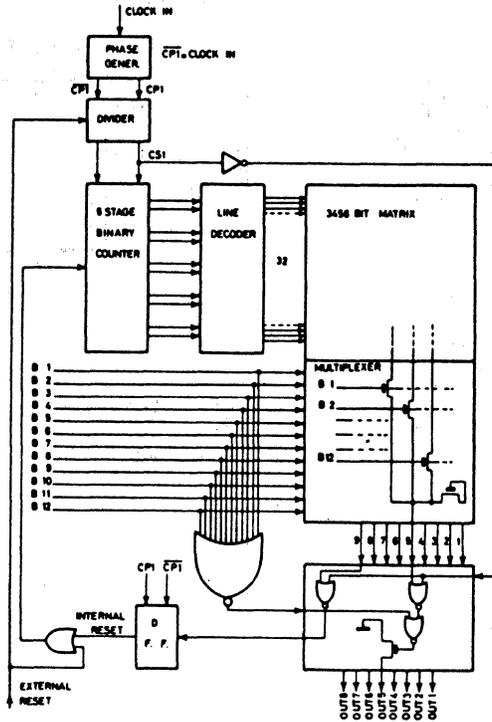


[Empty box]

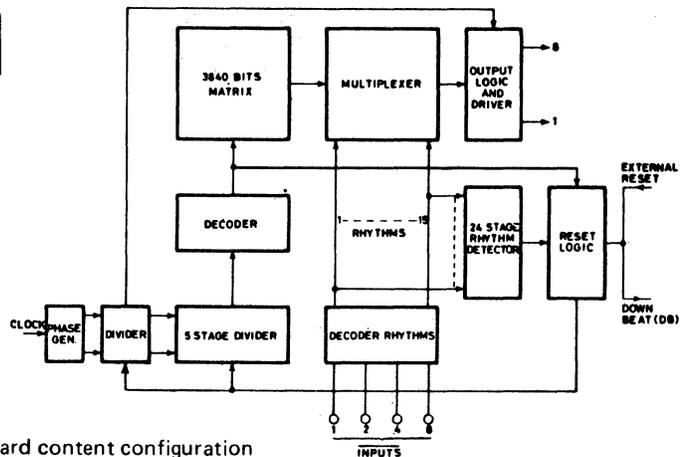
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

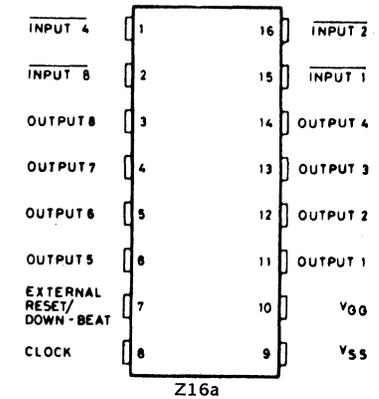
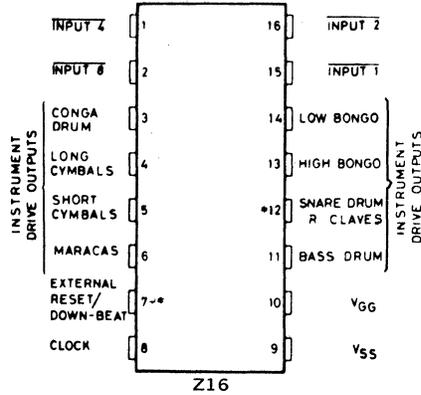
Z15



Z16



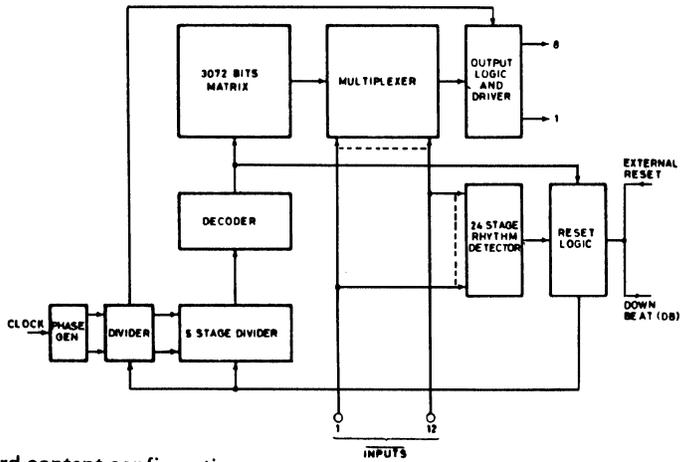
Standard content configuration



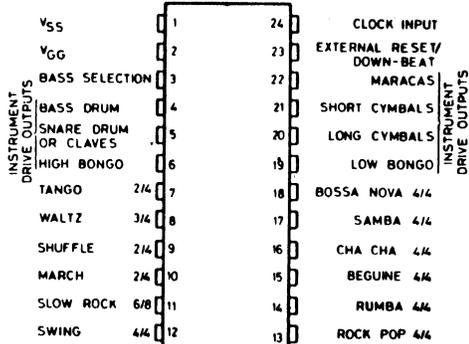
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

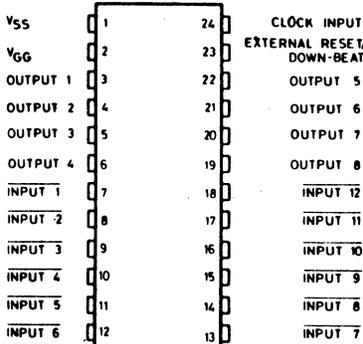
**Z17**



Standard content configuration

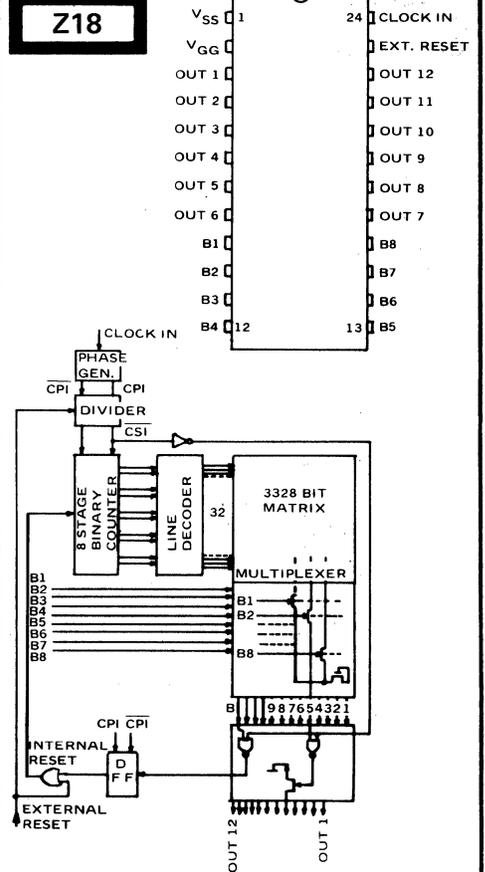


Z17

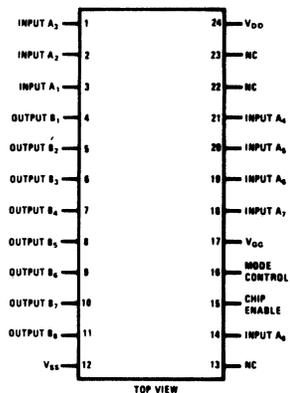
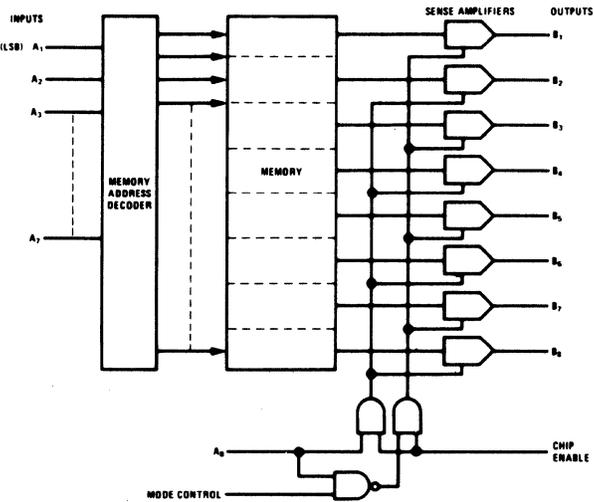


Z17a

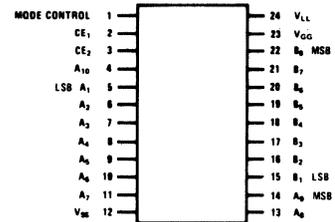
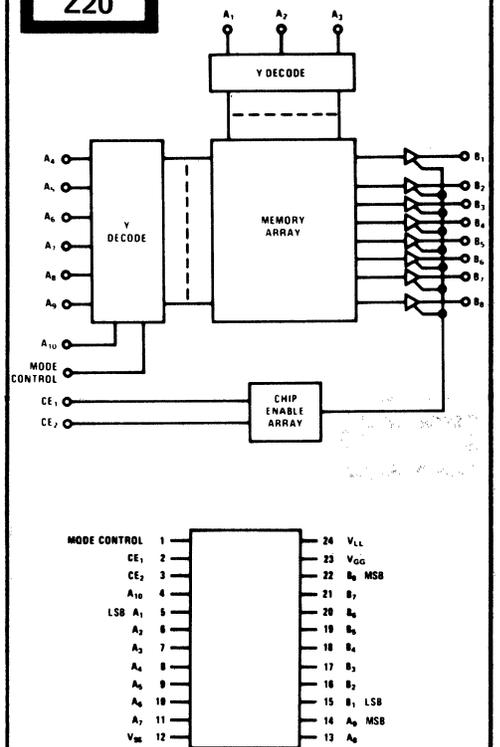
**Z18**



**Z19**



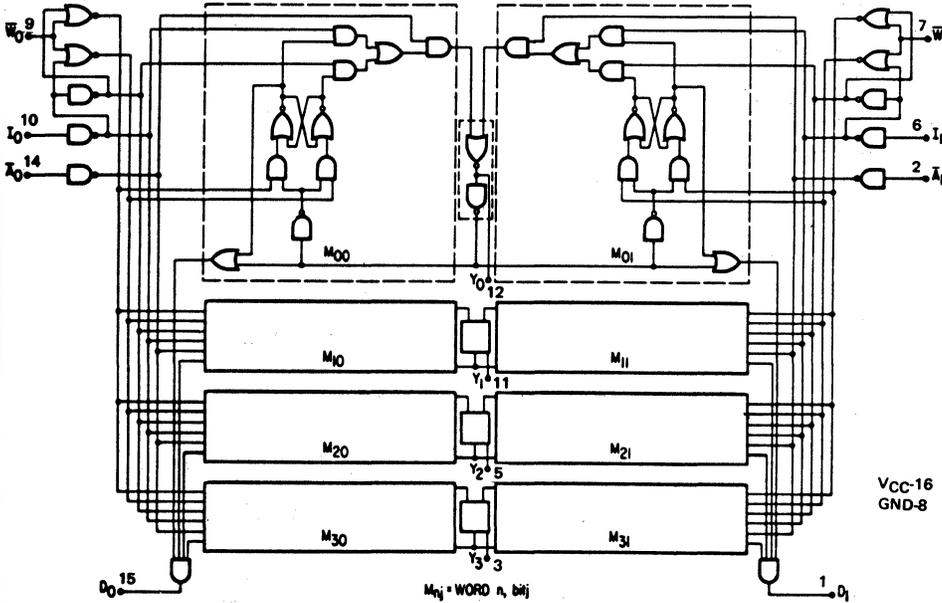
**Z20**



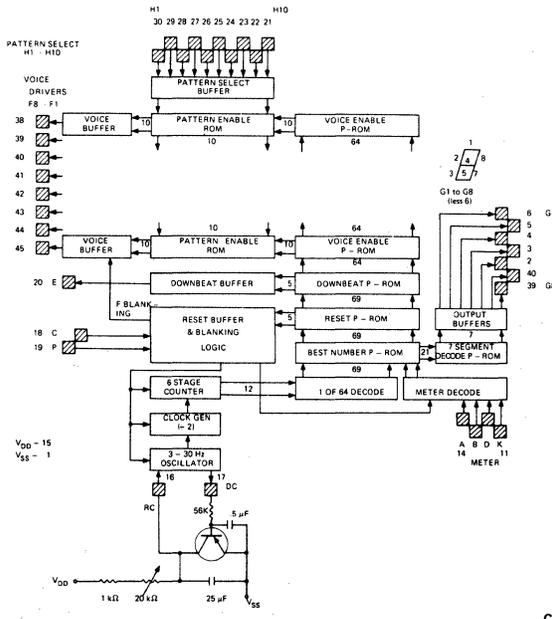
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

Z21



Z22

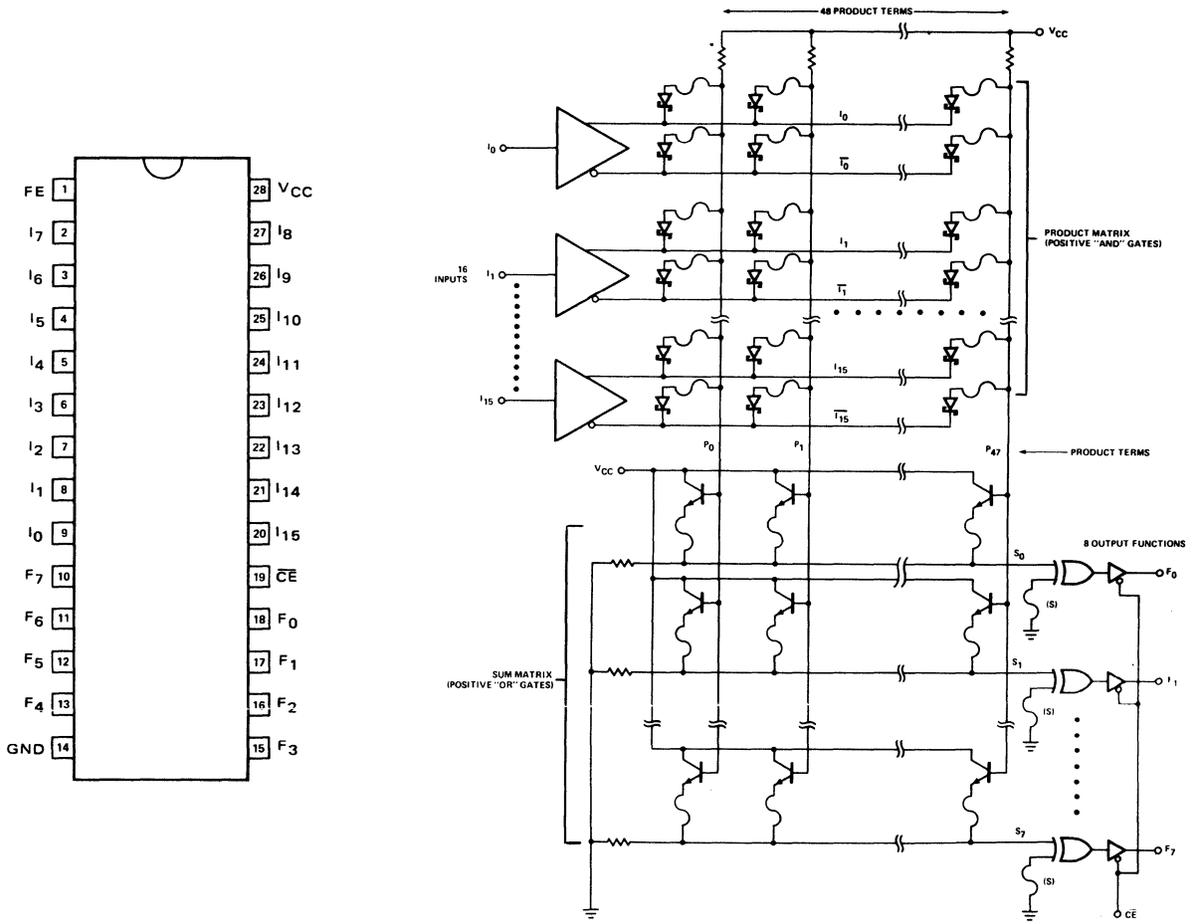


VSS	1	40	G7
G5	2	39	G8
G4	3	38	F1
G3	4	37	F2
G2	5	36	F3
G1	6	35	F4
	7	34	F5
	8	33	F6
	9	32	F7
K	10	31	F8
D	11	30	H1
B	12	29	H2
A	13	28	H3
VDD	14	27	H4
RC	15	26	H5
DUMP	16	25	H6
CHARGE	17	24	H7
	18	23	H8
	19	22	H9
E	20	21	H10

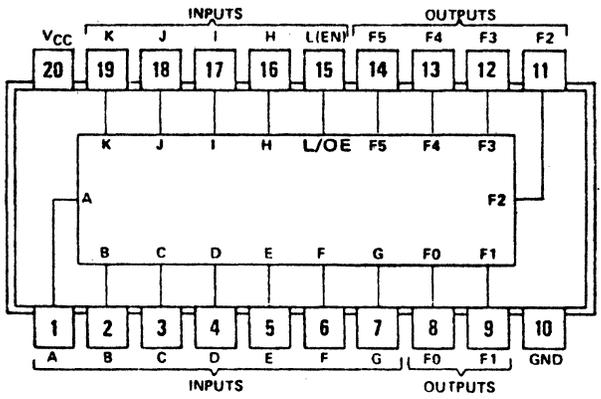
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

**Z24**



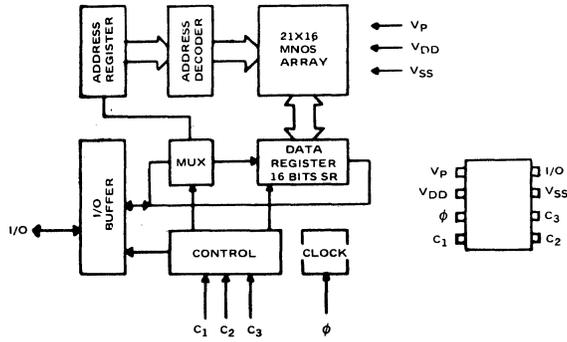
**Z25**



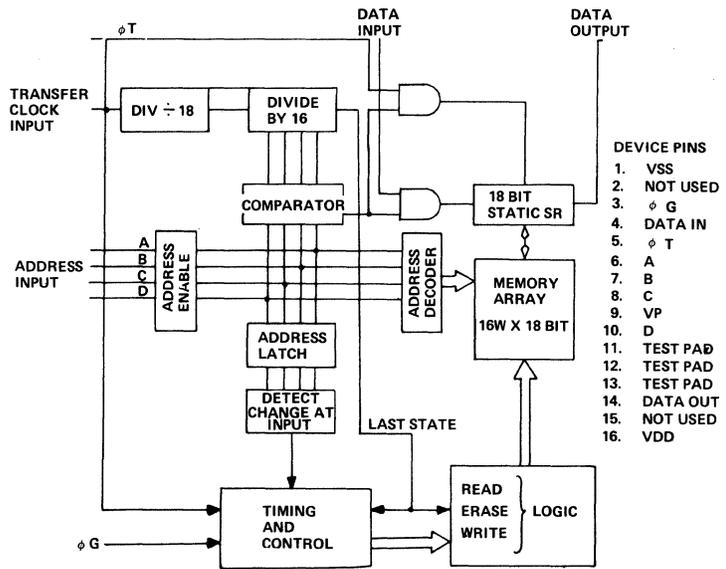
# 22. LOGIC/BLOCK DRAWINGS

IN DRAWING NUMBER SEQUENCE

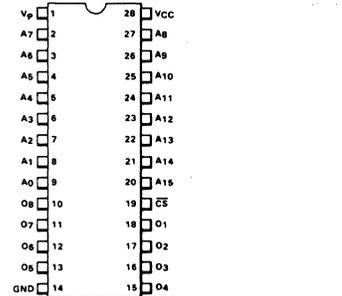
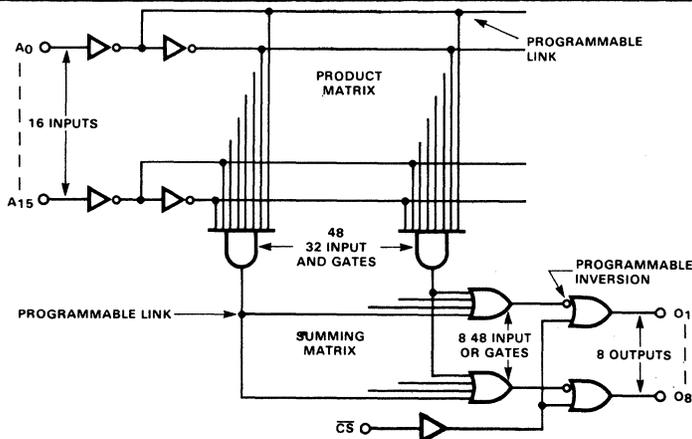
Z26



Z27



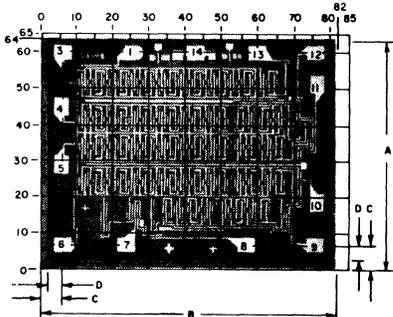
Z28



# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

CH1

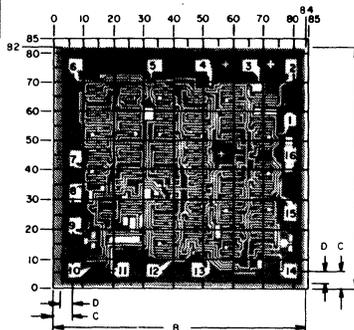


DIMENSIONS  
Grid Graduations Are In Mils ( $10^{-3}$  Inch)

	Mils	Millimeters
A	61 - 69	1.550 - 1.752
B	79 - 87	2.007 - 2.209
C	4 - 10	0.102 - 0.254
D	3.3 - 4.3	0.084 - 0.109
CHIP THICKNESS:		
	5 - 9	0.127 - 0.228

Millimeter dimensions are derived from the basic inch dimensions as indicated.

CH2

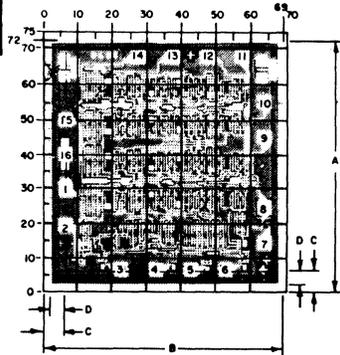


DIMENSIONS  
Grid Graduations Are In Mils ( $10^{-3}$  Inch)

	Mils	Millimeters
A	79 - 87	2.007 - 2.209
B	81 - 89	2.058 - 2.260
C	4 - 10	0.102 - 0.254
D	3.3 - 4.3	0.084 - 0.109
CHIP THICKNESS:		
	5 - 9	0.127 - 0.228

Millimeter dimensions are derived from the basic inch dimensions as indicated.

CH3

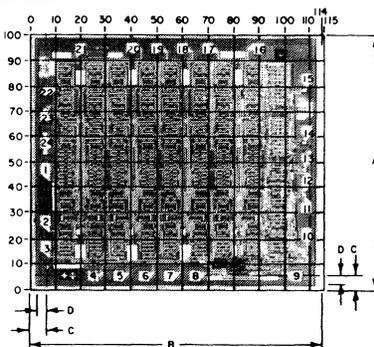


DIMENSIONS  
Grid Graduations Are In Mils ( $10^{-3}$  Inch)

	MILS	MILLIMETERS
A	69 - 77	1.753 - 1.955
B	66 - 74	1.677 - 1.879
C	4 - 10	0.102 - 0.254
D	3.3 - 4.3	0.084 - 0.109
CHIP THICKNESS:		
	5 - 9	0.127 - 0.228

Millimeter dimensions are derived from the basic inch dimensions as indicated.

CH4



DIMENSIONS  
Grid Graduations Are In Mils ( $10^{-3}$  Inch)

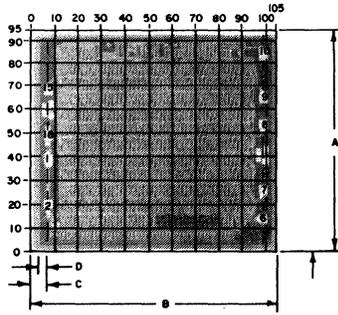
	Mils	Millimeters
A	97 - 105	2.464 - 2.667
B	111 - 119	2.820 - 3.022
C	4 - 10	0.102 - 0.254
D	3.3 - 4.3	0.084 - 0.109
CHIP THICKNESS:		
	5 - 9	0.127 - 0.228

Millimeter dimensions are derived from the basic inch dimensions as indicated.

# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**CH5**



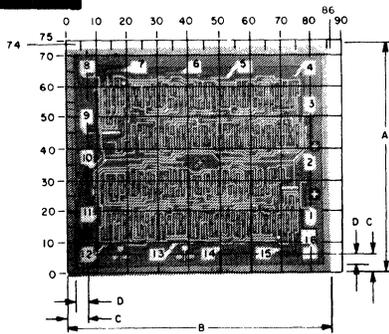
**DIMENSIONS**

Grid Graduations Are In Mils ( $10^{-3}$  Inch)

	Mils	Millimeters
A	92 - 100	2.337 - 2.540
B	102 - 110	2.591 - 2.794
C	4 - 10	0.102 - 0.254
D	3.3 - 4.3	0.084 - 0.109
CHIP THICKNESS:		
	5 - 9	0.127 - 0.228

Millimeter dimensions are derived from the basic inch dimensions as indicated.

**CH6**



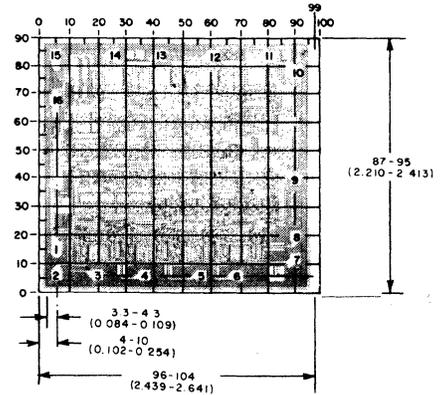
**DIMENSIONS**

Grid Graduations Are In Mils ( $10^{-3}$  Inch)

	Mils	Millimeters
A	71 - 79	1.804 - 2.006
B	83 - 91	1.194 - 1.397
C	4 - 10	0.102 - 0.254
D	3.3 - 4.3	0.084 - 0.109
CHIP THICKNESS:		
	5 - 9	0.127 - 0.228

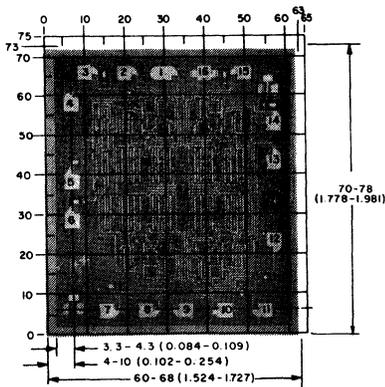
Millimeter dimensions are derived from the basic inch dimensions as indicated.

**CH7**



Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid graduations are in mils ( $10^{-3}$  inch).

**CH8**

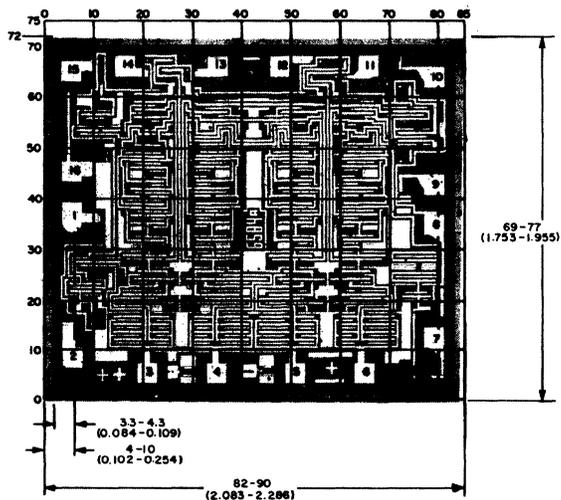


Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid Graduations are in Mils ( $10^{-3}$  inch).

# 23. OUTLINE DRAWINGS

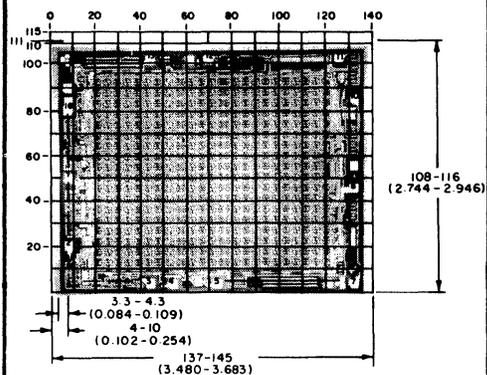
IN DRAWING NUMBER  
SEQUENCE

CH9



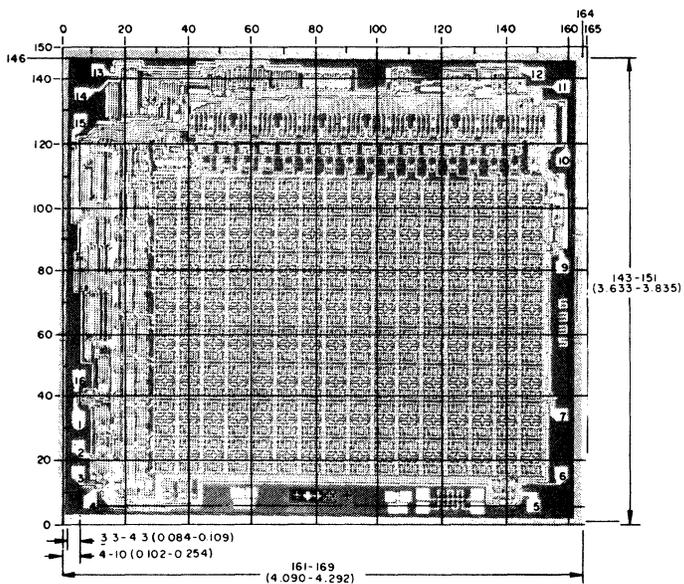
Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid graduations are in mils ( $10^{-3}$  inch).

CH10



Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid graduations are in mils ( $10^{-3}$  inch).

CH11

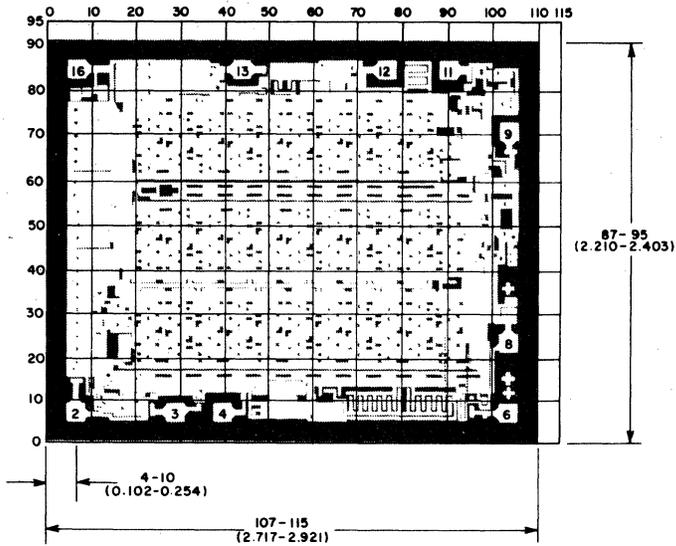


Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid Graduations are in Mils ( $10^{-3}$  inch).

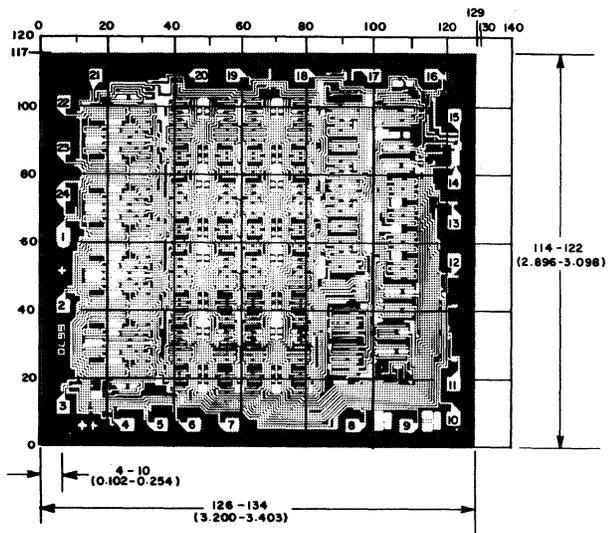
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

CH12



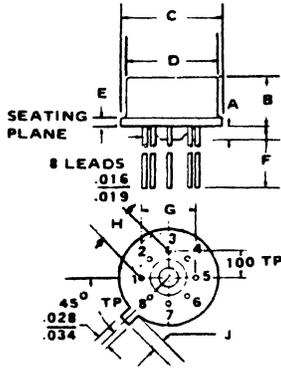
CH13



# 23. OUTLINE DRAWINGS

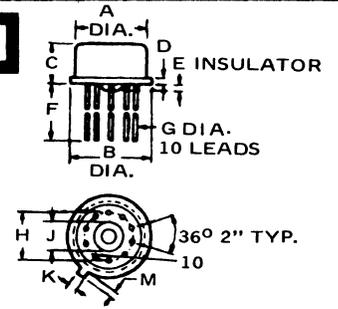
IN DRAWING NUMBER  
SEQUENCE

**CY4**



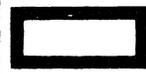
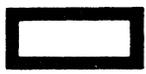
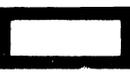
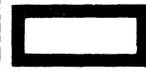
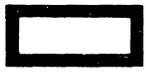
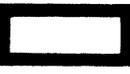
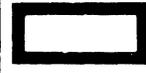
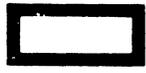
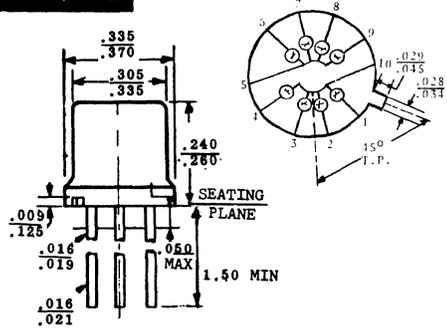
	A	B	C	D	E	F	G	H	J
CY4	.040	.165	.335	.305	.040	.500	.200		.029
	MAX	.185	.370	.335	MAX	MIN	T.P.		.045
CY4a	.050	.165	.335	.305	.040	.500	.200		.029
	MAX	.185	.370	.335	MAX	MIN	T.P.		.045
CY4b	.050	.240	.335	.305	.040	.500	.200		.029
	MAX	.260	.370	.335	MAX	MIN	T.P.		.045
CY4c	.015	.165	.355	.315	.020	.500	.190	.120	.029
	.045	.185	.370	.325	.030	.562	.210	.160	.040
CY4d	.015	.165	.355	.315	.020	.500	.220	.120	.029
	.045	.185	.370	.325	.030	.562	.230	.160	.040
CY4e	.015	.240	.355	.325	.030	.500	.230		.029
	.050	.260	.362	MAX	MIN				.045
CY4f	.040	.165	.335	.305	.040	.500	.230		.029
	MAX	.185	.370	.335	MAX	MIN			.045

**CY7**



	A	B	C	D	E	F	G	H	J	K	M
CY7	.315	.365	.160	.020	.015	.750	.016	.220	.120	.028	.029
	.325	.370	.185	.030	.045	.810	.019	.240	.160	.034	.040
CY7a	.305	.335	.165	.040	.050	.500	.016	.230		.028	.029
	.335	.370	.185	MAX	MAX	MIN	.019			.034	.045
CY7b	.305	.335	.240	.040	.010	.500	.016	.230	.160	.028	.029
	.335	.370	.260	MAX	.040	MIN	.019	TP	MAX	.034	.045
CY7c	.315	.355	.165	.020	.015	.500	.016	.220	.120	.028	.029
	.325	.370	.185	.030	.045	.562		.240	.160	.034	.040

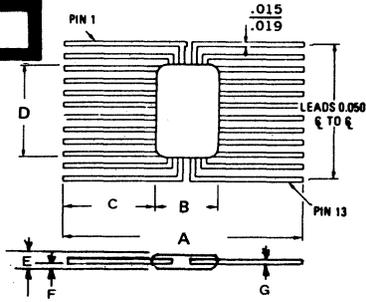
**CY8**



# 23. OUTLINE DRAWINGS

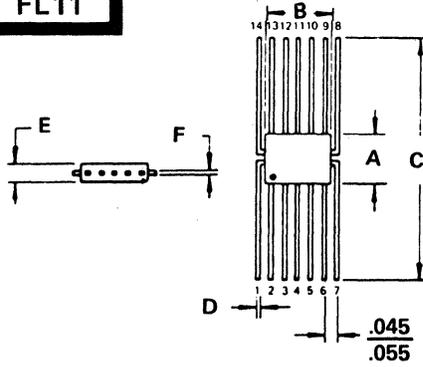
IN DRAWING NUMBER  
SEQUENCE

**FL3**



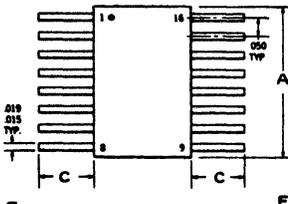
	A	B	C	D	E	F	G
FL3	.973	.285	.355	.408	.043		
	MAX	MAX	MIN	MAX	.063		
FL3a	.990	.270	.360	.390	.034	.016	.003
	MAX	MAX	MAX	MAX	.044	.023	.004
FL3b	.940	.245	.330	.370	.040	.011	.003
	.960	.275	.370	.395	.070	.039	.004
FL3c	.855	.355	.250	.355	.060	.020	.004
	1.055	.385	.335	.385	.078	.030	.006

**FL11**



	A	B	C	D	E	F
FL11	.165	.240	.865	.014	.065	.003
	.195	.270	.895	.017	MAX	.005
FL11a	.160	.240	.760	.010	.040	.003
	.190	.275	MIN	.019	.070	.006
FL11b	.230	.230	.900	.014	.030	.003
	.260	.260	.980	.019	.070	.006
FL11c	.240	.240	.740	.015	.055	.004
	.260	.260	1.00	.019	.080	.006

**FL14**

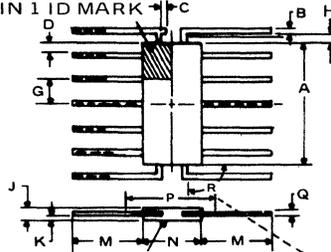


**NOTES:**  
All dimensions in inches  
Leads are gold-plated Kovar  
Package weight is 0.4 gram

	A	B	C	D	E	F
FL14	.370	.245	.250	.038	.004	.060
	.410	.280	.350		.006	.085
FL14a	.370	.247	.250		.003	.053
	.400	.270	.370		.006	.065
FL14b	.390	.270	.260		.004	.045
	MAX	MAX	MIN			.060
FL14c	.360	.240	.070			.030
	.410	.275	MIN			.070
FL14d	.370	.245	.330	.011	.003	.040
	.395	.275	.370	.039	.004	.070
FL14e	.385	.270	.360		.003	.050
	MAX	MAX	TYP		.005	.060
FL14f	.395	.270	.330	.025	.003	.070
	.425		MIN		.007	MAX
FL14g	.371	.247	.250	.024	.004	.049
	.409	.283	MIN	TYP	.006	.090
FL14h	.371	.247	.250	.024	.004	.055
	.400	.285	.350	TYP	.006	.080

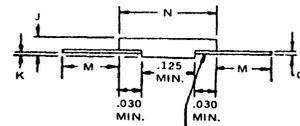
**FL21**

INDEX AREA,  
NOTCH OR  
PIN 1 ID MARK



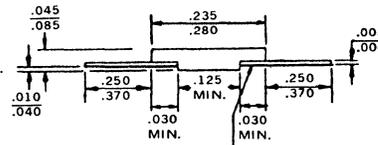
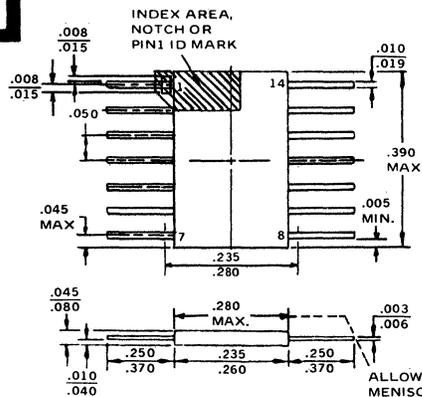
SEATING PLANE  
ALLOWS FOR OFF-CENTER LID,  
MENISCUS AND GLASS OVERRUN

	A	B	C	D	G	H	J	K	M	N	P	Q	R
FL21	.280	.010	.008	.005	.050	.004	.030	.010	.165	.120	.220	.003	30°
	MAX	.019	.015	MIN		MIN	.070	.040	.390	.200	MAX	.006	30°
FL21a	.280	.010	.008	.005	.050	.004	.030	.010	.250	.240	.280	.003	30°
	MAX	.019	.015	MIN		MIN	.085	.040	.370	.260	MAX	.006	30°



OPTIONAL CONFIGURATION  
FOR F21 ONLY - IF IT IS  
USED, NO ORGANIC OR  
POLYMERIC MATERIAL SHALL  
BE MOLDED TO THE BOTTOM  
OF THE PACKAGE TO COVER  
THE LEADS.

**FL22**



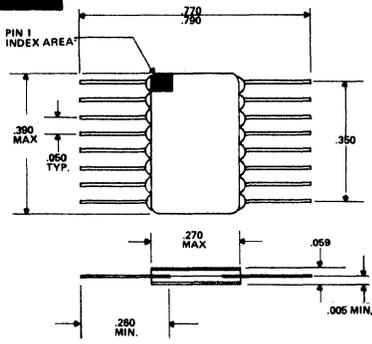
OPTIONAL CONFIGURATION -  
IF IT IS USED, NO ORGANIC OR  
POLYMERIC MATERIALS SHALL BE  
MOLDED TO THE BOTTOM OF  
THE PACKAGE TO COVER THE  
LEADS.

ALLOWS FOR OFF-CENTER LID,  
MENISCUS AND GLASS OVERRUN

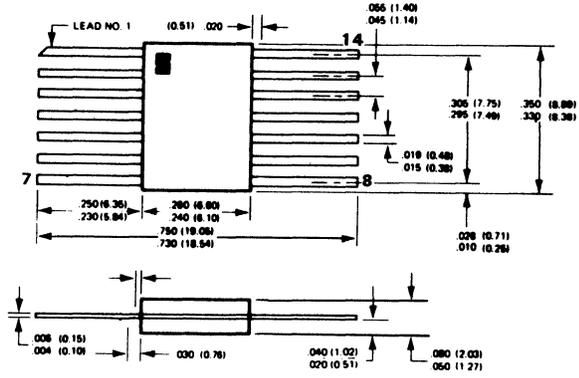
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

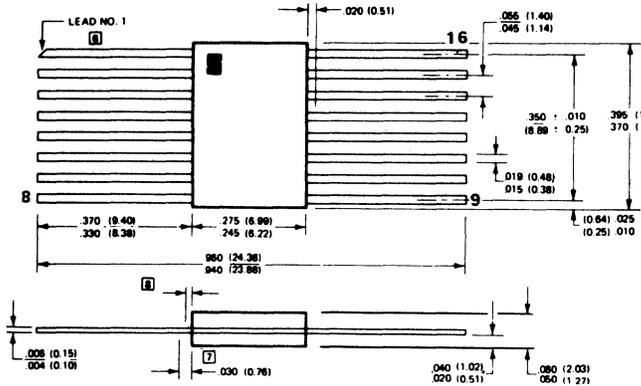
**FL23**



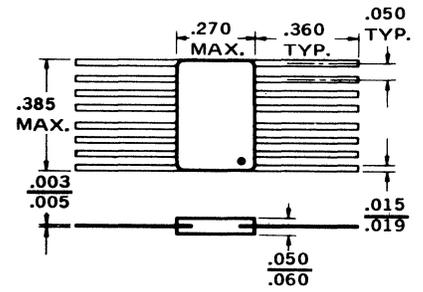
**FL24**



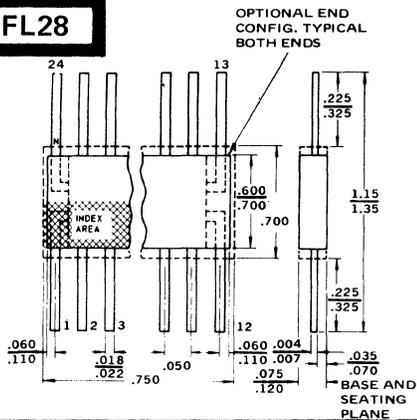
**FL25**



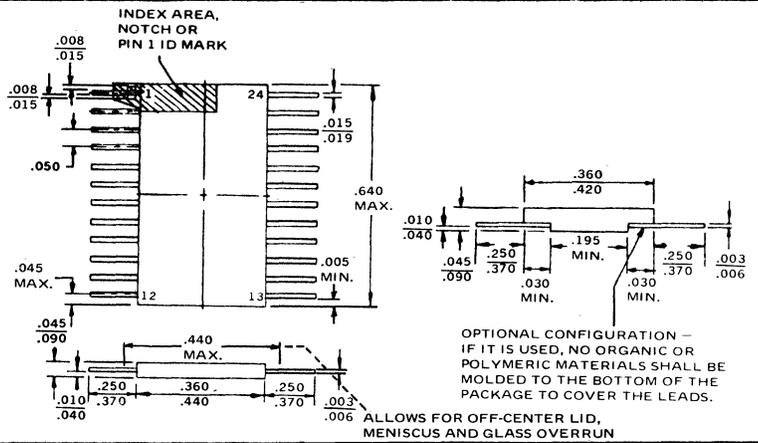
**FL27**



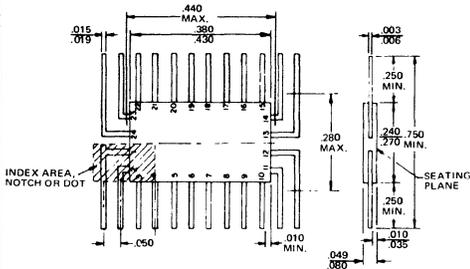
**FL28**



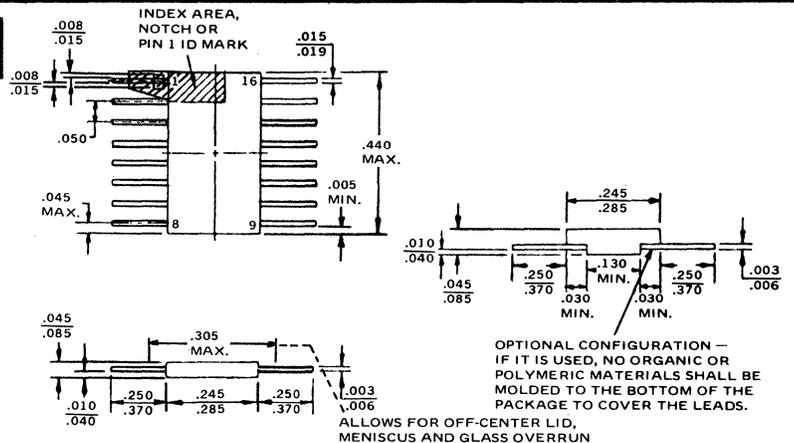
**FL29**



**FL30**



**FL31**



# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

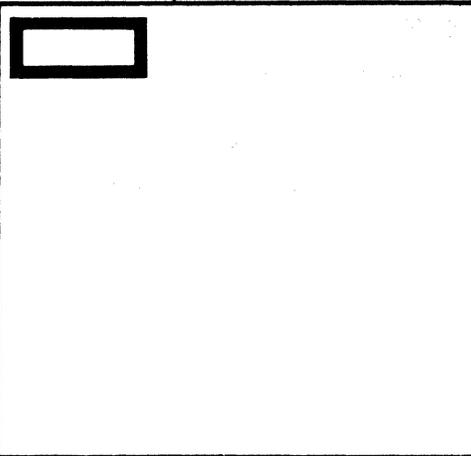
### FL33

	A	B	C	D
FL33	.240	.360	.070	.930
	.280	.410	MAX	.980
FL33a	.390	.390	.045	.930
	MAX	MAX	.065	.980
FL33b	.245	.370	.030	.900
	.280	.410	.070	.980

### FL34

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.40	10.18	0.370	0.400
B	0.22	8.80	0.245	0.280
C	1.50	2.00	0.060	0.080
D	0.30	0.40	0.015	0.019
E	0.08	0.15	0.003	0.006
F	1.27	85C	0.05	85C
G	0.60	0.60	0.025	0.030
H	0.25	3.40	0.200	0.370
I	18.80	-	0.745	-
J	-	0.51	-	0.020
K	-	0.38	-	0.015

NOTES  
1 LEAD NO. 1 IDENTIFIED BY TAB ON LEAD OR DOT ON COVER  
2 LEADS WITHIN 0.13 mm (0.005") TOTAL OF TRUE POSITION AT MAXIMUM MATERIAL CONDITION.



### FL35

INDEX AREA: A DOT OF CONTRASTING COLOR SHALL BE LOCATED ADJACENT TO AND INDICATING PIN 1 AND SHALL BE WITHIN THE SHADED AREA SHOWN. ANY OTHER DOTS SHALL ALSO BE LOCATED IN THIS AREA.

0.280 MIN 14 PLACES  
0.015 MAX  
0.050 12 PLACES  
OF PACKAGE  
14 PLACES  
0.15  
0.019  
0.250  
0.280  
0.010 MAX GLASS 14 PLACES  
0.700  
0.770  
0.350  
0.385  
0.035  
0.065  
14 PLACES  
0.003  
0.005  
0.010 MIN

### FL36

0.080 MAX  
0.004  
0.006  
0.030  
0.045  
0.050  
0.005  
0.385 MAX GLASS  
0.275  
0.750  
0.770  
0.015  
0.019  
0.020  
0.040  
PIN NO. 1 IDENT

### FL37

0.080 MAX  
0.004  
0.006  
0.007  
0.018  
0.390 MAX GLASS  
0.050  
0.005  
0.275 MAX  
0.080  
0.900  
0.020  
0.040  
0.015  
0.019  
PIN NO. 1 IDENT

### FL39

0.050  
0.090  
0.004  
0.006  
0.010  
0.025  
0.375  
0.400  
0.050  
0.065  
0.300 MAX GLASS  
0.940  
0.960  
0.020  
0.040  
0.015  
0.019  
PIN NO. 1 IDENT

### FL40

0.010  
0.014  
0.080  
0.300  
0.280  
0.345  
0.410  
0.370  
0.050  
0.066  
0.070  
0.030  
0.300 MAX  
0.040  
0.070  
0.020  
0.040  
0.015  
0.019  
PIN NO. 1 IDENT

### FL41

0.080  
0.080  
0.004  
0.006  
0.010  
0.025  
0.337  
0.350  
0.050  
0.065  
0.275 MAX GLASS  
0.080  
0.900  
0.020  
0.040  
0.015  
0.019  
PIN NO. 1 IDENT

### FL42

0.080 MAX  
0.004  
0.006  
0.010  
0.025  
0.300 MAX GLASS  
0.050  
0.065  
0.275 MAX GLASS  
0.080  
0.900  
0.020  
0.040  
0.015  
0.019  
PIN NO. 1 IDENT

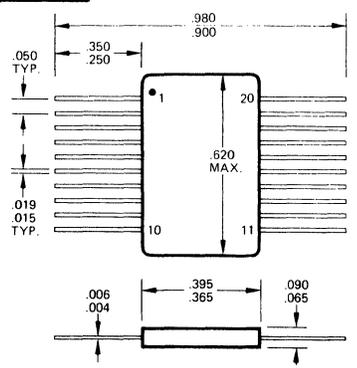
### FL43

0.080 MAX  
0.004  
0.006  
0.010  
0.025  
0.275 MAX GLASS (SQUARE)  
0.050  
0.065  
0.750  
0.770  
0.020  
0.040  
0.015  
0.019  
PIN NO. 1 IDENT

# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

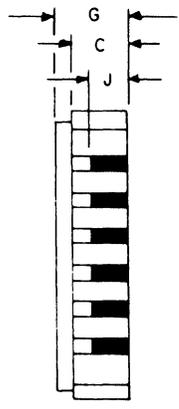
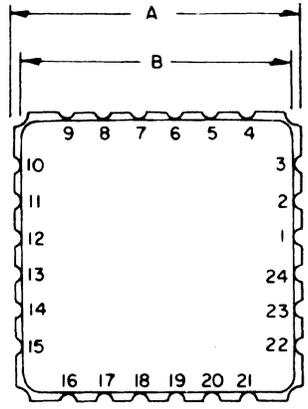
**FL44**



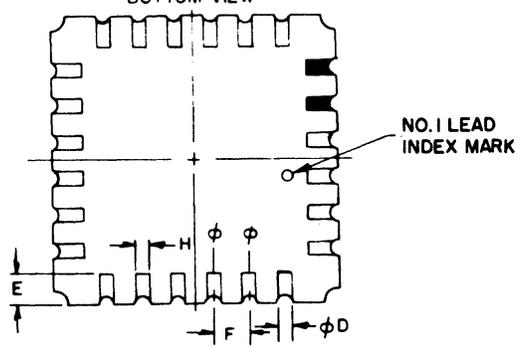
[Empty box]

[Empty box]

**FL45**

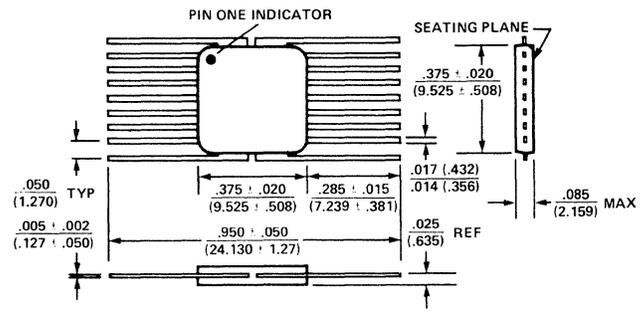


TOP VIEW  
BOTTOM VIEW



SYMBOL	DIMENSIONS			
	Inches		Millimeters	
	Min	Max	Min	Max
A	.410	.395	10.41	10.03
B	.385	.375	9.78	9.52
C	.075	.040	1.91	1.02
$\phi D$	.020	.010	0.51	0.25
E	.040	.030	1.02	0.76
F	.035	.045	1.40	1.14
G	.100	.050	2.54	1.27
H	.025	.015	0.64	0.38
J	-	.025	-	0.64

**FL46**

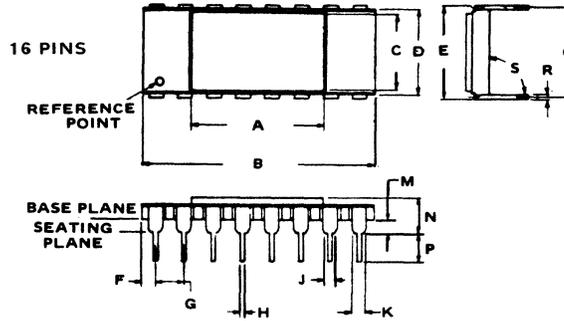


[Empty box]

# 23. OUTLINE DRAWINGS

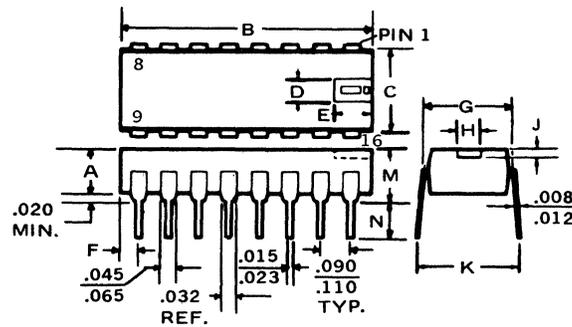
IN DRAWING NUMBER SEQUENCE

**ML1**



	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	S	NOTCH	
ML1		.800 MAX			.320 MAX		.092 .108				.018 MIN	.180 MAX		.300				
ML1a		.808 MAX			.294 MAX		.092 .108				.015 MIN	.194 MAX	.130	.294 .400				
ML1b	.462 .468	.790 .810	.273 .279	.275 .295	.320 MAX	.045 .055	.090 .110	.016 .020	.031 .035	.048 .052	.025 .050	.198 MAX	.100 .158	.300 MIN	.008 .012		90° 95°	
ML1c		.740 .870	.275 .310	.275 .320	.290 MAX		.090 .110	.016 .023		.040 .070	.020 MIN	.180 MAX	.125 MIN	.290 .320	.008 .015		YES	
ML1d	.500 MAX	.780 .810			.290 MAX			.016 .020			.020 .050	.140 MAX	.125 .175	.300 MIN	.008 .012			
ML1e	.485 MAX	.810 MAX		.295 MAX		.040 .060		.015 .020			.020 .060	.165 MAX	.125 MIN	.300	.008 .012			
ML1f		.790 .810		.302 .318	.310		.095 .105	.016 .020		.054	.095	.220 MAX	.165 MAX				YES	
ML1g		.790 .810		.300 TYP	.340 TYP			.016 .020	.054 TYP		.025 .045	.141 MAX	.130	.340 TYP			YES	
ML1h	.350 .430	.735 .830		.240 .295		.030 .060	.090 .110	.015 .020	.040 .055		.020	.175 MIN	.150 MIN	.290 .310	.008 .012		NO NOTCH	
ML1j		.810 MAX		.275 .295	.290 .310		.100 TYP	.015 .020			.020	.200 MIN	.090 MIN		.008 .012		NO	
ML1k	.430 .470	.770 .810	.265 .285	.278 .300	.290 MAX		.090 .100	.015 .020		.045 .065	.025 MIN	.110 MIN	.125 MIN	.375 NOM	.010 .012		YES	

**ML2**

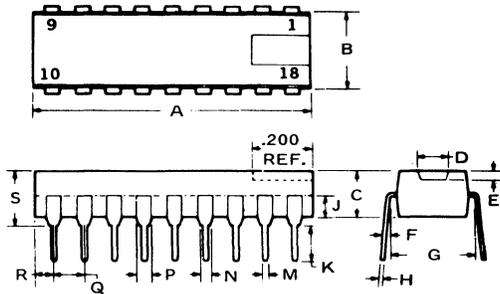


	A	B	C	D	E	F	G	H	J	K	M	N
ML2	.130 .155	.820 .850	.245 .255	.075	.125	.062	.290 .310	.075	.025	.290 .410	.200 MAX	.100 MIN
ML2a	.145 .155	.820 .850	.245 .255	.075	.125	.062	.290 .310	.075	.025	.290 .410	.200 MAX	.100 MIN
ML2b	.082 .100	.750 MAX	.265 MAX				.300 .310			.330 .370	.200 MAX	.100 MIN
ML2c	.180	.800	.270				.300			.200 MAX	.100 MIN	
ML2d	.125 .155	.745 .855	.245 .255	.060 .075	.060 .125	.025 .063	.290 .310	.080 .075	.025	.290 .410	.200 MAX	.100 MIN
ML2e	.140 MAX	.890 MAX	.260 MAX	.080	.080	.030			.030	.310 .350	.175 MAX	.125 MIN
ML2f	.170	.744 .857	.240 .267				.324 MAX			.290 .375	.185 MAX	.122 .150
ML2g		.870 MAX	.260 MAX				.300 TYP			.350 MAX	.200 MAX	.130 TYP
ML2h	.180	.881	.240 .255				.324			.290 .375	.200 MAX	.104 .150
ML2j		.870 MAX	.220 .280						.020 .040	.200 MAX	.100 MIN	
ML2k	.181 MAX	.870 MAX	.240 .263			.094 MAX	.287 .311			.301 .348	.200 MAX	.125 MIN
ML2m	.170 MAX	.744 .850	.241 .251				.324 MAX			.289 .375	.185 MAX	.125 .153
ML2n	.169 .177	.866 MAX				.061	.291 .307			.275 .322	.196 MAX	.110 .129

# 23. OUTLINE DRAWINGS

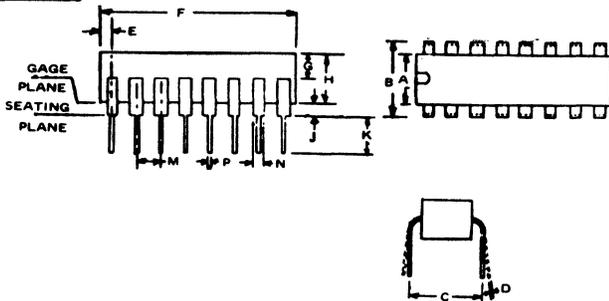
IN DRAWING NUMBER SEQUENCE

## ML3



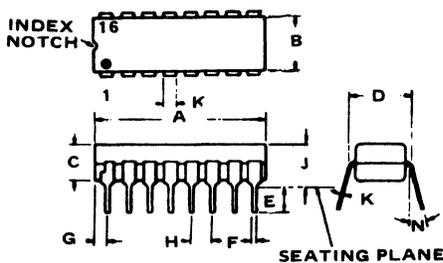
	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	S	REMARKS
ML3	.905	.240	.140	.100	.025	15°	.290	.008	.075	.100	.016	.032	.045	.090	.0575	.200	NOTCH
	.925	.260	.160	REF	REF	MAX	MIN	.012	REF	.150	.024	REF	.065	.110	REF	MAX	
ML3a	.905	.240	.140	.100	.025	15°	.290	.008	.075	.100	.016	.032	.045	.090	.0575	.200	DOT AT PIN 1
	.925	.260	.160	REF	REF	MAX	MIN	.012	REF	.150	.024	REF	.065	.110	REF	MAX	
ML3b	.920						.290	.008		.125	.015	.033	.060	.100	.030	.200	NOTCH
	MAX						.310	.013		MIN	.021	MIN		.070	MAX		
ML3c	.911	.302				15°	.300	.014		.118	.023	.032	.062	.100	.050	.200	NOTCH R .025
	MAX	MAX				MAX	TYP	MAX		MIN	MAX	MAX	MAX	TYP	MAX		
ML3d	.930	.220	.180			15°	.290	.008		.090	.015			.100		.200	DOT AT PIN 1
	MAX	.280	MAX			MAX	.310	.012		MIN	.020			TYP		MAX	
ML3e	.920	.250	.180			15°	.290	.008		.090	.015			.100		.200	
	MAX	.295	MAX			MAX	.310	.012		MIN	.020			TYP		MAX	
ML3f	.890	.240	.130				.300	.008		.100	.014	.032	.040	.090		.200	NOTCH
	.920	.260	.140				.400	.015		MIN	.023	TYP	.065	.110		MAX	
ML3g	.885	.252				15°	.300	.009		.100	.019			.055	.100	.181	NOTCH
	MAX					MAX				MIN						MAX	
ML3h	.890	.251	.139		.020		.290	.009		.125	.016		.060	.090	.050	.159	NOTCH
	.910	.261	.149		.030		.310	.011		MIN	.020			.110	TYP	.169	

## ML4



	A	B	C	D	E	F	G	H	J	K	M	N	P	REMARKS	
ML4	.265	.300	.300	.008	.050	.800	.050	.090	.020	.100	.045	.015			
	.285	.325		.012	.100	.840	.090	.200		.160	.070	.021			
ML4a	.244	.300	.350	.007		.818		.100	.050	.125	.100			.020	NOTCHED
	MAX	MAX	MAX	.009				.160	.020	.125	.100				NOTCHED
ML4b	.265	.300	.350	.010		.800		.100	.020	.125	.100			.019	NOTCHED
	MAX	MAX	MAX	.014		MAX		MAX	MAX	.165					
ML4c	.220	.290	.290	.008		.747		.200		.100	.090	.030	.014		NOTCHED OR DOT
	MAX	.310	.310	.015		.815		MAX		MIN	.110	.070	.023		
ML4d	.866		.326		.125	.866	.031	.149	.035	.164	.100				NOTCHED
	MAX		MAX		MAX	MAX	.039			MIN	.160				
ML4e	.265	.300	.330	.009		.750		.082		.130	.100	.050	.017		DOT ONLY
	MAX	.310	.390	.011		.100									
ML4f	.240	.300	.300	.008		.745		.100	.020	.125	.100			.014	
	MAX	.325		.012		.785		.135	.065	.160				.020	
ML4g	.270	.300	.300	.007		.760		.150	.020	.125	.100			.015	NOTCHED
	MAX	MAX	MAX	.013				MIN						.021	
ML4h	.245		.290	.008	.045	.745		.125	.020	.100	.090	.032	.015		NOTCHED
	MAX		MAX	.012	.055	.855		.155	MIN	MIN	.110	REF	.023		
ML4i	.278	.325	.300	.014	.050	.787		.180	.020	.188	.100	.032	.023		NOTCHED
	MAX	MAX	MAX	MAX	MAX	MAX		MAX	MIN	MIN			MAX		

## ML5



	A	B	C	D	E	F	G	H	J	K	L	M	N	REMARKS
ML5	.740	.240	.135	.290	.115	.015	.015	.090	.170	.006			0°	
	.780	.275	.165	.325	.135	.020	.035	.110	.200	.012			15°	
ML5a	.795	.258		.300	.118	.020		.100	.197	.008	.019	.239		
	MAX	MAX		TYP	MIN	TYP		TYP	MAX	.014	MIN	.384		
ML5b	.815	.240	.025	.290	.115	.015	.052	.100	.160	.008			10°	
	MAX	.260	.035	.310	.135	.020	.072		.180	.012				
ML5c	.760	.245	.115	.290	.120	.015		.090	.130	.008			15°	NO INDEX NOTCH
	MAX	.255	.125	.310	.130	.021		.110	.160	.012			MAX	
ML5d	.795	.220	.180	.290	.090	.015		.100	.200	.008			15°	
	MAX	.235	MAX	.310	MIN	.020		TYP	MAX	.012			MAX	
ML5e	.810	.250	.180	.290	.090	.015		.100	.200	.008			15°	
	MAX	.295	MAX	.310	MIN	.020		TYP	MAX	.012			MAX	
ML5f	.759		.129	.300	.135	.018	.029	.100		.059				
	MAX		MAX	MAX	MAX									



# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

### ML14

	A	B	C	D	E	F	G	H	J	K
ML14	.500	.600	.011	.130	.020	.021	2.00	.100	.125	.185
			MAX		MIN	MAX				
ML14a		.385	.010	.185	.020	.015	2.02	.085	.120	
		MAX		MIN	MAX			MAX		
ML14b	.510	.600	.011	.130	.025	.021	2.01	.100	.200	
			MAX		MIN	MAX			MIN	
ML14c		.600	.010		.020		2.04	.100	.090	
		NOM	NOM		MIN		MAX	NOM	MIN	
ML14d	.590	.600	.008	.165	.020	.016	2.020	.090	.125	.520
		MAX		MAX	MAX	MAX	MAX	MIN		

### ML15

	A	B	C	D	E	F	G
ML15	.325	.220	.008	0°	.750	.015	.200
	MAX	.280	.015	15°	.785	.023	MAX
ML15a	.290	.265	.009		.755	.016	.170
	MAX	.291	.011		.758	.020	.219

### ML17

	A	B	C	D	E	F	G	H	J
ML17									

### ML19

	A	B	C	D	E	F	G	H
ML19	.780	.290	.150	.009		.200	.090	.015
	MAX	.310	MIN	.011		MAX	.110	.023
ML19a	.685	.325	.100	.008	.240	.200	.090	.015
	MAX	MAX	MIN	.015	.280	MAX	.110	.023
ML19b	.685	.305	.130	.009	.235	.200	.090	.015
	MAX	MAX	MAX	.010	.285	MAX	.110	.023
ML19c	.775	.350	.150		.200	.200	.090	.015
	MAX	MAX			MAX	MAX	.110	.023
ML19d	.789	.326	.100	.007		.200	.090	.015
	MAX	MAX	.165	.014		MAX	.110	.023
ML19e	.660	.290	.100	.008	.220	.200	.090	.015
	MAX	MIN	MIN	.015	.280	MAX	.110	.023
ML19f	.755	.290	.130	.008	.280	.200	.090	.015
	MAX	.310	MIN	.014		MAX	.110	.023
ML19g	.787	.324	.104			.185		
	MAX	MAX	.150			MAX		

### ML21

	A	B	C	D	E	F	G	REMARKS
ML21	.475	1.13	.008	.590	.160	.090	.014	INDEX DOT ONLY
	MAX	1.28	.015	.650	MAX	MIN	.020	
ML21a	.520	1.25	.010	.600	.180	.125	.019	NOTCH ONLY
ML21b	.545	1.300	.010	.650	.195	.215	.020	INDEX DOT ONLY
	MAX	1.200	.008	.600	.120	.140	.015	
ML21c	.495	1.24	.010	.600	.157	.090	.015	NO INDEX DOT NO NOTCH
	MAX	1.30				MIN	.019	

### ML22

	A	B	C	D	E	F	G	H	J
ML22	.240	.740	.160	.090	.020	.090	.014	.290	.008
	MAX	.830	MIN	MIN	MIN	.110	.025	.350	.015
ML22a	.235	.735	.200	.115	.015	.090	.015	.290	.008
	MAX	.815	MAX	.135	MIN	.110	.023	.410	.015
ML22b	.245	.810	.110	.090	.020	.100	.014	.010	
	MAX	.830	MIN	MIN	MIN		.019		
ML22c	.288	.787	.218	.099	.019	.090	.014	.299	.005
	MIN	MIN	MIN	MIN	MIN	.109	.022	.013	

### ML23

	A	B	C	D	E	F	G	H	J
ML23	1.250	.515	.110	.125	.015			.250	.0085
	MAX	.545	.135	TYP	.025			TYP	.0095
ML23a	1.250	.515	.110	.125	.015			.250	.0085
	MAX	.545	.135	TYP	.025			TYP	.0095
ML23b	1.300	.495	.137	.090	.015	.397	.397		.010
	MAX	.595	MAX	MIN	.019	.495	.495		TYP

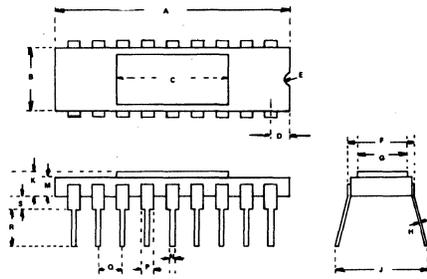
### ML24

	A	B	C	D	E	F	G	H	J
ML24									

# 23. OUTLINE DRAWINGS

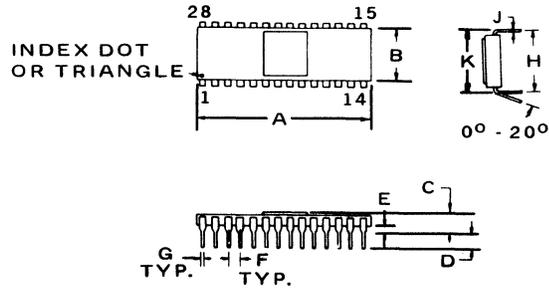
IN DRAWING NUMBER  
SEQUENCE

**ML25**



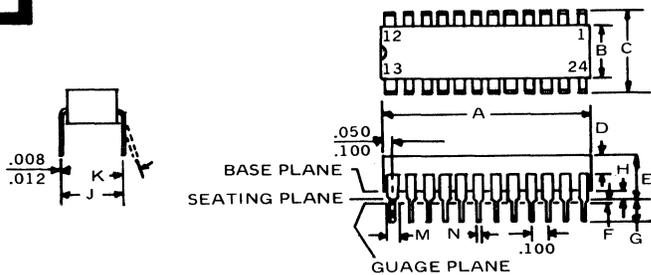
	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	S
ML25	.900	.280				.280	.010	.300	.090		.032	.054	.100	.230	.040	
ML25a	.890	.278	.460	.040	.025	.290	.271	.009	.375	.125	.065	.016	.040	.095	.125	.040
	.910	.288	.470	.060		.310	.281	.011		.155	.085	.020	.045	.105	.175	.060

**ML29**



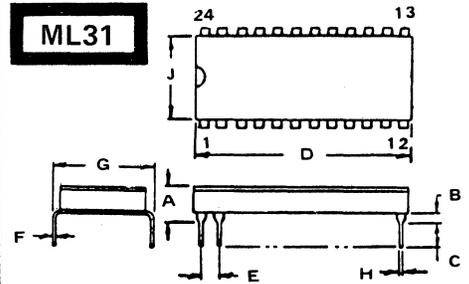
	A	B	C	D	E	F	G	H	J	K
ML29	1.33	.475	.160	.090	.020	.090	.014	.590	.008	.590
	1.47	.530	MAX	MIN	MIN	.110	.020	.650	.015	.650
ML29a	1.40	.530	.200	.125	.040	.090	.018	.610	.010	.610
	MAX	MAX	MAX	MIN	.060	.110	.020	.650		MAX

**ML30**



	A	B	C	D	E	F	G	H	J	K	M	N	REMARK
ML30	1.18	.490	.600	.015	.090	.020	.100		.300	2°	.045	.015	NC NOTCH
	1.22	.520	.625	.050	.200		.150			15°	.070	.021	
ML30a	1.22	.500	.545		.110		.100	.040		0°		.015	
	1.26	.520	.600		.150		.140			15°		.018	
ML30b	1.25	.520	.600		.180		.125	.020	.650		MAX	.010	
					MAX								
ML30c	1.23	.515			.150		.100	.025	.750			.016	
	1.29	.575			.273		.200	.063	MAX			.026	
ML30d	1.23	.520	.625		.220		.100	.030	.750	0°	.055	.015	
	1.28	.550	MAX		MAX		MIN	MIN	MAX	15°		.023	
ML30e	1.15	.480	.600	.020	.090	.000	.100	.020	.600	0°	.045	.015	NC NOTCH
	1.22	.520	.625	.080	.150	.030	.180	.065		15°	.055	.020	
ML30f	1.23	.500			.160			.020	.605	5°		.016	
	1.27	.540			.200			.030		15°		.020	
ML30g	1.14	.515	.590		.200	.015	.125	.015	.590	0°	.030	.014	
	1.29	.610	.620		.280	.080	.200	.080	.620	15°	.070	.023	
ML30h	1.244	.540		.075	.150		.125	.075	.600	0°	.060	.018	
	1.256						MIN		.650	15°		TYP	

**ML31**

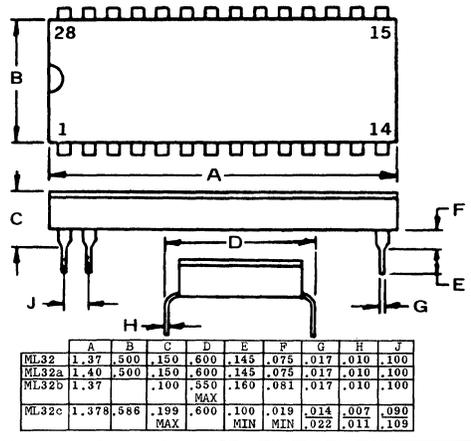


	A	B	C	D	E	F	G	H	J
ML31	.150	.075	.145	1.17	.100	.010	.100	.800	.500
ML31a	.100	.051	.140	1.17	.100	.010	.100	.800	.500
ML31b	.100		.171	1.37	.100	.010	.600	.017	.500
ML31c	.158	.024	.130	1.22	.100	.009	.600	.018	
				MAX					

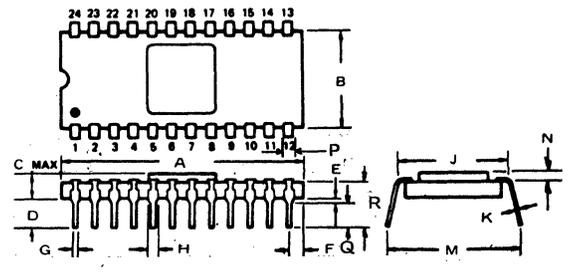
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

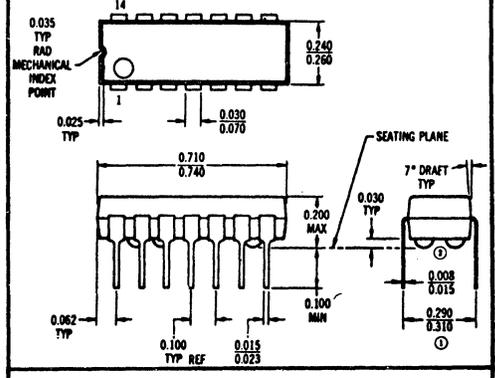
**ML32**



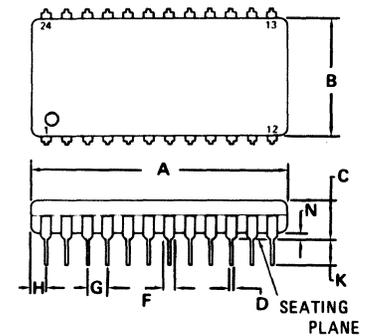
**ML34**



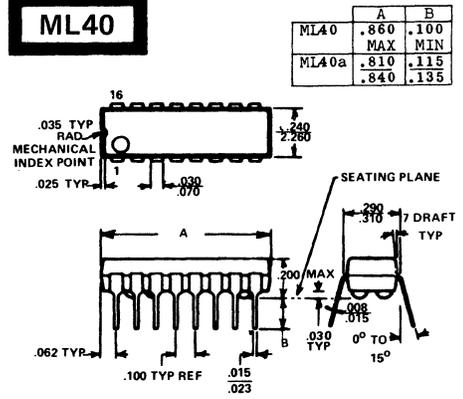
**ML38**



**ML39**

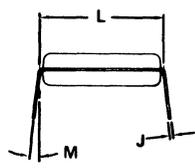


**ML40**



	A	B	C	D	F	G	H	J	K	L	M	N
ML39	1.235	.340	.180	.014	.040	.095	.070	.008	.120	.580	0°	.020
	1.265	.560	.200	.020	.060	.105	.080	.012	.140	.600	10°	.040
ML39a	1.180	.480	.090	.015	.045	.100	.020	.008	.100	.800	15°	.020
	1.220	.520	.150	.020	.055	TYP	.060	.012	.160	TYP	MAX	.065
ML39b	1.235	.340	.165	.014	.040	.100	.065	.008	.115	.600	15°	.020
	1.265	.560	.200	.020	.065	TYP	.090	.012	.140	TYP	MAX	.040
ML39c	1.290	.550	.225									
	MAX	MAX	MAX									
ML39d	1.220	.600	.175									
	MAX	MAX	MAX									

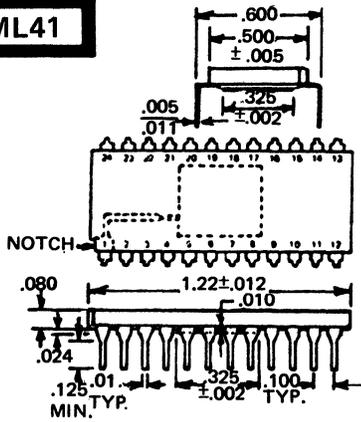
NOTES:  
 1. LEADS, TRUE POSITIONED WITHIN 0.25 mm (0.010) DIA AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION (DIM. "D")  
 2. DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.



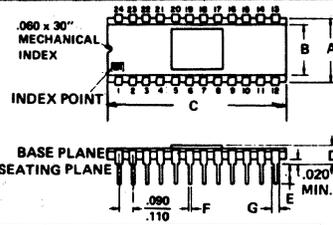
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

**ML41**

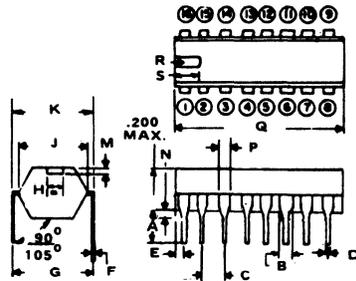


**ML47**



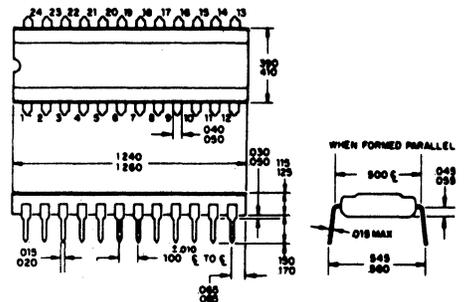
	A	B	C	D	E	F	G	H	J	K	REMARKS
ML47	.450 .470	.355 .365		.150 MAX	.050 .150	.017 .023	.045 .055	.007 .011	.490 .510	0° 20°	
ML47a	.470 .530		1.14 MIN	.070 MIN	.100 MIN	.015 .022		.009 .011	.750 MAX		INDEX POINT ONLY NO NOTCH
ML47b	.500		1.20	.130	.100	.021		.011	.600		INDEX POINT ONLY
ML47c	.490 .580		1.10 MAX	.140 MAX	.120 MAX	.017 .023		.008 .012	.590 .670		INDEX POINT ONLY
ML47d	.500		1.40	.130	.190	.021		.011	.600		INDEX POINT ONLY
ML47e	.480 .510		1.19 MAX	.100 MAX	.145 MAX	.015 .021	.045 .050	.008 .012	.625 .675		

**ML48**

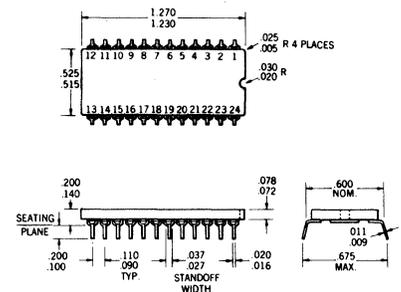


	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	S
ML48	.125 MIN	.033 MIN	.100	.015 .021	.100	.008 .014	.300 .350	.080 .260	.240 .310	.010	.020 MIN	.070 MAX	.070 MAX	.870 MAX	.093	.180
ML48a	.140 MIN	.038 MIN	.100	.018		.008 .014	.300 .350		.240 .310	.010				.870 MAX		
ML48b	.125 MIN	.033 MIN	.100	.014 .020	.031 MAX	.017 MAX			7.37		.020			.881 MAX		
ML48c	.120 MIN		.100	.015 .023	.008 .015		.070 .090	.220 .280		.020 MIN	.020 MIN	.030 MAX	.070 MAX	.870 MAX	.083 MAX	.180 MAX
ML48d	.180 TYP			.095 .105	.020			.300 TYP			.020 MIN			.870 MAX		
ML48e	.120 .183		.100	.014 .021	.086 MAX	.009	.300 .374		.324		.020 MIN			.885 MAX		

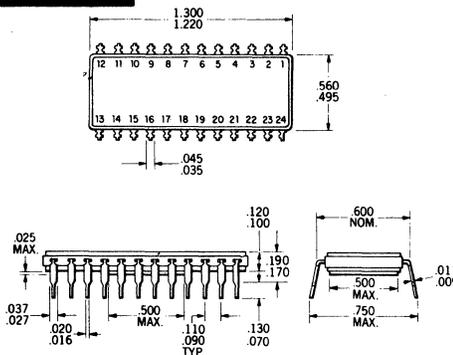
**ML50**



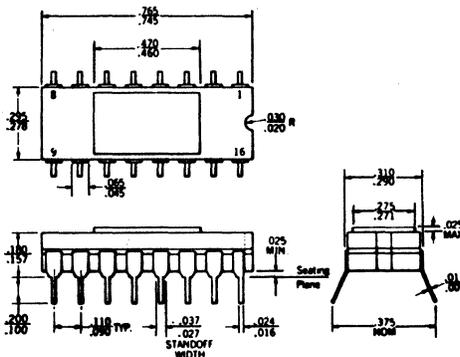
**ML51**



**ML52**



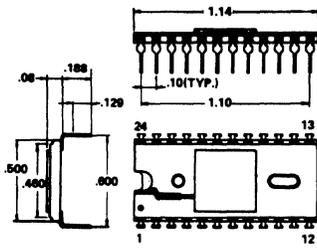
**ML57**



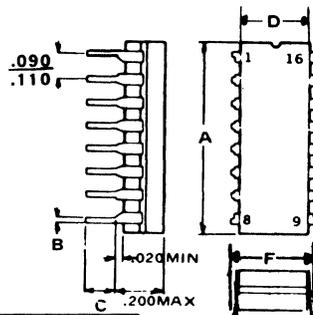
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

**ML59**

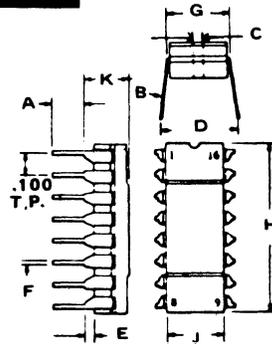


**ML60**



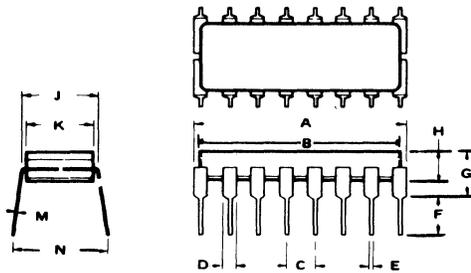
	A	B	C	D	E	F
ML60	.785	.020	.150	.290	.350	
				.310	MAX	
ML60a	.755	.015	.130	.280	.325	.290
	.785	.025	MIN	MAX	.395	.310
ML60b	.740	.015	.115	.240		.325
	.780	.020	.135	.275		MAX
ML60c	.799	.014	.099		.299	.335
	MAX	.020	.149			MAX

**ML61**



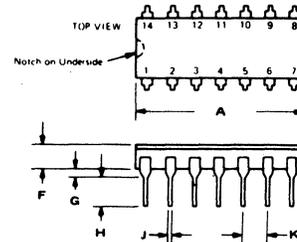
	A	B	C	D	E	F	G	H	J	K
ML61	.100	.009		.375	.015	.033	.290	.750	.240	.200
	MIN	.011			MIN	MAX	.310	.785	.260	MAX
ML61a	.130	.008	.050	.300	.020	.015	.290	.755	.245	.200
	MIN	.014		.350	MIN	.023	.310	.785	.260	MAX
ML61b	.115	.008		.290	.015	.015	.290	.735	.235	.200
	.135	.015		.410	MIN	.023	.325	.815	.290	MAX
ML61c	.130	.008		.325	.020	.015	.290	.755	.280	.200
	MIN	.014		.375	MIN	.023	.310	.785	MAX	MAX
ML61d	.125	.008		.325	.020	.015	.290	.755	.280	.145
	.165	.014		.395	.040	.023	.315	.785	MAX	.175
ML61e	.125	.008		.020	.015	.290	.750	.245	.160	
	.160	.012			.040	.020	.310	.780	.275	.200

**ML62**



	A	B	C	D	E	F	G	H	J	K	M	N
ML62	.740		.090	.055	.019	.100	.200	.090	.290	.235	.009	.375
	.780		.110			MIN	MAX	.110	.310	.265	.011	
ML62a		.700	.080	.040	.015	.100	.200	.145	.290	.245		
			.110	MIN	MIN	MAX	MAX	.310	.275			
ML62b	.780	.725	.090		.015	.105	.180		.320	.265	.010	.310
	MAX	MAX	.110		.019	.145	MAX		MAX	MAX		.350
ML62c	.730		.080	.030	.015	.100	.200	.120	.290	.230	.008	.350
	.770		.110	.070	.023	MIN	MAX	.310	.265	.015	NOM	

**ML63**

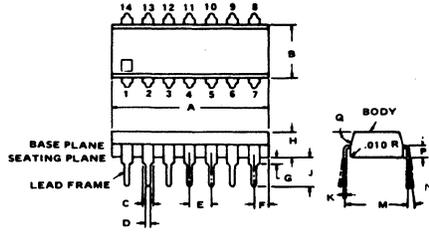


	A	B	C	D	E	F	G	H	J	K
ML63	.685	.300	.246	.350	.009	.104	.050	.125	.020	.100
	.715		.256		.011	MAX	MIN	MIN		
ML63a	.725	.300	.265	.310	.010	.180	.020	.105	.015	.090
	MAX	MAX	MAX	.350		MAX		.145	.019	.110
ML63b	.660	.290	.230	.350	.008	.120	.020	.100	.015	.090
	.785	.310	.265		.011	.180	.080	MIN	.023	.110
ML63c	.750	.308	.245		.009	.140	.015	.125	.016	.100
	.785	.314	.271		.011	.185	.060	.200	.020	TYP

# 23. OUTLINE DRAWINGS

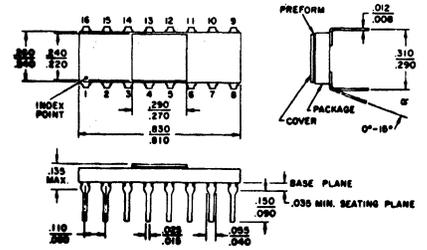
IN DRAWING NUMBER SEQUENCE

**ML64**

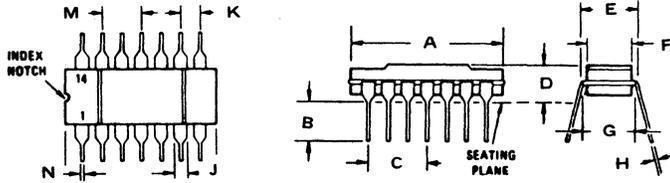


	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	REMARKS
ML64	.780	.245	.050	.018	.090	.065	.015	.115	.135	.015	.290	0°	.060	70°	
ML64a	.780	.255	.054	.021	.110	.085	.035	.125	.155	.030	.310	20°			INDEX NOTCH
ML64b	.775	.235	.045	.015	.090		.020	.140	.180	.008	.320				INDEX NOTCH
ML64c	.785	.244	.061	.018	.100		.019	.196	.101	.007	.300	0°			INDEX
ML64d	.757			.018	.100	.084	.020	.133	.165	.014		7°			INDEX
ML64e	.745	.240	.050	.014	.100		.020					0°			INDEX DOT
ML64f	.770	.280	.065	.020			.050					15°			
ML64f	.800	.252		.018	.100		.019	.181	.149		.300				

**ML65**

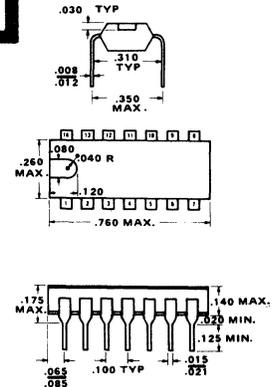


**ML66**

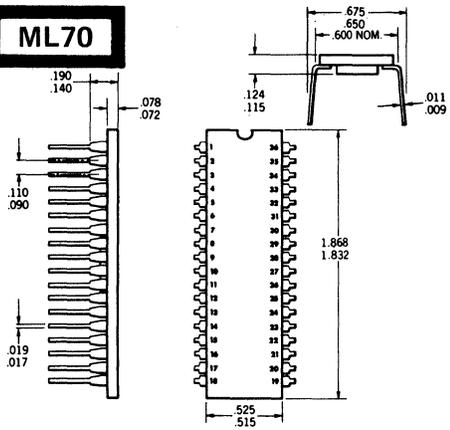


	A	B	C	D	E	F	G	H	J	K	M	N
ML66	.660	.100	.290	.200	.325	.220	.290	.008	.030	.090	.190	.015
ML66a	.755	.130		.200	.245	.290	.008	.070	.100			.015
ML66b	.755	.130		.200	.325	.280	.290	.008	.070	.090		.015
ML66c	.755	.149		.200	.375	.244	.310	.014	.062	.110		.023

**ML69**

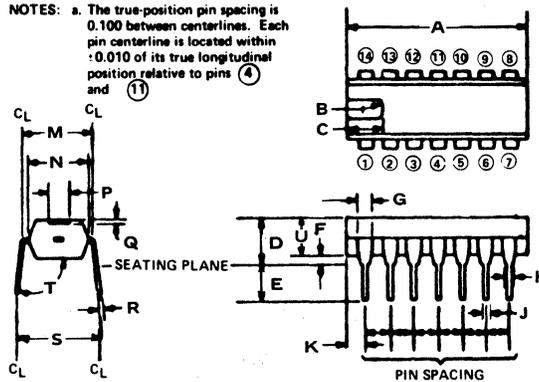


**ML70**



**ML71**

NOTES: a. The true-position pin spacing is 0.100 between centerlines. Each pin centerline is located within ±0.010 of its true longitudinal position relative to pins ④ and ⑩.

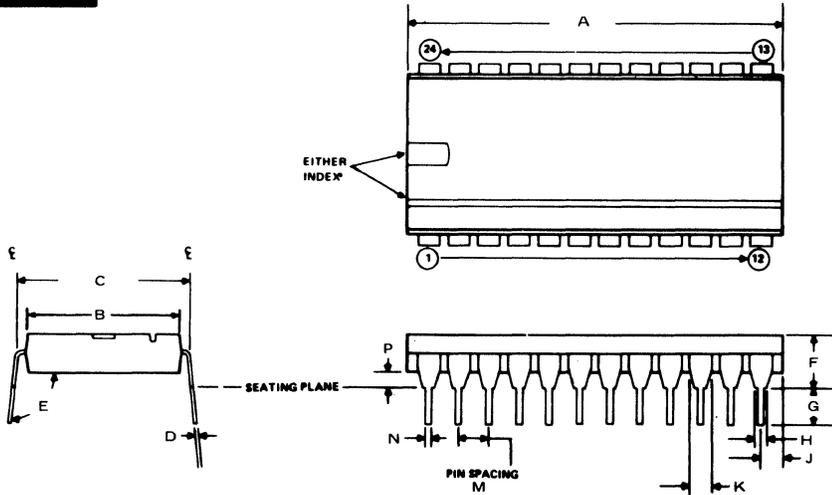


	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	S	T	U
ML71	.710	.093	.110	.200	.125	.020	.070	.033	.015	.055	.290	.240	.080	.010	.008	.300	90°	
ML71a	.770			.200	.125	.020		.036	.015	.055	.290	.260		.014	.014	.350	105°	
ML71b	.670		.110	.185	.135	.035	.055	.033	.019	.065		.258	.080	.030	.010	.350	75°	
ML71c	.770	.093	.110	.200	.110	.014	.055	.033	.015	.065		.240	.080	.030	.008	.290	75°	
ML71d	.710	.093	.110	.200	.125	.020	.070	.038	.015	.055		.240	.080	.010	.008	.300	75°	
ML71e	.770			.200	.125	.020		.038	.015	.055		.240	.080	.010	.014	.350	105°	
ML71e	.759		.059		.135		.059		.018	.079	.300							.129
ML71f	.748	.098		.200	.110	.019	.045		.019	.074	.300	.251			.010	.300	90°	
ML71g	.767			.185	.120	.020	.047		.014	.086	.324				.009	.300	105°	

# 23. OUTLINE DRAWINGS

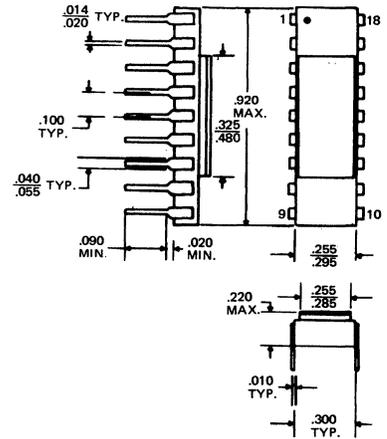
IN DRAWING NUMBER SEQUENCE

**ML72**

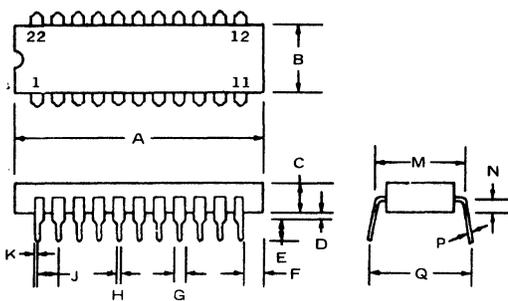


	A	B	C	D	E	F	G	H	J	K	M	N	P	REMARKS
ML72	1.310 MAX	.650 .675	.626 .625	.008 .014	90° 105°	.200 MAX	.125 MIN	.033 MIN	.095 MAX	.070 MAX	.100 MAX	.015 MIN	.020 MIN	NOTCH ONLY
ML72a	1.245 MAX	.585 .595	.625 .675	.008 .014	90° 105°	.200 MAX	.125 MIN	.033 MIN	.095 MAX	.070 MAX	.100 MAX	.015 MIN	.020 MIN	NOTCH ONLY
ML72b	1.290 MAX	.590 .610	.590 .610	.008 .014	90° 105°	.200 MAX	.125 MIN	.033 MIN	.055 MAX	.060 MAX	.100 MAX	.015 MIN	.020 MIN	EITHER INDEX
ML72c	1.310 MAX	.550 .610	.590 .610	.008 .014	90° 105°	.200 MAX	.125 MIN	.033 MIN	.095 MAX		.100 MAX	.015 MIN	.020 MIN	NOTCH ONLY
ML72d	1.270 MAX	.515 .590	.590 .610	.008 .012	90° 105°	.200 MAX	.090 MIN				.100 MAX	.018 MIN	.020 MIN	NOTCH AND DOT AT PIN 1
ML72e	1.310 MAX	.480 .590	.590 .610	.008 .012	90° 105°	.200 MAX	.090 MIN				.100 TYP	.015 MIN	.015 MIN	NOTCH ONLY

**ML73**

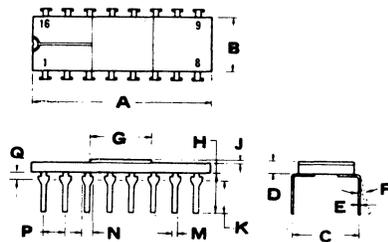


**ML77**



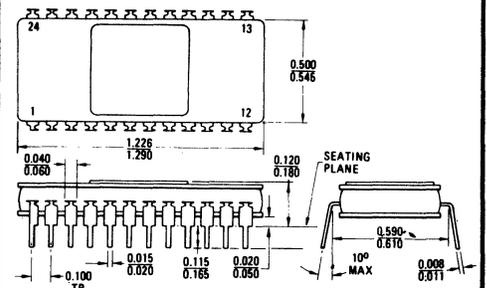
	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	REMARKS
ML77	1.080 MAX	.270 .340	.170 MAX	.030 MIN	.150 MIN	.010 MIN	.060 TYP	.015 REF	.100 REF	.025 MAX	.400 MAX	.045 MAX	.008 .012	.370 .480	
ML77a	1.079 MAX	.358 MAX	.165 MAX	.047 MAX	.118 MAX	.050 MAX	.059 MAX	.016 MAX	.100 MAX		.400 MAX		.009 MAX	.425 MAX	
ML77b	1.141 MAX	.180 MAX	.020 MIN	.114 MIN	.078 MAX	.047 MAX	.015 MAX	.021 MAX	.100 MAX		.429 MAX		.009 MAX	.400 .490	
ML77c	1.130 MAX	.320 .380	.180 MAX	.020 MIN	.090 MIN	.040 MAX	.015 TYP	.020 TYP	.100 TYP		.390 .410		.008 .012		
ML77d	1.110 MAX	.355 .395	.185 MAX	.015 MIN	.090 MIN	.040 MAX	.015 TYP	.020 TYP	.100 TYP		.390 .410		.008 .012		
ML77e	1.12 MAX	.360 MAX	.165 MAX	.035 MIN	.100 MIN		.020 TYP	.100 TYP			.400 MAX		.010 MAX		
ML77f	1.079 MAX	.181 MAX	.019 MIN	.100 MIN	.050 MAX		.014 TYP	.100 TYP			.413 MAX		.010 MAX		
ML77g	1.070 TYP	.380 TYP	.020 MIN	.100 MIN			.019 TYP	.100 TYP							
ML77h	1.060 1.090	.364 .380	.170 MAX	.020 MIN	.150 MIN	.025 MAX	.056 TYP	.018 TYP	.100 TYP		.420 MAX		.009 .011	.515 MAX	

**ML78**



	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	REMARKS
ML78	.805 .835	.270 .280	.300 MAX	.060 .085	.008 .012	0° 7°	.400 MAX	.180 .210	.016 MAX	.135 .190	.015 .020	.045 .055	.100 MAX	.020 .055	
ML78a	.818 MAX	.272 MAX	.300 MAX	.078 MAX	.010 MAX	0° 15°	.378 MAX			.080 .149	.016 MAX	.050 MAX	.100 MAX	.039 MAX	NO INDEX NOTCH

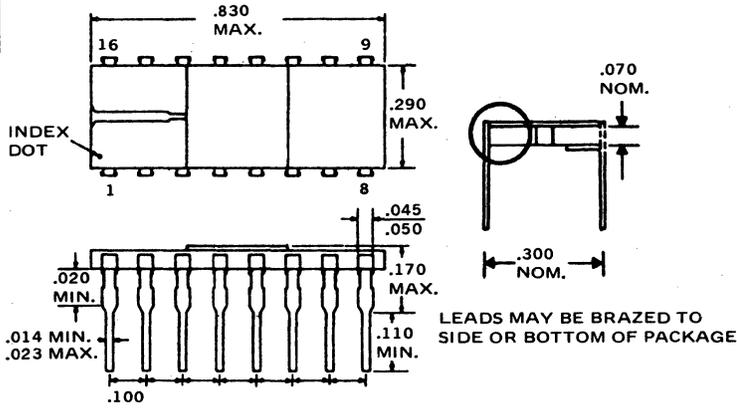
**ML79**



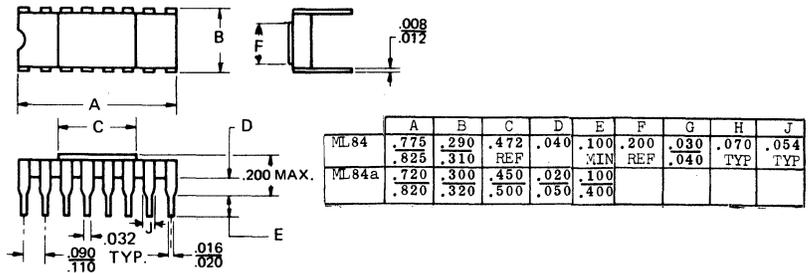
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

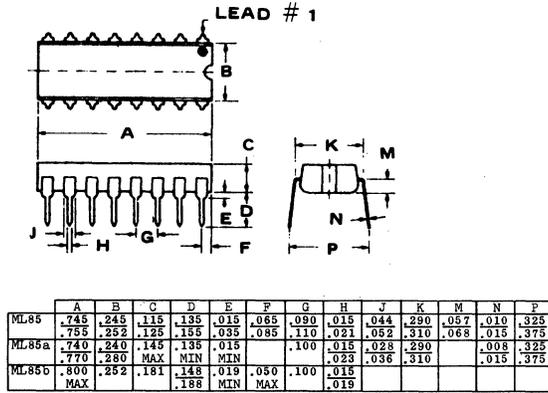
**ML82**



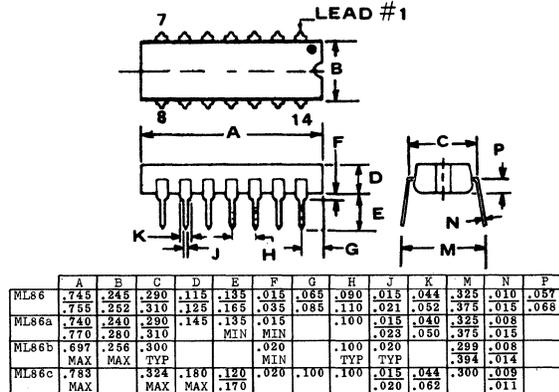
**ML84**



**ML85**



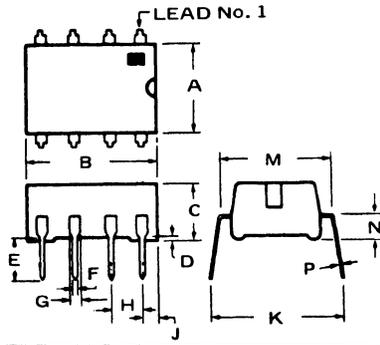
**ML86**



# 23. OUTLINE DRAWINGS

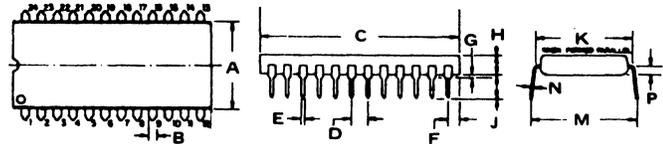
IN DRAWING NUMBER  
SEQUENCE

**ML87**



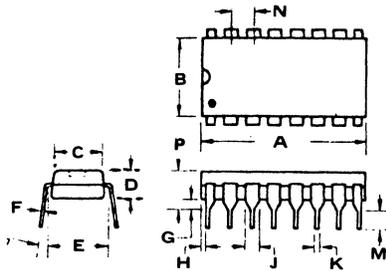
	A	B	C	D	E	F	G	H	J	K	M	N	P
ML87	.250	.365	.155	.010	.015	.044	.090	.025	.325	.290	.100	.010	
	MAX	.375	.165	MIN	.020	.050	.110	.045	.375	.310	MAX		
ML87a	.245	.365	.155	.010	.120	.015	.044	.090	.025	.325	.290	.075	.010
	.252	.375	.165	.020	.135	.021	.052	.110	.045	.375	.310	.085	.015

**ML88**



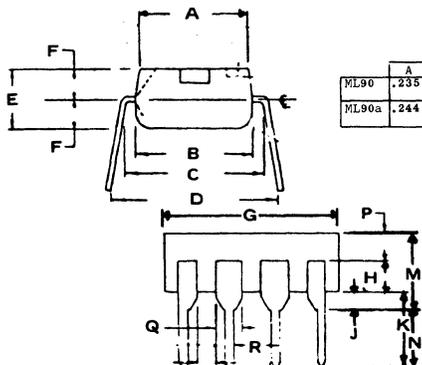
	A	B	C	D	E	F	G	H	J	K	M	N	P	REMARKS
ML88	.540	.040	1.24	.090	.015	.065	.030	.115		.600	.645	.015	.045	
	.550	.050	1.25	.110	.020	.085	.050	.125		.650	.655	MAX	.055	
ML88a	.527		1.25	.090	.017		.020	.208	.120	.590		.007		INDEX NOTCH ONLY
				.110	.019							.014		
ML88b	.530		1.240	.100	.015		.050	.170	.175	.600		.007		INDEX NOTCH ONLY
			1.250		.021		TYP					.015		
ML88c	.525		1.24	.090	.015		.030	.180	.145	.585	.825	.003		INDEX NOTCH
	.535		1.25	.110	.022		NOM	MAX	MIN	.595	.675	.011		
ML88d	.510	.045	1.23	.090	.015		.020	.180	.145	.590	.700	.009		INDEX NOTCH
	.540	.065	1.29	.110	.023		.060	MAX	MIN	.620	NOM	.011		
ML88e	.500		1.22	.090	.015		.020	.180	.145	.590	.650	.005		INDEX DOT
	.545		1.29	.110	.023		MIN	MAX	MIN	.610	NOM	.015		
ML88f	.580		1.190	.090	.015	.030	.030	.080			.590	.008		
	.610		1.230	.110	.021	.065	.070	.120			.620	.012		
ML88g	.515	.045	1.230	.090	.027		.025	.125	.037	.600	.750	.009		INDEX NOTCH
	.575	.065	1.290	.110	.037		.063	.210	.175	TYP	MAX	.011		

**ML89**



	A	B	C	D	E	F	G	H	J	K	M	N	P
ML89	.760	.250	.235	.130	.300	.010	.040	.020	.060	.020	.130	.092	.180
	.765											.108	MAX
ML89a	.745	.245		.115	.290	.010	.015	.020	.044	.015	.120	.090	.130
	.755	.252		.125	.310	.015	.035	.030	.054	.021	.135	.110	.160
ML89b	.750	.235		.180	.325	.008	.020			.015	.090	.200	
	.880	.275			MAX	.015				.023	MIN	.110	MAX
ML89c	.787	.252			.300		.020	.043			MAX	.141	.100
	MAX	MAX			MIN		MIN				MAX	.125	MAX
ML89d	.745	.245		.290	.325	.009	.020			.018	.125	.090	.200
	.755	.255		.310	.375	.011	MIN			.022	MIN	.110	MAX
ML89e	.850		.250	.130	.300	.010	.100			.060	.018	.130	.165
										TYP	TYP	MAX	
ML89f	.748	.251			.307	.008	.020	.039	.047	.017	.129	.092	.159
		TYP			.387	.013	.040	.047	.055	.021	.153	.108	.179
ML89g	.760	.248			.300	.008	.019		.051	.014	.110	.100	.200
						.014	MIN			.022	MIN		
ML89h	.760	.267			.300	.008	.019		.051	.014	.110	.100	.200
						.014	MIN			.022	MIN		

**ML90**

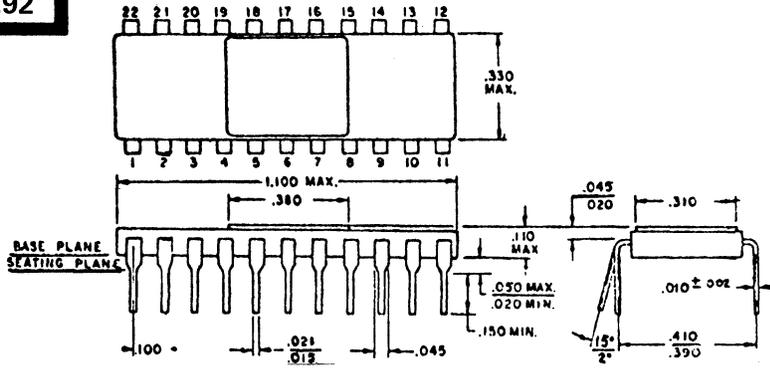


	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R
ML90	.235	.250	.300	.360	.130	.065		.070	.030	.150		.060	.060	.020	
				MAX											
ML90a	.244	.244	.300		.179		.377		.019	MIN	.199	.100		.050	.007
															.014

# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

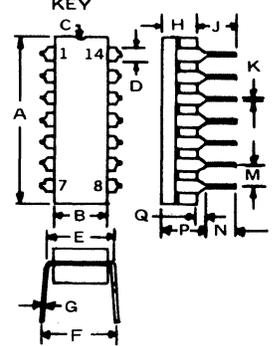
**ML92**



**ML93**

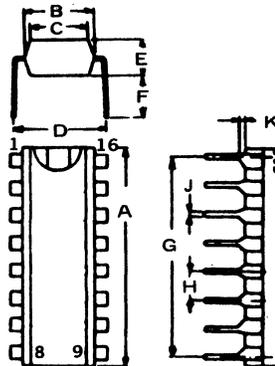
	A	B	C	D	E	F	G	H	J	K	M	N	P	Q
ML93	.750	.245	.025	.057	.290	.350	.009	.160	.180	.015	.090	.140	.200	.020
ML93a	.755	.275		.065	.310	.400	.012	MAX	MAX	.018	.110	MAX	MAX	.055
	MAX	.250		.060				.130		.020	.092			.040
ML93b	.755	.280		.070	.290	.335	.008	.145	.145	.015	.090	.125	.165	.020
	.785	MAX		MAX	.315	.395	.014	.175	.205	.023	.110	.165	.215	.040
ML93c	.765			.060	.300		.008			.021	.100	.125	.175	.025
							.012			.022	.110	MIN	MAX	.070
ML93d	.785	.260	.025	.055	.290	.360	.008			.016	.090	.125	.200	.020
	MAX	MAX		.065	.320	.410	.012			.022	.110	MAX	MAX	.070
ML93e	.799	.299						.180	.149	.016	.100	.130	.200	.019
	MAX	MAX						MAX	.170	.020		.151	MAX	
ML93f	.785	.251	.039	.059	.299		.009	.180	.120	.018	.100	.100	.199	.020
	MAX							MAX	MIN				MAX	MIN
ML93g	.745	.240		.035	.300	.300	.008			.014	.100	.125	.120	.020
	.770	.260		.065	TYP	.325	.012			.020	TYP	.150	.160	.065
ML93h	.715	.240		.040	.300	.300	.008			.014	.100	.115	.155	.020
	.770	.260		.065	TYP	.325	.012			.020	TYP	.150	.200	.050

PIN LOCATING KEY

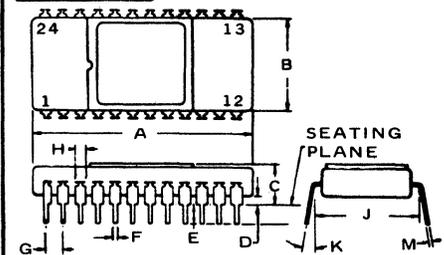


**ML94**

	A	B	C	D	E	F	G	H	J	K
ML94	.787	.265	.210	.330	.129	.160	.700	.090	.017	.030
	MAX	MAX						.110		
ML94a	.755	.244			.179	.120		.090	.007	.019
								.110	.014	
ML94b	.854	.355		.298	.177	.187		.090	.015	.019
	TYP	MAX		MAX	MIN			.110	.023	MIN
ML94c	.740	.252		.330	.126	.125		.100	.018	.025
	MAX	MAX		MAX	MAX					
ML94d	.870	.240		.290	.180	.140	.700	.100	.015	.020
	MAX	.260		.310	MAX	MIN	TP	TP	.021	MIN

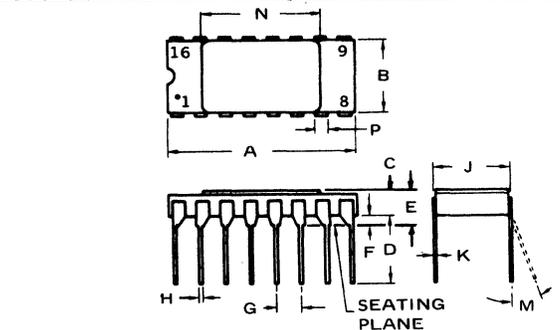


**ML95**



	A	B	C	D	E	F	G	H	J	K	M
ML95	1.10	.500	.120	.020	.115	.015	.100	.040	.590	10°	.008
	1.29	.545	.180	.050	.165	.020		.060	.610	MAX	.011
ML95a	1.14	.500	.120	.020	.115	.015	.100	.040	.590	10°	.011
	1.29	.560	.180	.050	.165	.020		.060	.610	MAX	.012

**ML98**



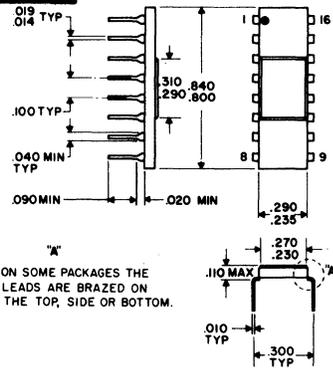
	A	B	C	D	E	F	G	H	J	K	M	N	P
ML98	.740	.278	.086	.185		.020	.100	.017	.300	.008	10°	.470	.045
	.737	.268	.144	.200		.045		.023		.012	MAX		.060
ML98a	.745	.278	.100	.120		.020	.100	.017	.300	.008	10°	.470	.045
	.808	.310		.245		.045		.023	.012	MAX			.060
ML98b	.740			.140	.105	.025	.100	.016	.300	.008	10°		.045
	.787			.190	.155	.045		.020		.012			.055



# 23. OUTLINE DRAWINGS

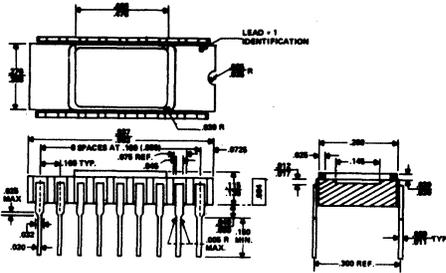
IN DRAWING NUMBER SEQUENCE

**ML110**

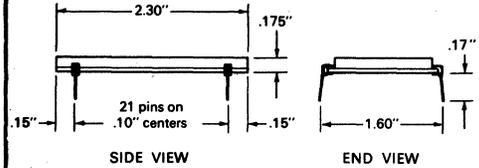


"A"  
ON SOME PACKAGES THE LEADS ARE BRAZED ON THE TOP, SIDE OR BOTTOM.

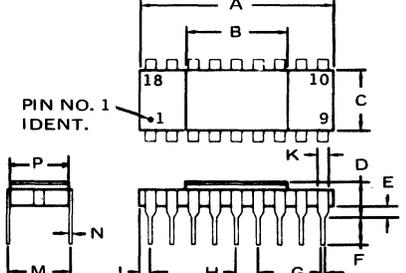
**ML112**



**ML113**

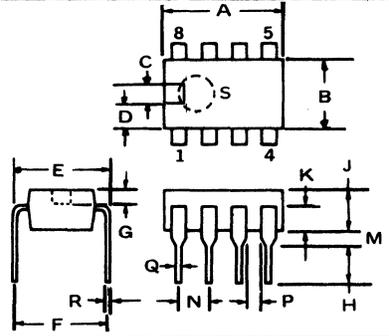


**ML115**



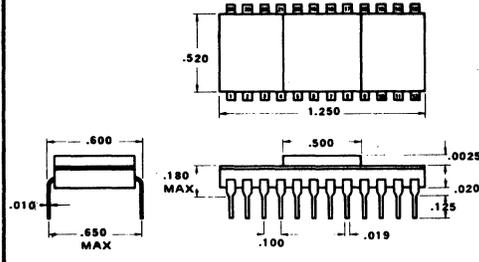
	A	B	C	D	E	F	G	H	J	K	M	N	P
ML115	.915		.310	.140	.020	.125	.018	.100	.050	.050	.300	.008	.292
	MAX			MAX	MIN	MIN						.011	
ML115a	.910	.490	.295	.165	.020	.125	.015	.090	.040	.054	.300	.008	
	MAX	MAX	MAX	MAX	MAX	MIN	.020	.110	.060			.012	
ML115b	.900	.460	.284	.155	.035	.135	.016	.100	.050		.300	.008	.320
	MAX	MAX	MAX	MAX	MAX	MAX	.020		MAX			.012	MAX
ML115c	.880	.275	.200	.015	.100	.014	.090			.040	.280	.008	
	.920		.305	MAX	MIN	MIN	.023	.110		.065	.380	.015	
ML115d	.890	.275	.200	.020	.100	.016	.090	.045	.043			.008	.290
	.915		.295	MAX	.060	.150	.023	.110	.065	.060		.012	.310

**ML116**

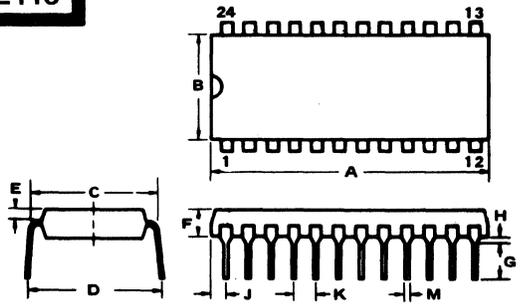


	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	S
ML116	.400	.245	.080	.085	.300	.300	.075	.125	.128	.065	.020	.100	.040	.016	.001	NA
	MAX	.255			.320	.350	MAX	MIN	.132	MAX				.020	.010	
ML116a	.380	.245	.065	.065	.290	.290	.025	.100	.125		.020	.090		.018	.008	NA
	MAX	.255	.075	.075	.310	.410		.150	.155		MIN	.110		.023	.012	
ML116b	.400	.245			.300	.310	.030	.125	.125	.065	.020	.100		.018	.015	.092
	MAX	.255			.320	.350	MAX	MIN	.135		MIN	.100		.021	.019	DIA

**ML117**



**ML118**



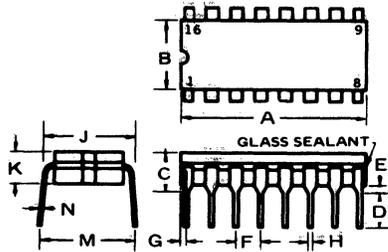
	A	B	C	D	E	F	G	H	J	K	M
ML118	1.25	.540	.610	.624	.075	.160	.125	.030	.075	.100	.010
		MAX		.650			MIN	MIN			
ML118a	1.22		.600	.600	.055	.125	.137	.035	.056	.100	.014
					.049	.082	.200	.150	.047	.086	.020
ML118b	1.25		.600	.625		.181	.114	.019	.086	.100	.010
	MAX					MIN	MIN	MAX	MAX		
ML118c	1.22		.543	.600	.055	.125	.137	.035	.066	.100	.001
	1.25		.559	.649	.062	.200	.150	.047	.086		
ML118d	1.20	.500		.600		.170	.100	.020		.080	.015
	1.30	.600		.730		.200	.150	.050		.110	.023
ML118e	1.31	.550	.550		.090			.020	.070	.100	.018
	MAX							TYP		MIN	TYP
ML118f	1.23	.530	.590			.145		.020	.070	.100	.016
	1.25	.550	.610			.165		MIN	REF	TYP	.023
ML118g	1.307	.541	.590	.606		.181	.125	.019	.094	.099	.018x.011
	1.322	.561	.610	.649		MAX	MIN	MAX	MAX		
ML118h	1.259	.499	.599	.599		.157	.110	.031		.100	.017



# 23. OUTLINE DRAWINGS

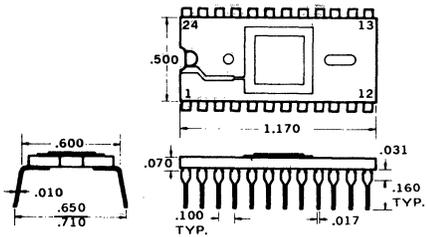
IN DRAWING NUMBER SEQUENCE

## ML127

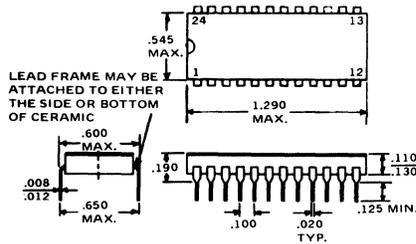


	A	B	C	D	E	F	G	H	J	K	M	N
ML127	.785 MAX	.280 MAX	.175	.130 MIN	.020	.090 MIN	.050 MAX	.015 MAX	.310 MAX	.160 MAX	.300 MAX	.008 MAX
ML127a	.290 MAX	.220 MAX	.100 MAX	.020 MIN	.090 MAX	.015 MAX	.015 MAX	.023 MAX			.290 MAX	.008 MAX
ML127b	.750 MAX	.245 MAX	.160 MAX	.125 MAX	.020 MAX	.100 MAX	.020 MAX	.015 MAX	.290 MAX			.008 MAX
ML127c	.787		.220 MAX			.100	.017		.290 MAX		.410	.009
ML127d	.760					.100	.017			.110	.300	.009
ML127e	.755 MAX	.265 MAX	.170 MAX	.100 MIN	.020 MIN	.090 MAX	.015 MAX	.016 MAX			.375	.009
ML127f	.785 MAX	.280 MAX	.270 MAX	.125 MIN	.020 MAX	.090 MAX	.050 MAX	.016 MAX	.290 MAX	.160 MAX	.360 MAX	.008 MAX
ML127g	.799 MAX		.200 MAX	.100 MAX	.014 MAX	.100 MAX	.050 MAX	.012 MAX	.324 MAX		.299	.008 MAX
ML127h	.765	.248	.175	.105 MAX	.025	.100		.015 MAX	.300	.150	.300 MIN	.008 MAX
ML127j	.750 MAX	.245 MAX	.200 MAX	.125 MIN	.020	.090 MAX	.015 MAX	.016 MAX	.325 MAX	.180 MAX	.400 MAX	.009 MAX
ML127k	.750 MAX	.265 MAX	.200 MAX	.125 MIN	.080 MAX	.090 MAX	.010 MAX		.290 MAX		.380 MAX	.009 MAX
ML127m	.785 MAX	.291 MAX		.105 MAX	.125 MAX	.020 MAX	.090 MAX	.015 MAX	.290 MAX		NOM	.011
ML127n	.783 MAX	.283 MAX	.200 MAX	.100 MIN	.020 MIN	.100	.020		.300		.315	.360
ML127p	.735 MAX	.290 MAX	.220 MAX	.100 MAX	.020 MAX	.090		.015 MAX	.290 MAX	.160 MAX	.290 MAX	.008 MAX
ML127q	.833 MAX	.280 MAX	.155 MAX	.165 MAX	.040	.110	.050	.023	.315		.315	.360
ML127r	.755 MAX	.310 MAX	.165 MAX	.125 MAX	.020 MAX	.090 MAX	.015 MAX	.015 MAX	.290 MAX	.145 MAX	.008 MAX	.014
ML127s	.750 MAX	.245 MAX	.200 MAX	.100 MAX	.015 MAX	.090 MAX	.015 MAX	.016 MAX	.290 MAX		.375	.009
ML127t	.745 MAX	.240 MAX	.155 MAX	.100 MAX	.020 MAX	.100 TYP	.015 TYP	.014 TYP	.300		NOM	.011
ML127u	.745 MAX	.240 MAX	.155 MAX	.115 MAX	.020 MAX	.100 TYP	.015 TYP	.014 TYP	.300			.008
ML127v	.820 MAX	.240 MAX	.200 MAX	.100 MAX	.015 MAX	.090 MAX		.014 MAX	.325 MAX		.300	.008
ML127w	.744 MAX	.259 MAX	.200 MAX	.125 MAX	.020	.100		.014 MAX	.290		.311	.008
	.783	.279		.149				.022	.309		.397	.013

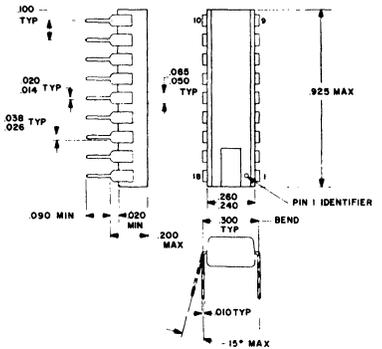
## ML128



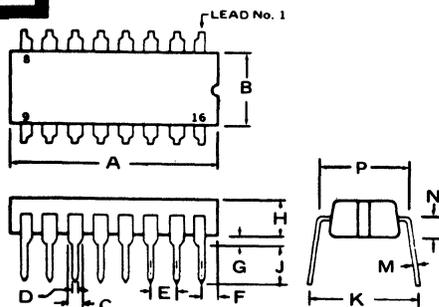
## ML130



## ML131



## ML132

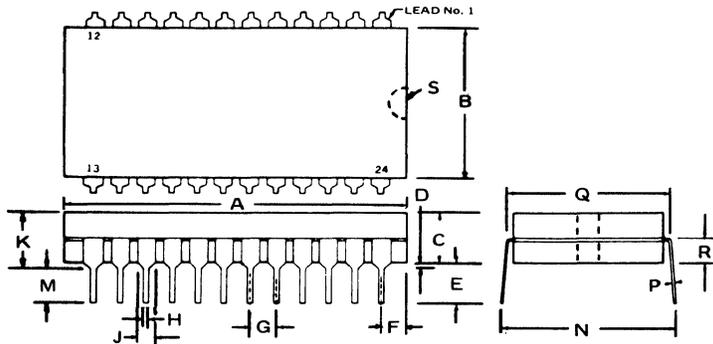


	A	B	C	D	E	F	G	H	J	K	M	N	P
ML132	.745 MAX	.245 MAX	.044 MAX	.015 MAX	.090 MAX	.020 MAX	.015 MAX	.115 MAX	.120 MAX	.325 MAX	.010 MAX	.057 MAX	.290 MAX
ML132a	.750 MAX	.235 MAX	.030 MAX	.015 MAX	.090 MAX	.030	.035 MAX	.125 MAX	.135 MAX	.375 MAX	.015 MAX	.068 MAX	.310 MAX
ML132b	.748	.251	.045	.019	.100		.019 MIN	.181 MAX	.110	.300	.010		

# 23. OUTLINE DRAWINGS

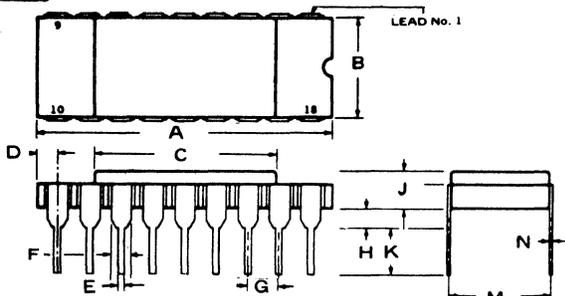
IN DRAWING NUMBER  
SEQUENCE

**ML133**



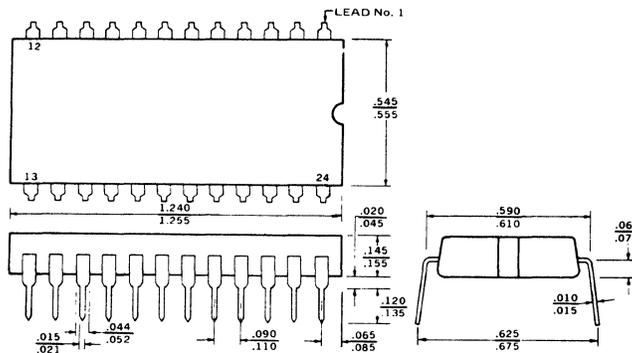
	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	S
ML133	1.235 MAX	.560 MAX	.200 MIN	.125 .140	.060 .100	.090 .110	.015 .023	.076 MAX				.600 .650	.008 .014	.590 .610	.100	NA
ML133a	1.290 MAX	.515 .525	.020 .070	.060 .100	.090 .110	.016 .020	.050 .060	.200 MAX	.125 MIN	.660 .710	.008 .012	.590 .620	.160 MAX	.025 RAD		
ML133b	1.200 MAX	.300 .600	.170 .200	.020 .050	.120 .200	.090 .110	.015 .023	.045 .070	.220 MAX	.100 .150	.600 .730	.008 .012	.550 .650	.070 .110		
ML133c	1.230 MAX	.510 .530	.140 .180	.015 MIN	.115 MIN	.090 .110	.014 .023	.040 .065	.220 MAX	.100 MIN	.600 .700	.008 .015	.625 MAX		YES	
ML133d	1.33 MAX	.531 MIN	.019 MIN			.100		.059 MAX	.199 MIN	.100			.009	.598		
ML133e	1.260 MAX	.600	.020 MIN		.080 MAX	.100	.018 TYP	.060 TYP	.200 MIN	.120	.625	.010 TYP				NA
ML133f	1.30 MAX	.520 MAX	.205 MAX	.020		.100	.016 MIN	.055 MIN	.225 MAX	.125 MIN	.600	.006 .014				.059

**ML134**

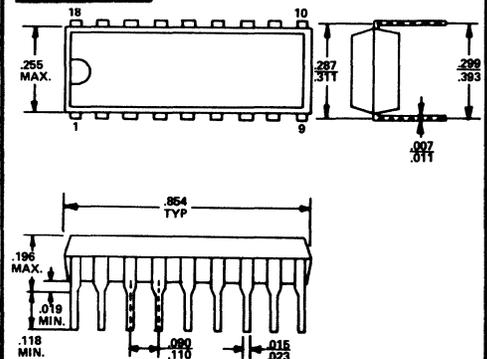


	A	B	C	D	E	F	G	H	J	K	M	N
ML134	.890 .930	.280 .310	.420 .470	.030 .065	.015 .021	.045 .060	.090 .110	.030 .070	.080 .120	.100 .140	.290 .320	.008 .012
ML134a	.921 MAX	.294 MAX	.059 MAX	.053	.020	.100	.029		.147 MAX	.133 .153	.299 .300	.009 .008
ML134b	.890 .910	.278 .298		.015 .021	.090 .110	.025 .045	.030 .045	.080 MAX	.105 .155	.135 .155	.300 .300	.008 .012
ML134c	.890 .910	.278 .298		.020 TYP	.040 TYP	.095 .105	.040 .060		.170 .190	.300 .320	.010 TYP	
ML134d	.890 .915	.280 .300		.035 .065	.015 .021	.054	.100	.020 .045	.155 MAX	.125 .150	.300 .300	.008 .012
ML134e	.890 .920	.300 .320		.018 .020	.051 .057	.090 .110	.025 .035	.095 .105	.125 .135	.300 .330	.009 .011	
ML134f	.900	.285		.016 .023	.043 .060	.100 TYP	.040 TYP	.150	.125	.300	.008 .012	
ML134g	.890 .910	.286 .294		.016 .020	.054 TYP	.100	.025 .045	.085 .105	.120 .140	.300 .350	.009 .011	
ML134h	.870 .910	.250 .300		.014 .024	.052 .054	.090 .110	.015 MIN	.140 .170	.100 MIN	.300 .400	.012	

**ML135**



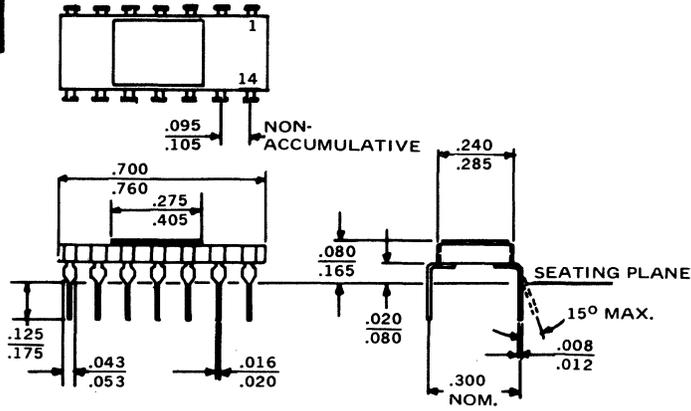
**ML136**



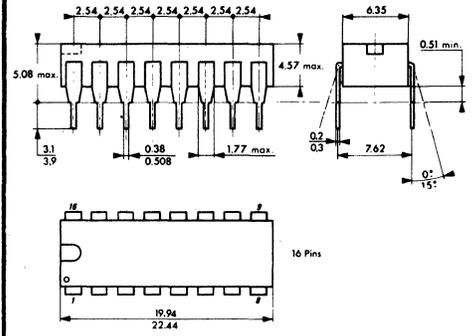
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

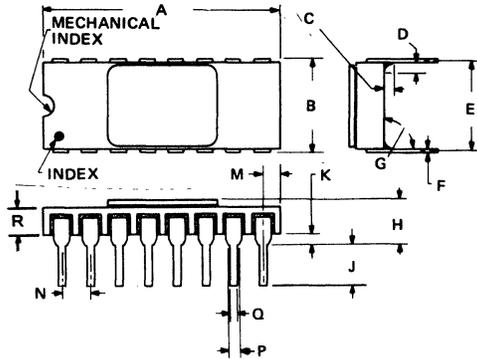
**ML137**



**ML139**

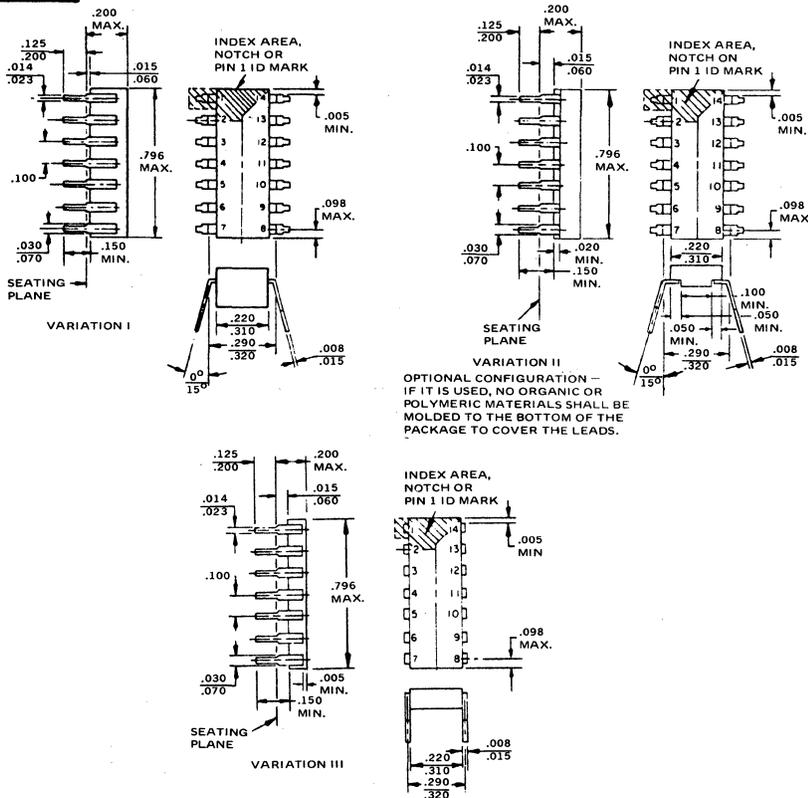


**ML140**



	A	B	C	D	E	F	G	H	J	K	M	N	P	Q	R	REMARK
ML140	.830	.295	.020	.040	.290	.008	90°	.200	.125	.020	.015	.090	.027	.015		
	MAX	.325	MAX	MAX	.310	.012	105°	MAX	.150	MIN	.090	.110	.030	.023		
ML140a	.920	.320			.300			MAX	.180	.018			.092	.015		
	MAX	MAX							MAX	MIN			.108	.019		
ML140b	.790	.285			.280	.008		.170	.140	.020	.032	.100	.030	.015		
	.820	.315			.300	.014		MAX	MIN	MIN	.072		TP	MIN	.021	
ML140c	.800	.310			.340	.010	90°	.150	.130	.035		.100	.054	.013		
	.810	.316			TYP	.011	105°	MAX	.150	.045			TYP	.020		
ML140d	.795				.279	.008	90°	.133	.125	.035	.051			.314	.017	
	.807				.318	.012		MAX	MIN	.043				.018		
ML140e	.780	.275			.290	.008	90°	.160	.120	.020	.050	.090	.032	.015	.070	NO INDEX
	.800	.291			.310	.012	99°	MAX	.140	.043		.110		.023	.068	DOT
ML140f	.792	.270			.290	.008	90°	.200	.120	.020	.087	.090	.032	.015	.067	INDEX DOT
	.808	.291			.310	.012	99°	MAX	.140	.045	MAX	.110		.023	.064	
ML140g	.780	.275			.280	.008		.200	.100	.015		.090	.032	.014		
	.820	.310			.380	.015		MAX	MIN	MIN		.110	TYP	.023		

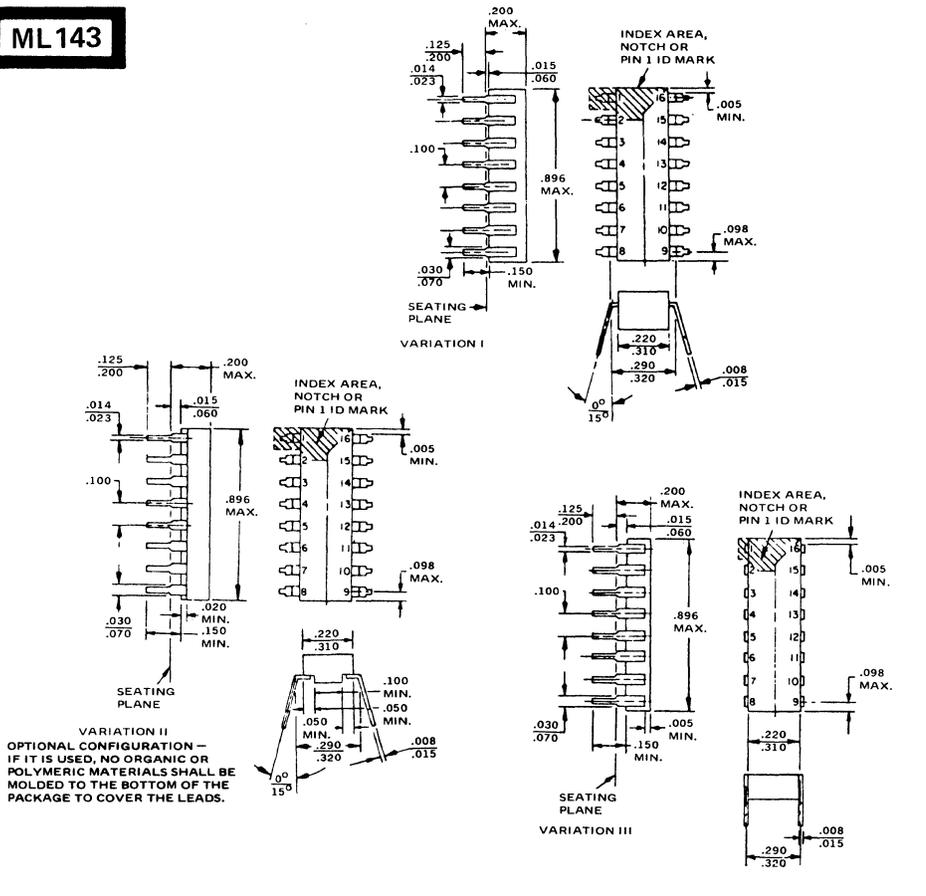
**ML142**



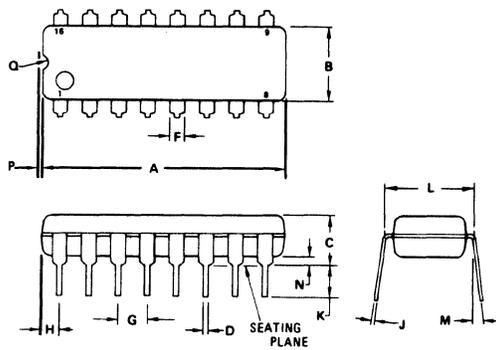
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**ML143**



**ML145**

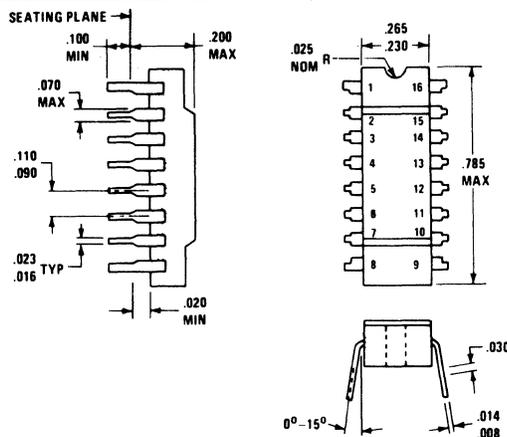


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	20.70	21.34	0.815	0.840
B	6.10	6.60	0.240	0.260
C	4.06	4.57	0.160	0.180
D	0.38	0.51	0.015	0.020
F	1.02	1.52	0.040	0.060
G	2.54 BSC		0.100 BSC	
H	1.32	1.83	0.052	0.072
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.37	7.87	0.290	0.310
M	- 10°		- 10°	
N	0.51	1.02	0.020	0.040
P	0.13	0.38	0.005	0.015
Q	0.51	0.76	0.020	0.030

NOTES:

- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
- DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL

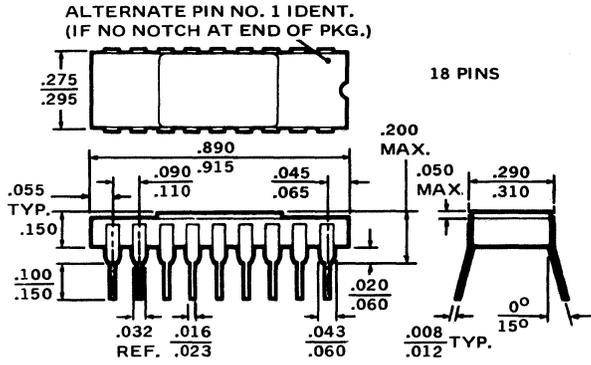
**ML146**



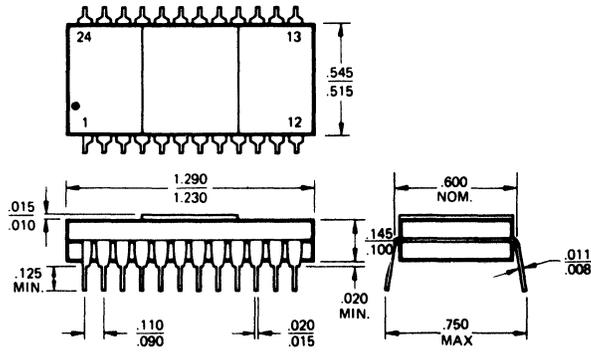
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

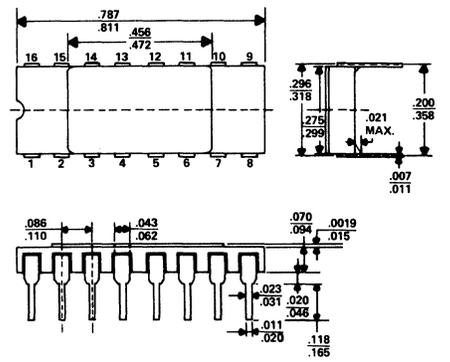
**ML147**



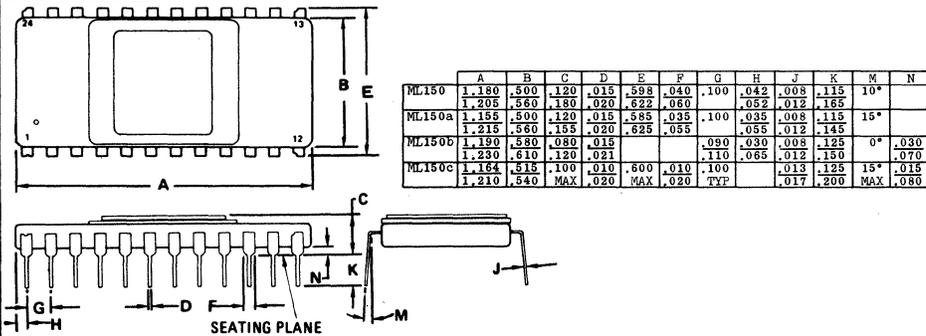
**ML148**



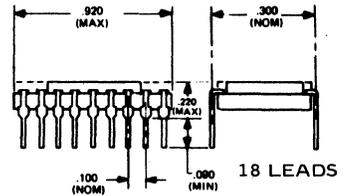
**ML149**



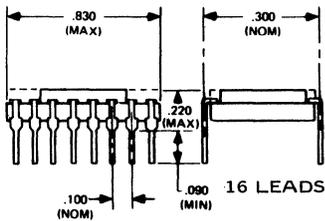
**ML150**



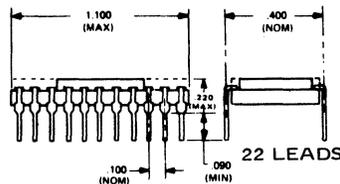
**ML152**



**ML153**



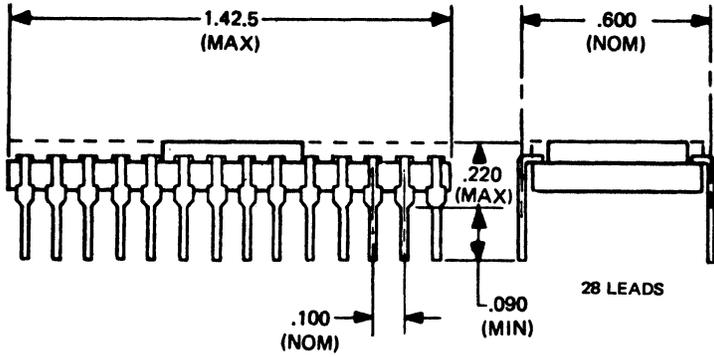
**ML154**



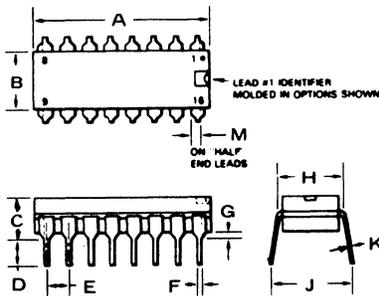
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

**ML155**

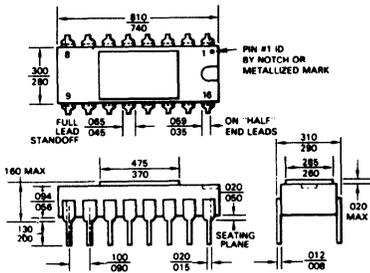


**ML157**

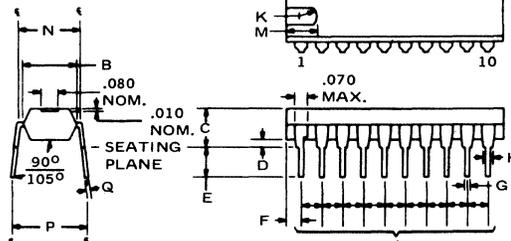


	A	B	C	D	E	F	G	H	J	K	M
ML157	.750	.240	.200	.100	.090	.016	.015	.300	.375	.008	.035
	.865	.260	MAX	.150	.110	.020	.050			.012	.055
ML157a	.750	.240	.200	.100	.100	.015	.020	.295		.008	
	.785	.255	MAX	MIN		.021	.040	.350		.012	
ML157b	.745	.240	.120	.125	.100	.014	.020	.300	.300	.008	
	.785	.250	.200	.150		.020	.065	.325		.012	
ML157c	.740	.250	.200	.100	.090	.014	.015	.325	.300	.008	
	.780	.300	MAX	MIN	.110	.023	MIN	MAX	.400	.015	
ML157d	.745	.245	.200	.100	.090	.016	.015	.290	.375	.009	
	.785	.271	MAX	.165	.110	.020	MIN	.310	NOM	.011	

**ML158**

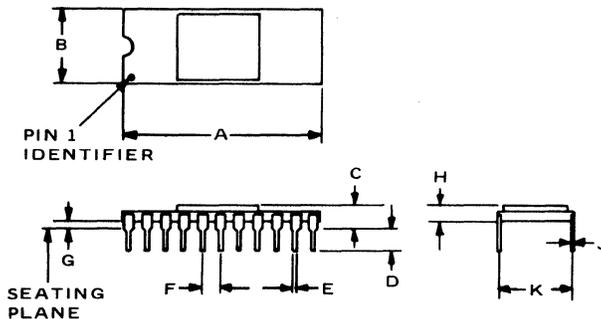


**ML161**



	A	B	C	D	E	F	G	H	J	K	M	N	P	Q
ML161	1.070	.240	.200	.020	.125	.075	.015	.033	.100	.093	.160	.290	.300	.008
	MAX	.260	MAX	MIN	.155	MAX	.021	MIN				.310	.350	.014
ML161a	1.015	.255	.200		.125		.018	.045	.090			.290	.325	
	1.025	.265	MAX		MIN		.022	.065	.110			.310	.375	
ML161b	.990	.300	.190											
	MAX	MAX	MAX											
ML161c	1.010	.300	.175											
	MAX	MAX	MAX											

**ML162**

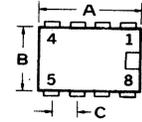


	A	B	C	D	E	F	G	H	J	K
ML162	1.06	.395	.180	.090	.015	.090	.020	.125	.008	.400
	1.09	.425	MAX	.130	.021	.110	.050	MAX	.012	REF
ML162a	1.050	.380	.170	.100	.014	.090	.015	.065	.008	.380
	1.100	.400	MAX	MIN	.023	.110	MIN	.135	.015	.480

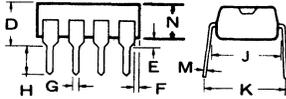
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

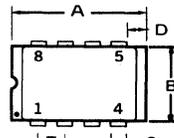
**ML163**



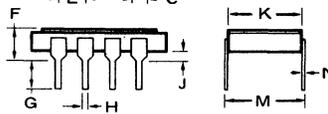
	A	B	C	D	E	F	G	H	J	K	M	N
ML163	.375	.245	.090	.200	.020	.010	.018	.120	.290	.325	.009	
	.385	.255	.110	MAX	TYP	MIN	.022	MIN	.310	.375	.011	
ML163a	.370	.245		.145	.020			.125	.305	.300	.009	.128
	.390	.252		MIN				MIN	.315	.350	.011	.138



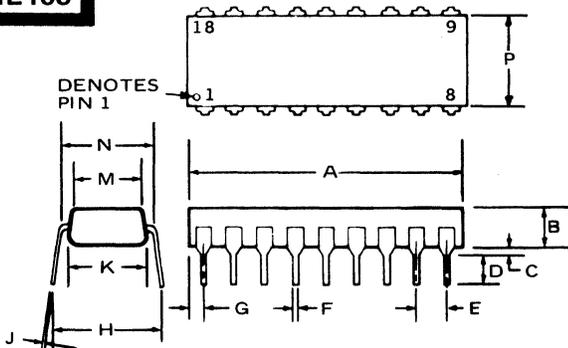
**ML164**



	A	B	C	D	E	F	G	H	J	K	M	N
ML164	.530		.040	.010	.090	.105	.125	.017	.020	.290	.290	.008
	MAX		.060	MIN	.110	.155	.200	.023	.080	.310	.320	.015
ML164a	.535	.320			.095	.194	.130		.015	.300	.290	.015
	MAX	MAX			.105	MAX	TYP		MIN	TYP	.400	

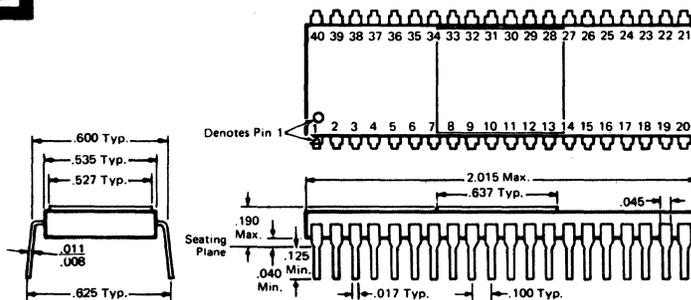


**ML165**



	A	B	C	D	E	F	G	H	J	K	M	N	P
ML165	.920	.180	.020	.120	.100	.018	.050	.350	.009	.250	.220	.300	
	MAX	MAX	MIN	MIN	TYP	TYP	TYP	TYP	.011	TYP	TYP	TYP	
ML165a	.915	.115	.015	.100	.090	.015	.060	.325	.010			.290	
	.925	.125	.035	.105	.110	.020	.070	.375	.015			.310	
ML165b	.865		.040	.130	.092	.020	.020		.010		.235	.300	.250
	MAX		TYP	TYP	TYP	TYP	TYP		TYP			TYP	
ML165c	.897		.019	.100	.100	.018							.300
	TYP		MIN	TYP	TYP								TYP
ML165d	.900	.140	.030	.110	.100	.020						.290	.250
					TYP	TYP							

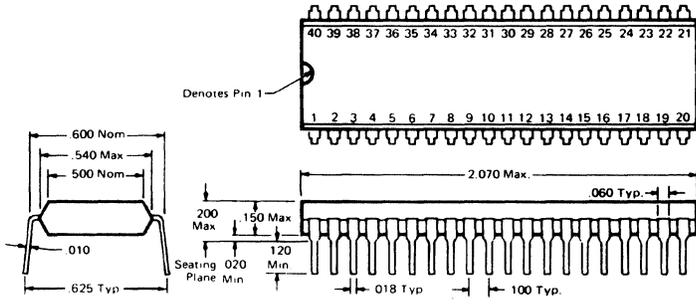
**ML166**



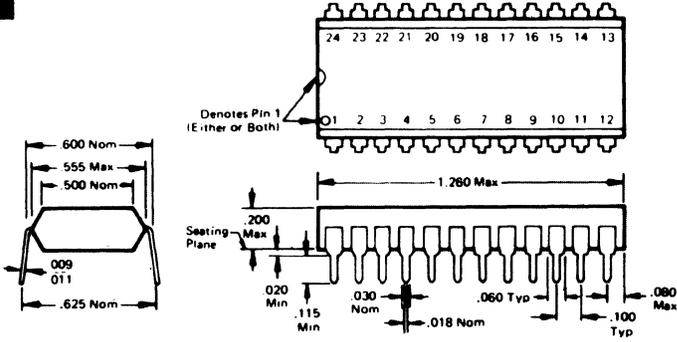
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

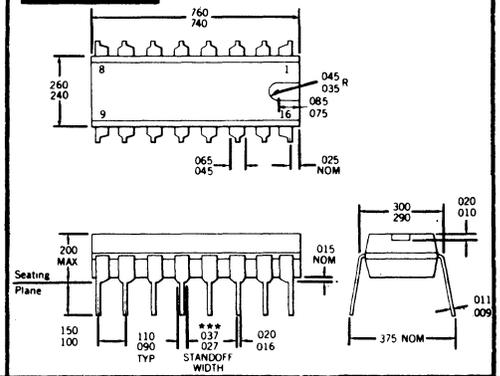
**ML167**



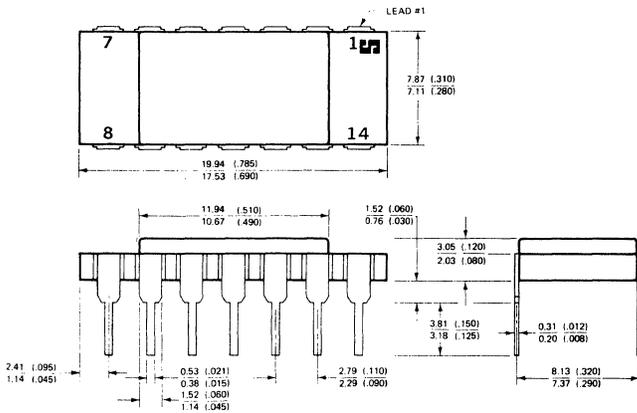
**ML168**



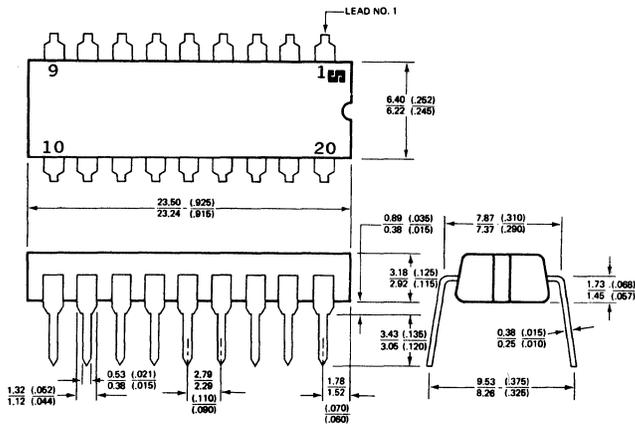
**ML170**



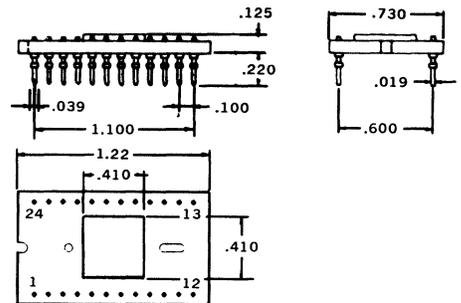
**ML171**



**ML172**



**ML173**

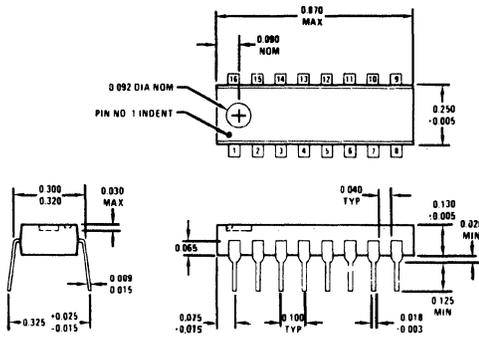




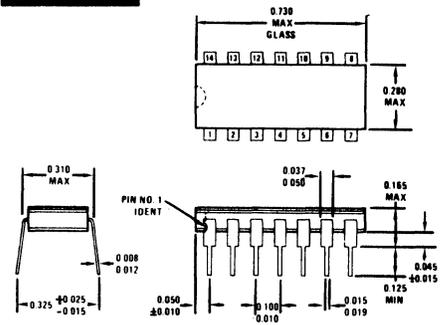
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

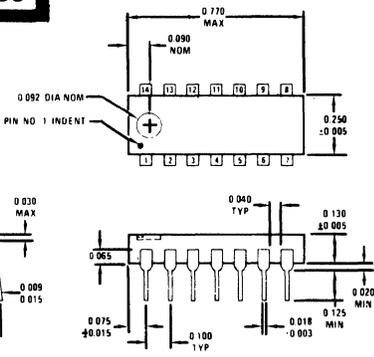
**ML178**



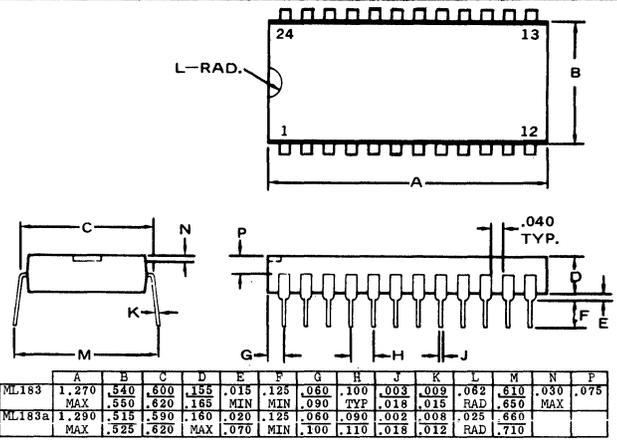
**ML179**



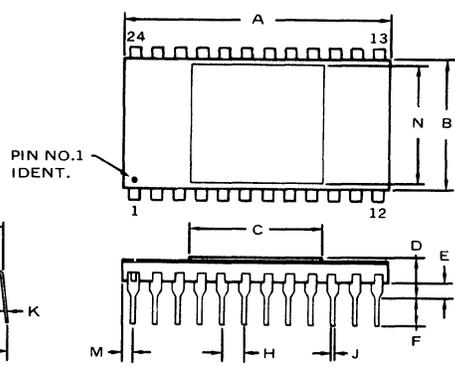
**ML180**



**ML183**

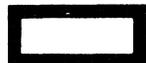
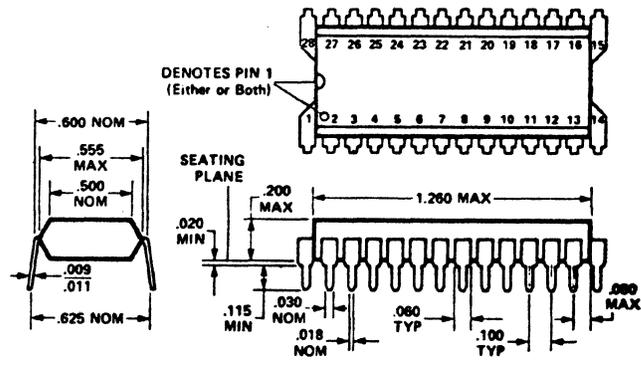


**ML184**



	A	B	C	D	E	F	G	H	J	K	L	M	N	P
ML184	1.290	.550	.610	.200	.040	.125	.610	.090	.016	.008		.060	.530	.610
	MAX	MAX	MAX	MAX	.060	MIN	MAX	.110	.020	.012		.090	MAX	.650
ML184a	1.220	.595	.520	.165	.020	.125	.600	.090	.015		.050			
	MAX	MAX	SQUARE	MAX	.060	MIN	REF	.110	.020		TYP			
ML184b	1.230	.600		.160	.040	.125		.100	.018	.010		.070		.625
	MAX	TYP		MAX	MIN	MIN		TYP	TYP	.012		MAX		TYP

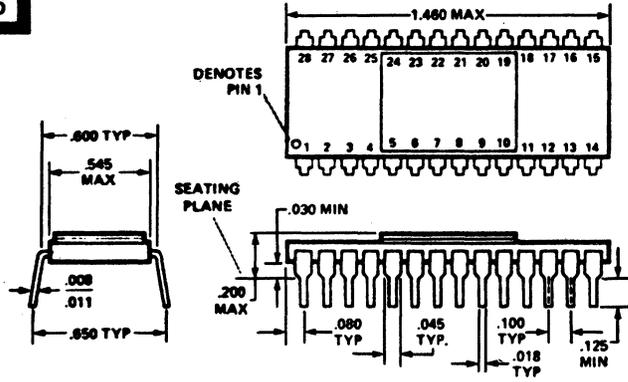
**ML185**



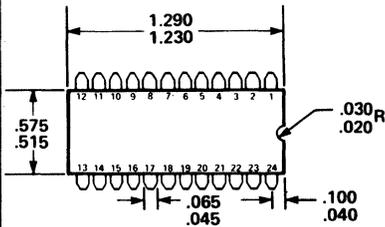
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

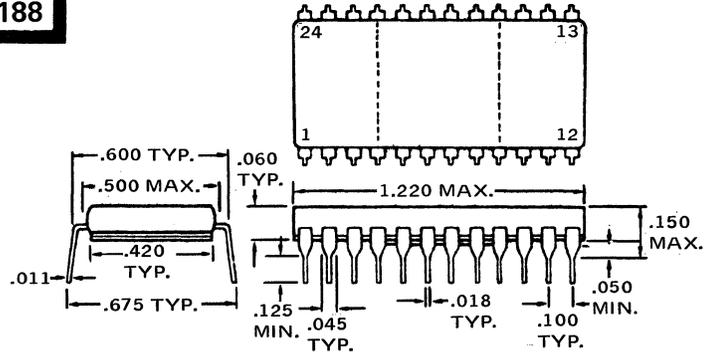
**ML186**



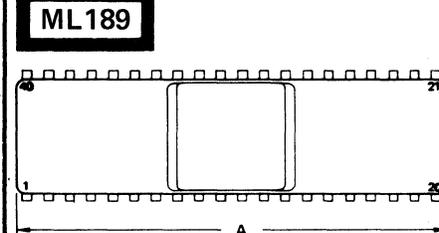
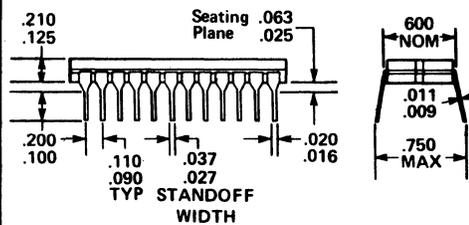
**ML187**



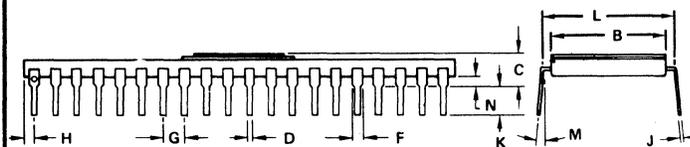
**ML188**



**ML189**

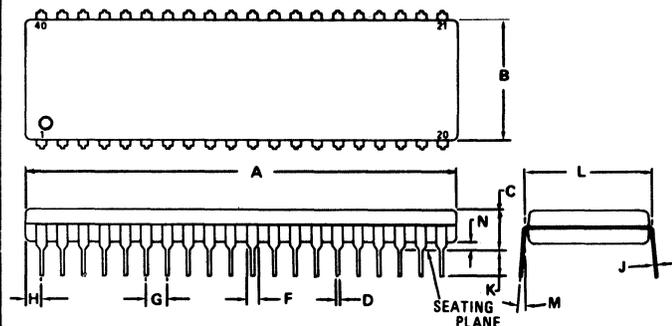


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	50.04	51.05	1.970	2.010
B	13.46	14.22	0.530	0.560
C	3.05	3.94	0.120	0.155
D	0.38	0.51	0.015	0.020
F	0.89	1.40	0.035	0.055
G	2.54 BSC		0.100 BSC	
H	0.89	1.40	0.035	0.055
J	0.20	0.28	0.008	0.011
K	3.05	3.68	0.120	0.145
L	14.86	15.87	0.585	0.625
M	15°		15°	
N	0.51	1.14	0.020	0.045



NOTES:  
1. LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.  
2. DIMENSION "L" TO INSIDE OF LEADS (MEASURED 0.51 mm (0.020) BELOW PACKAGE BASE)

**ML190**

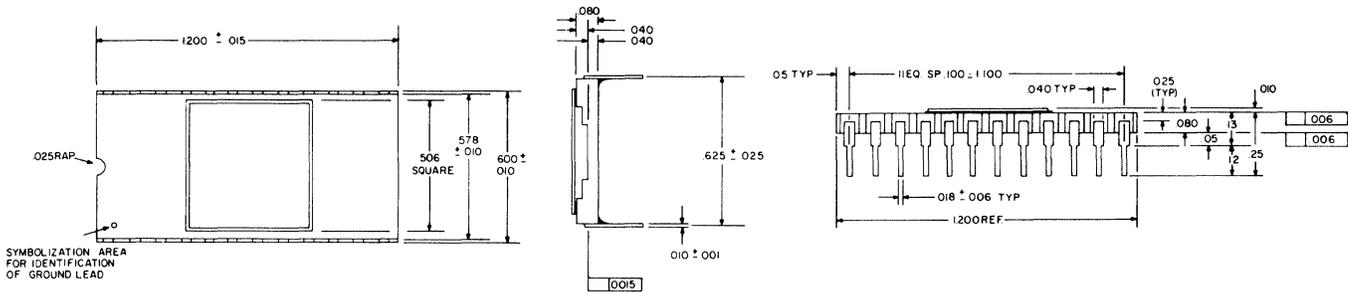


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	51.82	52.32	2.040	2.060
B	13.72	14.22	0.540	0.560
C	4.57	5.08	0.180	0.200
D	0.36	0.51	0.014	0.020
F	1.02	1.52	0.040	0.060
G	2.41	2.67	0.095	0.105
H	1.65	2.16	0.065	0.085
J	0.20	0.30	0.008	0.012
K	3.68	4.19	0.145	0.165
L	14.99	15.49	0.590	0.610
M	10°		10°	
N	0.51	1.02	0.020	0.040

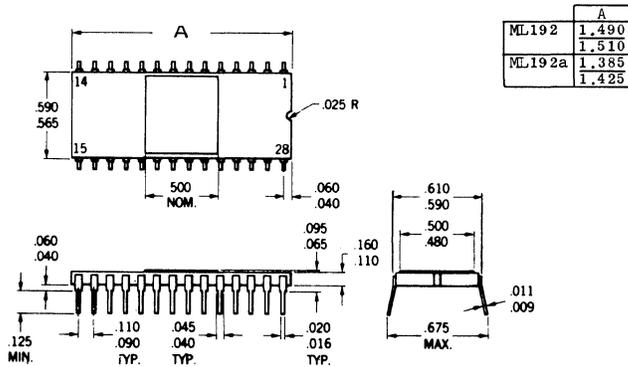
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

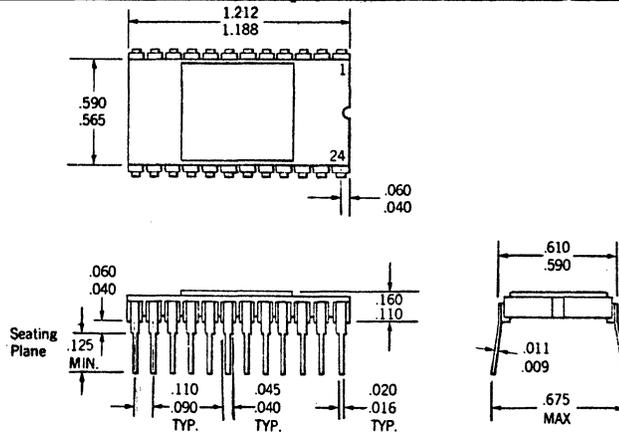
**ML191**



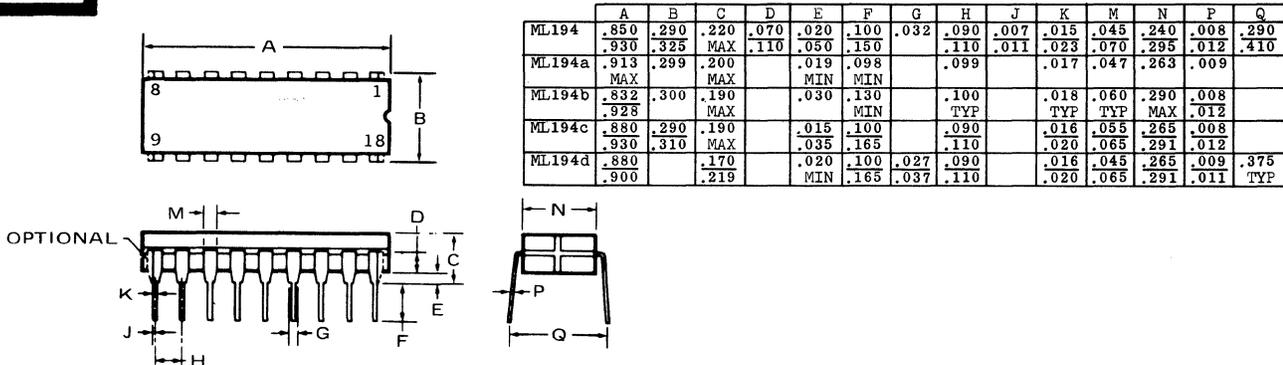
**ML192**



**ML193**



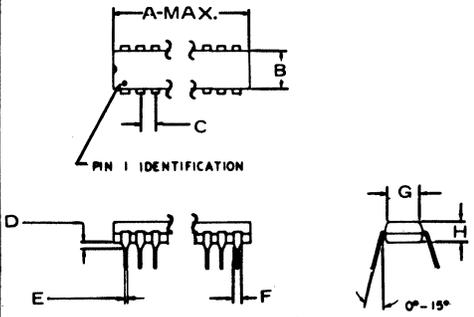
**ML194**



# 23. OUTLINE DRAWINGS

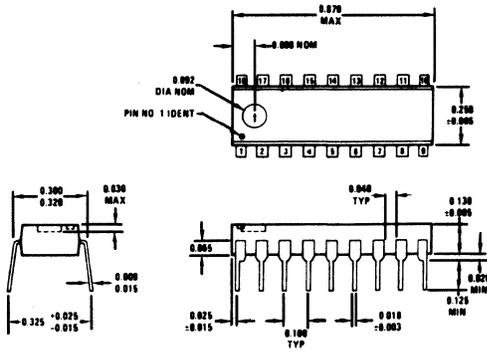
IN DRAWING NUMBER SEQUENCE

**ML195**

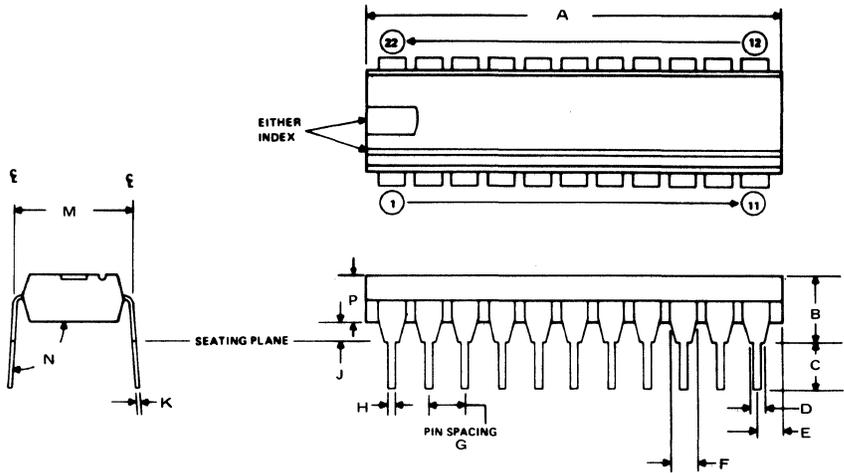


	A	B	C	D	E	F	G	H	No. OF PINS
ML195	2.40	.625	.100	.015	.017	.050	.516	.145	48
	MAX			TYP	TYP	TYP			

**ML196**

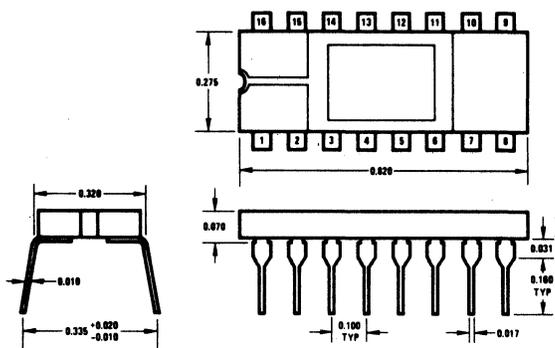


**ML197**



	A	B	C	D	E	F	G	H	J	K	M	N	P	REMARKS
ML197	1.120	.125	.035	.085	.090	.015	.020	.009	.400	.125	DOT AND NOTCH			
	MAX	MIN	.085	.110	.081	MIN	.420							
ML197a	1.100	.200	.125	.033	.030	.060	.100	.015	.020	.008	.390	90°		EITHER INDEX
	MAX	MAX	MIN	MIN	.070			.021	MIN	.014	.410	105°		

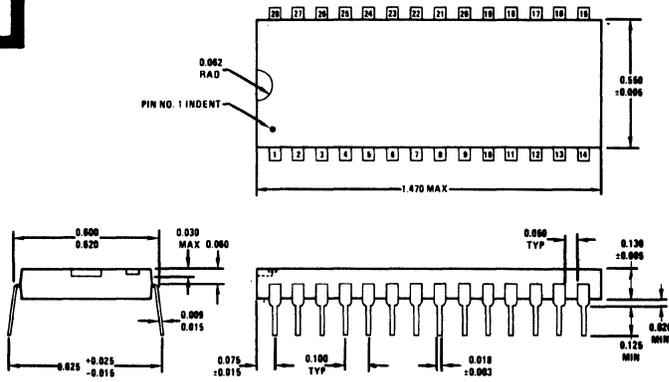
**ML198**



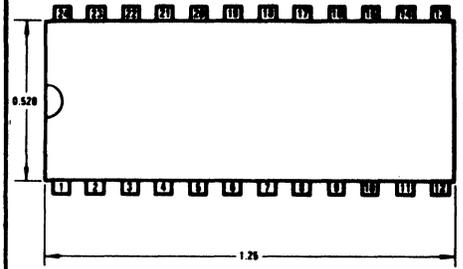
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

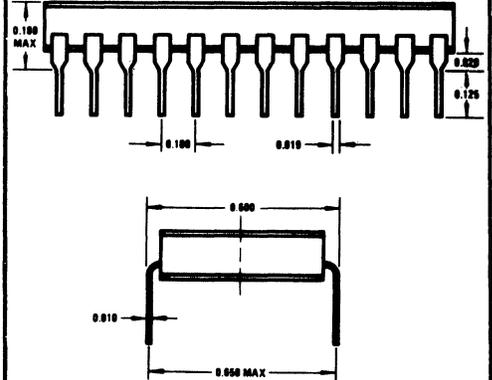
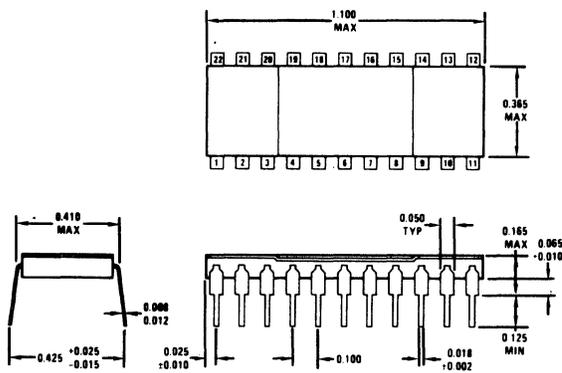
**ML199**



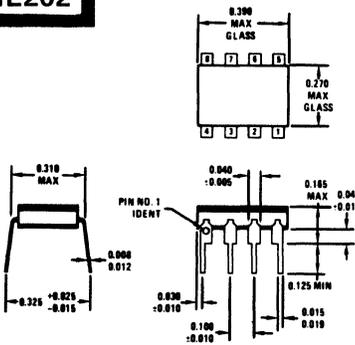
**ML200**



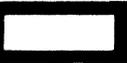
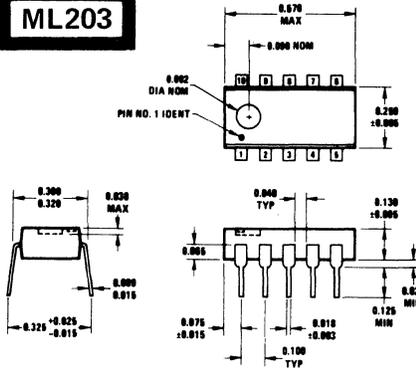
**ML201**



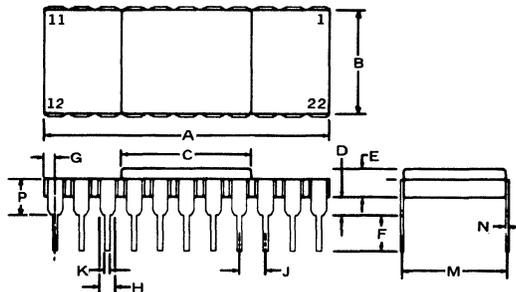
**ML202**



**ML203**



**ML204**

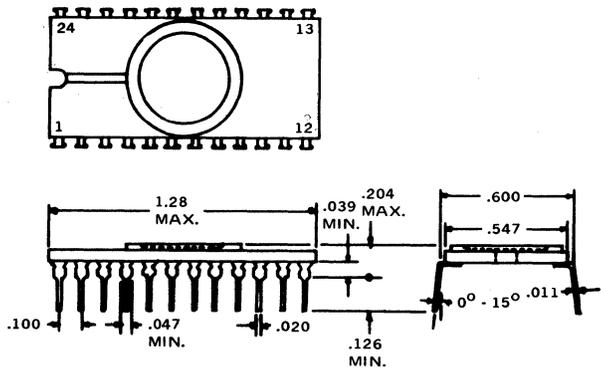


	A	B	C	D	E	F	G	H	J	K	M	N	P
ML204	1.040	.380	.490	.030	.080	.125	.030	.045	.090	.015	.390	.008	
	1.100	.410	.510	.070	.120	.150	.065	.060	.110	.021	.420	.012	
ML204a	1.100	.395	.450	.020	.080	.125		.050	.090	.017	.400	.008	.165
	MAX	MAX	MAX	.060	.120	MIN		TYP	.110	.023		.012	MAX

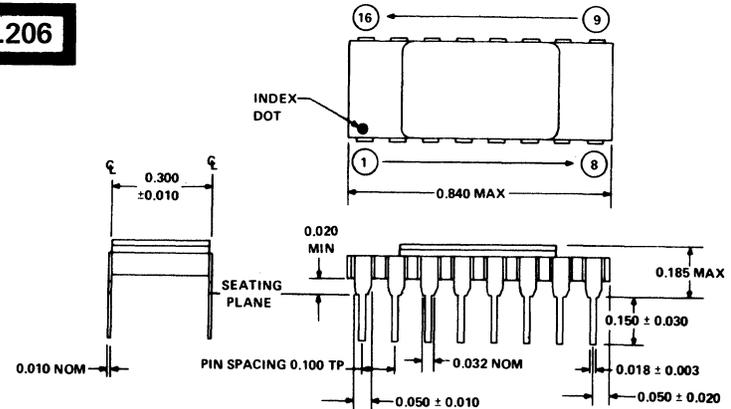
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

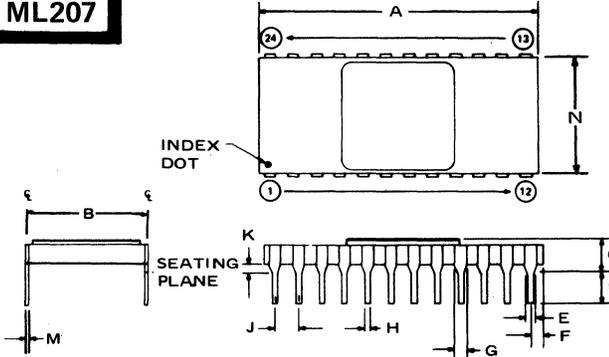
**ML205**



**ML206**

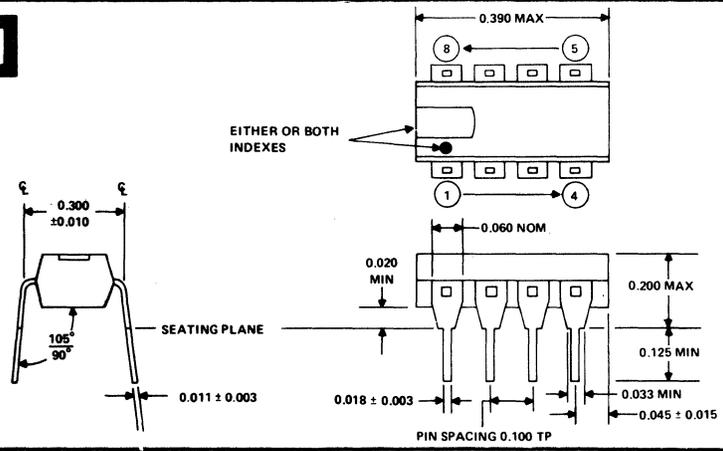


**ML207**



	A	B	C	D	E	F	G	H	J	K	M	N	REMARKS
ML207	1.290	.590	.185	.120	.032	.030	.040	.015	.100	.020	.010		DOT
	MAX	.610	MAX	.180	NOM	.070	.060	.021	TYP	MIN	NOM		
ML207a	1.185	.600	.196	.118			.038	.015	.090	.040	.008		DOT
	MAX		MAX	.141			.044	.023	.109	.059	.012		
ML207b	1.180	.585	.120	.100		.030		.015	.100	.020	.008		DOT
	MAX	.605	.165	.165		.070		.021	TYP	.060	.012		
ML207c	1.150	.600	.080	.125	.043			.014	.095	.020	.008		DOT
	MAX			.175	.053			.020	.105	.080	.012		
ML207d	1.290	.600		.125		.040	.050	.016	.090	.020	.008		DOT
	MAX			MIN		.060	TYP	.020	.110	.060	.012		
ML207e	1.186	.590	.200	.120		.035	.036	.015	.090	.040	.008	.578	
	MAX	.610	MAX	.140		.055	.044	.023	.060	.060	.012	.598	
ML207f	1.212		.160	.125		.050	.040	.020	.100	.040	.009		
	MAX		MAX	MIN		TYP	TYP	TYP	TYP	MIN	.011		
ML207g	1.178	.600	.199	.100			.050	.014	.090	.019	.007	.586	NOTCH
	MAX		MAX	MIN				.022	.109	.011			
ML207h	1.178	.600	.199	.100			.050	.014	.090	.019	.007	.586	NOTCH AND CONN
	MAX		MAX	MIN				.022	.109	MIN	.011		
ML207j	1.188	.600	.132	.160	.040			.016	.098	.040			NOTCH AND CONN
	MAX		MAX	MIN	TYP			.020	.102	.060			

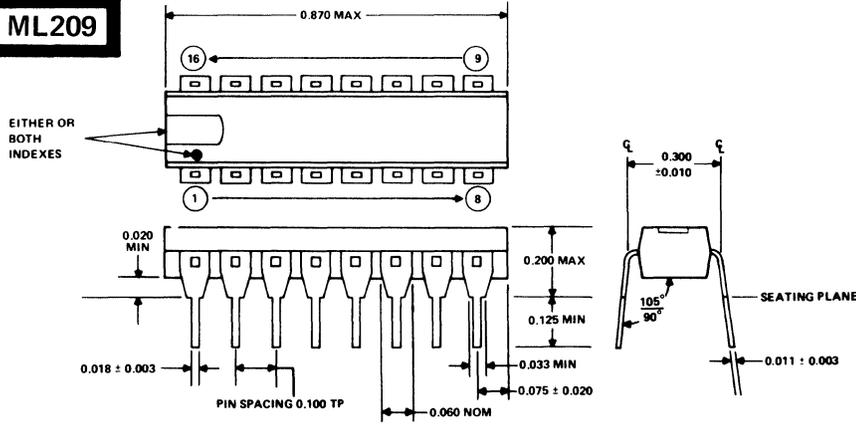
**ML208**



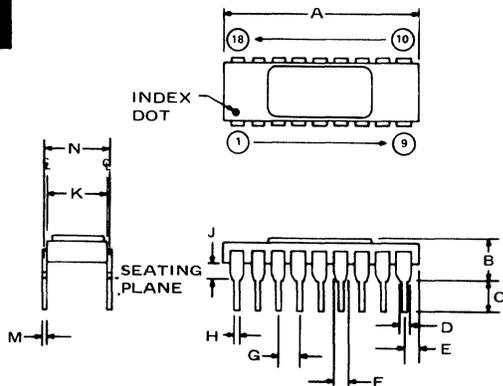
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

**ML209**

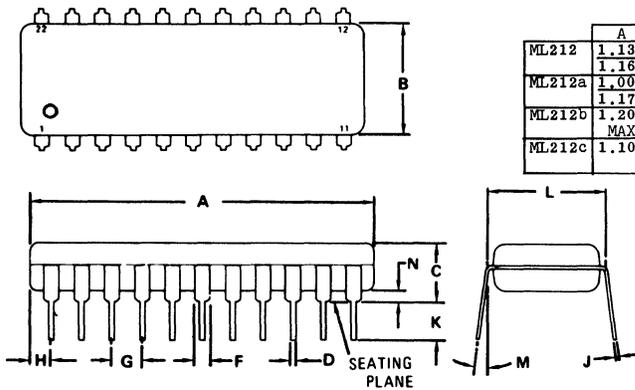


**ML210**



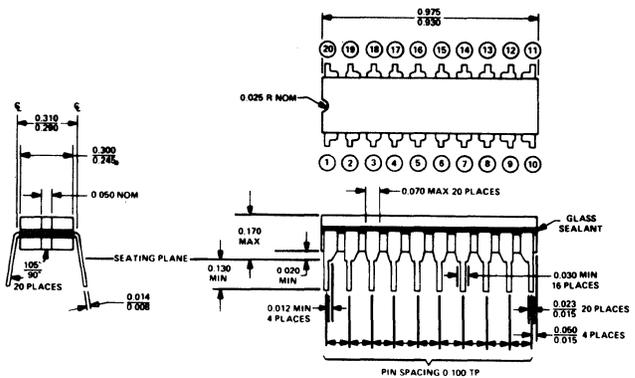
	A	B	C	D	E	F	G	H	J	K	M	N
ML210	.910 MAX	.185 MAX	.120 MAX	.032 MAX	.030 MAX	.040 MAX	.100 TYP	.015 TYP	.020 MIN	.290 MAX	.010	
ML210a	.880 MAX	.168 MAX	.123 MAX				.090 MAX	.018 MAX	.025 MAX		.008 MAX	.300 MAX
ML210b	.890 MAX	.180 MAX	.090 MAX					.015 MAX	.025 MAX	.278 MAX	.008 MAX	.300 MAX
ML210c	.910 MAX	.130 MAX					.045 MAX	.090 MAX	.015 MAX	.020 MAX	.008 MAX	.290 MAX
	.920 MAX	.160 MAX	.170 MAX				.065 MAX	.110 MAX	.020 MAX	.050 MAX	.012 MAX	.310 MAX

**ML212**



	A	B	C	D	F	G	H	J	K	L	M	N
ML212	1.135 MAX	.340 MAX	.180 MAX	.014 MAX	.040 MAX	.095 MAX	.070 MAX	.008 MAX	.120 MAX	.380 MAX	0*	.020 MAX
ML212a	1.000 MAX	.340 MAX	.170 MAX	.014 MAX	.030 MAX	.095 MAX	.040 MAX	.008 MAX	.100 MAX	.380 MAX	0*	.020 MAX
ML212b	1.170 MAX	.360 MAX	.200 MAX	.022 MAX	.060 MAX	.105 MAX	.080 MAX	.012 MAX	.140 MAX	.400 MAX	15*	.040 MAX
ML212c	1.200 MAX	.400 MAX	.190 MAX									
	1.100 MAX	.345 MAX		.020 TYP	.035 TYP	.100 TYP			.100 MIN	.400 MAX		.035 TYP

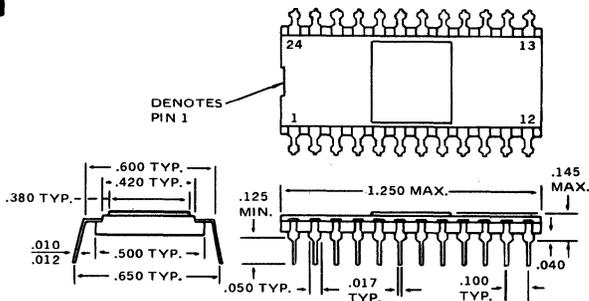
**ML213**



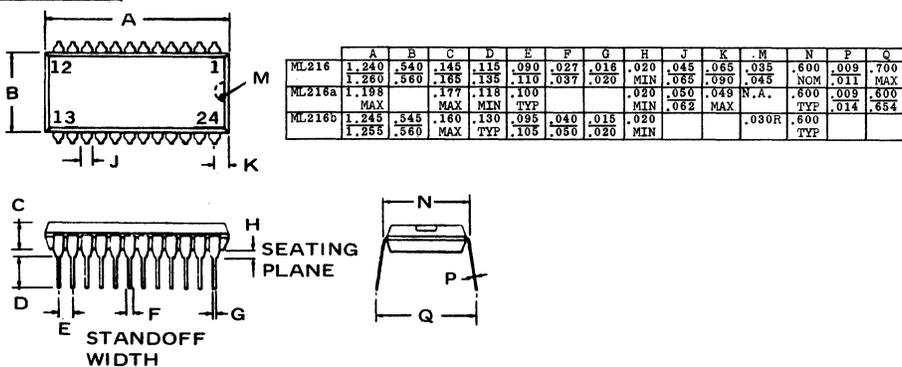
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

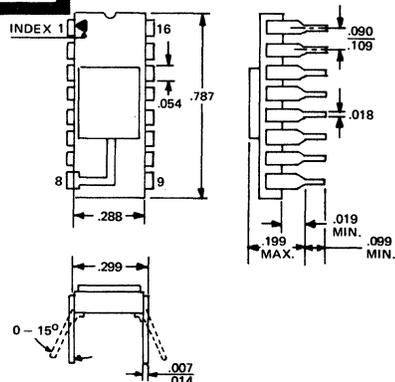
**ML214**



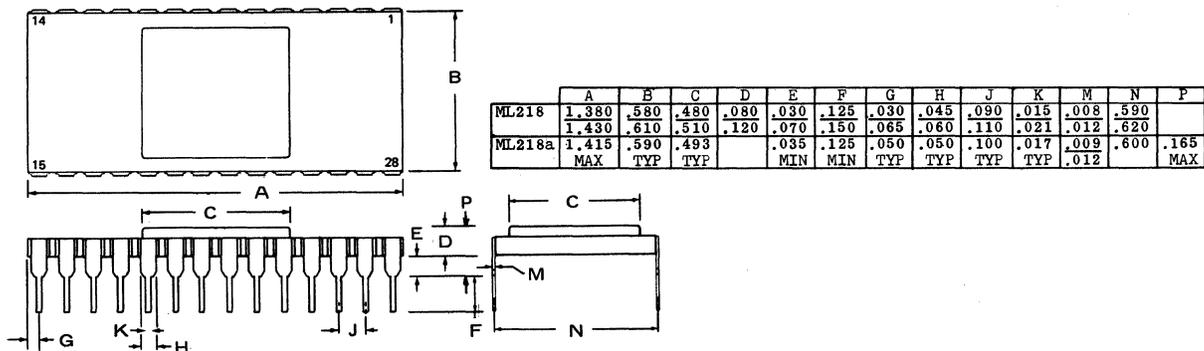
**ML216**



**ML217**



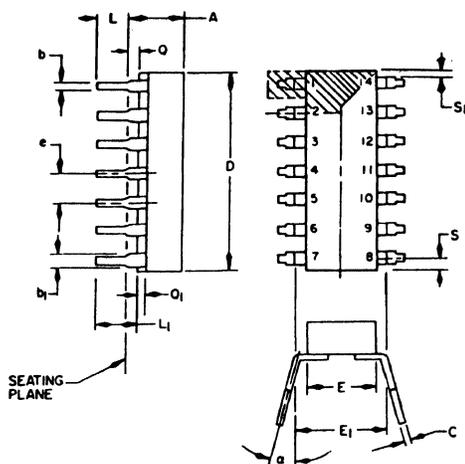
**ML218**



# 23. OUTLINE DRAWINGS

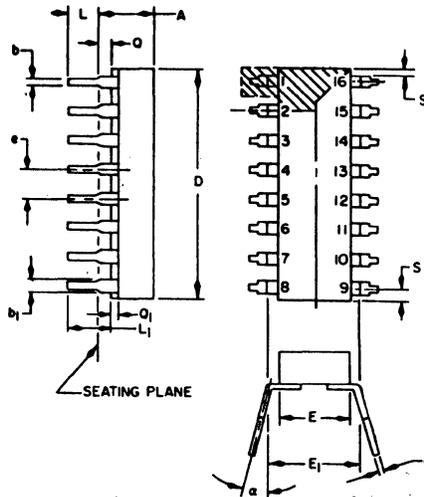
IN DRAWING NUMBER  
SEQUENCE

**ML219**



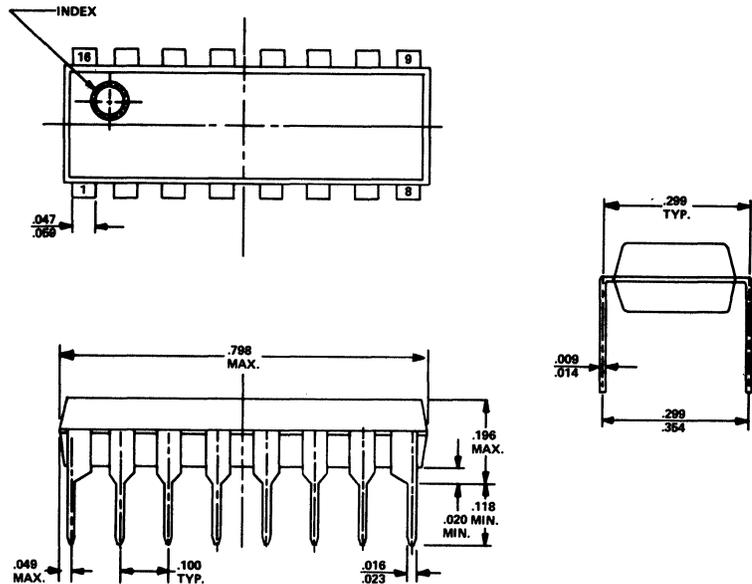
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A		.200		5.08
b	.014	.023	.36	.58
b <sub>1</sub>	.030	.070	1.02	1.78
c	.008	.015	.20	.38
D		.796		20.22
E	.220	.310	5.59	7.87
E <sub>1</sub>	.290	.320	7.37	8.13
e	.100 BSC		2.54 BSC	
L	.125	.200	3.18	5.08
L <sub>1</sub>	.150		3.81	
Q	.015	.060	.38	1.52
Q <sub>1</sub>	.020		.51	
S		.098		2.49
S <sub>1</sub>	.005		.13	
α	0°	15°	0°	15°

**ML220**



SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A		.200		5.08
b	.014	.023	.36	.58
b <sub>1</sub>	.030	.070	1.02	1.78
c	.008	.015	.20	.38
D		.896		22.76
E	.220	.310	5.59	7.87
E <sub>1</sub>	.290	.320	7.37	8.13
e	.100 BSC		2.54 BSC	
L	.125	.200	3.18	5.08
L <sub>1</sub>	.150		3.81	
Q	.015	.060	.38	1.52
Q <sub>1</sub>	.020		.51	
S		.098		2.49
S <sub>1</sub>	.005		.13	
α	0°	15°	0°	15°

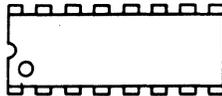
**ML221**



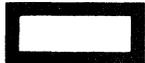
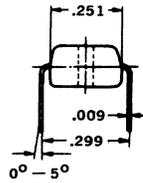
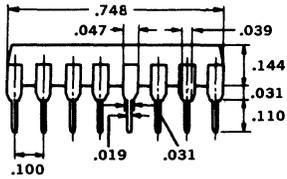
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

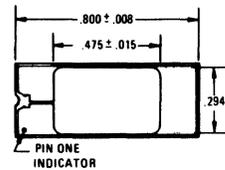
**ML222**



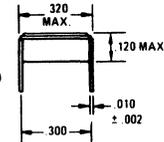
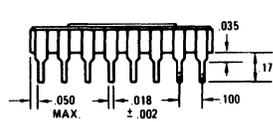
16 PINS



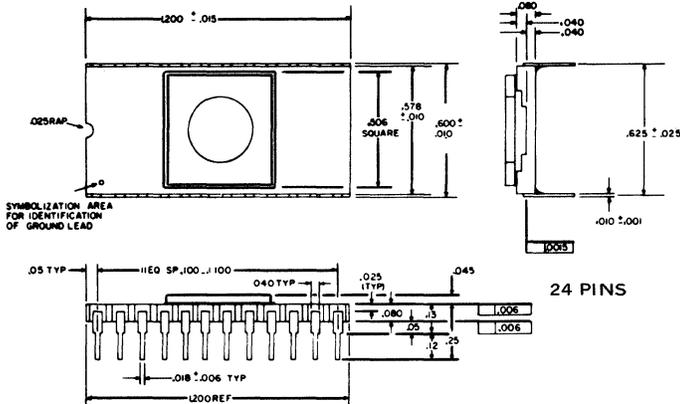
**ML223**



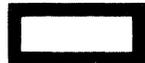
16 PINS



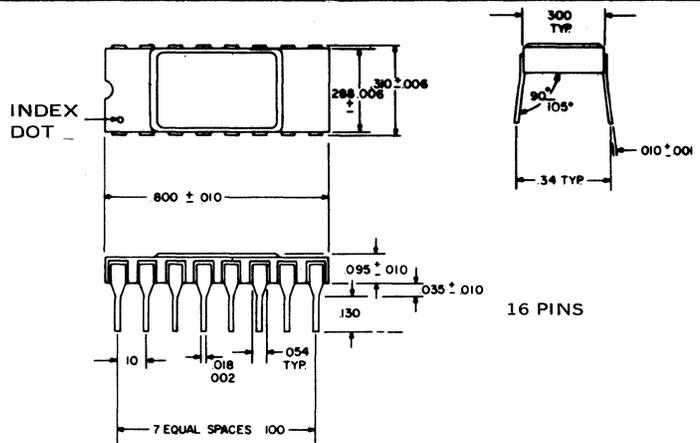
**ML224**



24 PINS



**ML225**

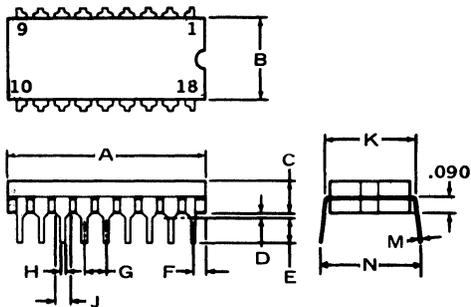


16 PINS

# 23. OUTLINE DRAWINGS

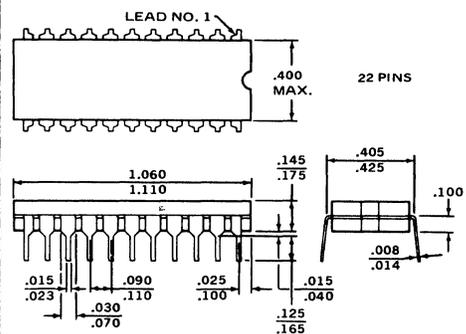
IN DRAWING NUMBER  
SEQUENCE

**ML226**

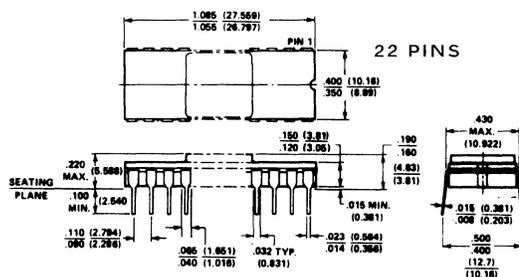


	A	B	C	D	E	F	G	H	J	K	M	N
ML226	.882	.310	.145	.015	.120	.018	.090	.015	.030	.290	.008	
	.925	MAX	.175	.040	.165	.050	.110	.023	.070	.320	.014	
ML226a	.880	.285	.145	.015	.125		.090	.015	.055	.290	.008	.325
	.910	.295	.175	.030	.145		.110	.023	.065	.320	.012	.385

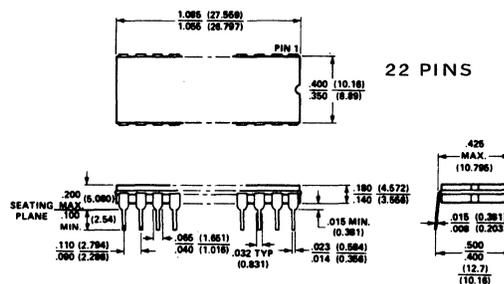
**ML227**



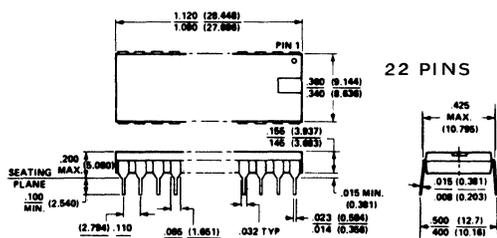
**ML228**



**ML229**



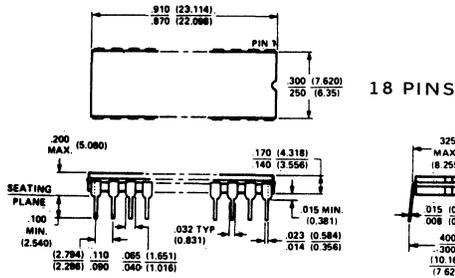
**ML230**



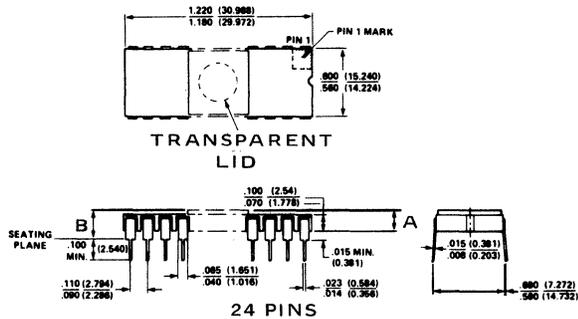
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

**ML231**

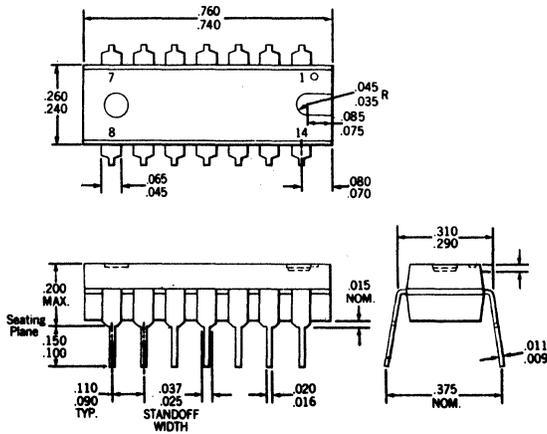


**ML232**

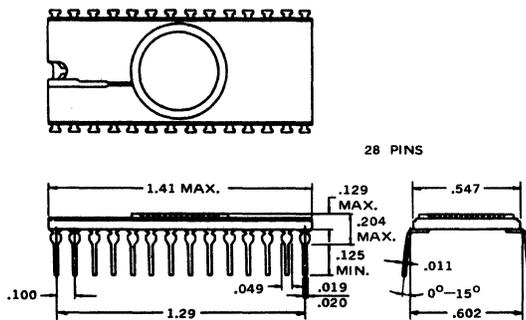


	A	B	TRANSPARENT LID
ML232	.080 (2.032)	.175 (4.445)	YES
	.125 (3.175)	MAX	
ML232a	.100 (2.54)	.195 (4.953)	NO
	.140 (3.556)	MAX	

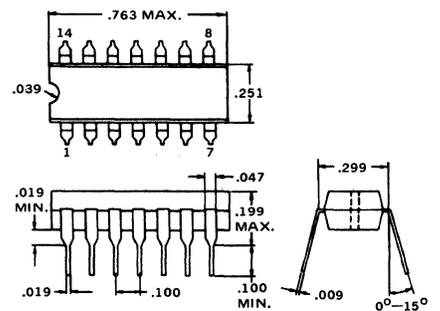
**ML233**



**ML234**



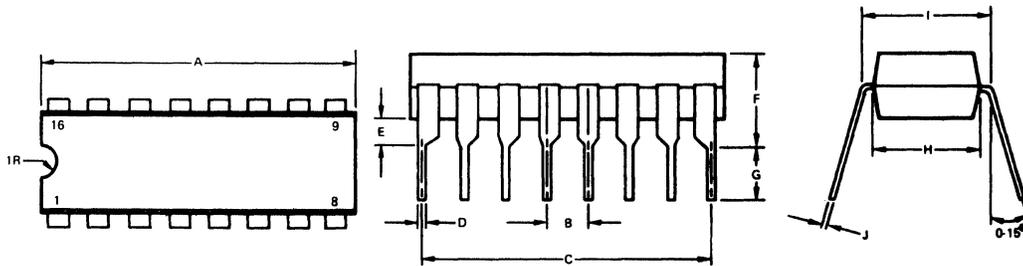
**ML235**



# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

## ML236



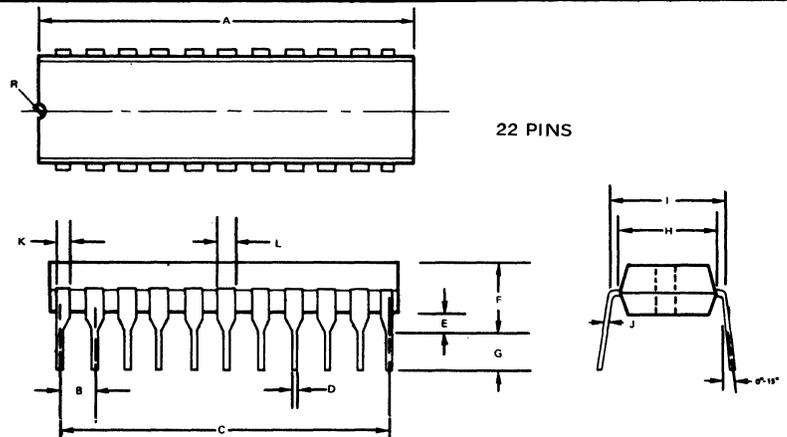
ITEM	MILLIMETERS	INCHES
A	19.4 MAX	0.76 MAX
B	2.54	0.10
C	17.78	0.70
D	0.5	0.02
E	0.5 MIN	0.02 MIN
F	4.55 MAX	0.18 MAX
G	2.54 MIN	0.10 MIN
H	6.4	0.25
I	7.82	0.30
J	0.25 +0.10 -0.05	0.01
R	1	0.039

Typical dimensions unless otherwise specified.

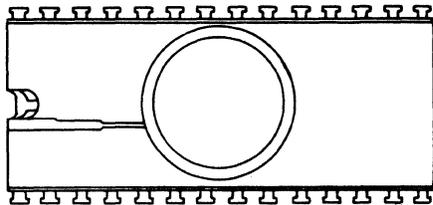
## ML237

ITEM	MILLIMETER	INCHES
A	28.0 Max.	1.10 Max.
B	2.54	0.10
C	25.4	1.00
D	0.50	0.02
E	0.5 Min.	0.02 Min.
F	5.2 Max.	0.20 Max.
G	2.54 Min.	0.10 Min.
H	8.5	0.33
I	10.16	0.40
J	0.25 +0.10 -0.05	0.01 +0.004 -0.002
K	1.15	0.045
L	1.40	0.055
R	1.2	0.047

Typical Dimensions Unless Otherwise Specified.

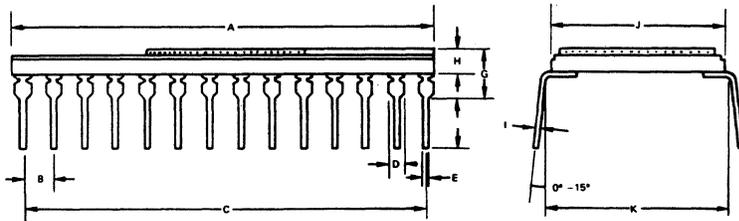


## ML238

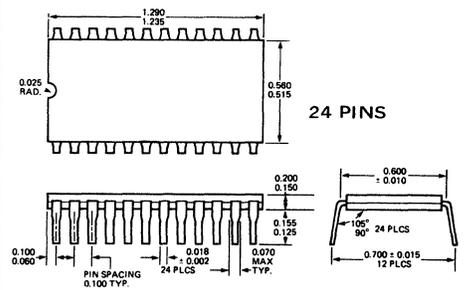


Item	Millimeters	Inches
A	36.0 Max.	1.41 Max.
B	2.54	0.1
C	33.0	1.29
D	1.27	0.06
E	0.50 ± 0.1	0.02 ± 0.004
F	3.2 Min.	0.13 Min.
G	5.2 Max.	0.20 Max.
H	3.3 Max.	0.13 Max.
I	0.30 ± 0.1	0.012 ± 0.004
J	13.9	0.55
K	15.3	0.60

Typical dimensions unless otherwise specified.



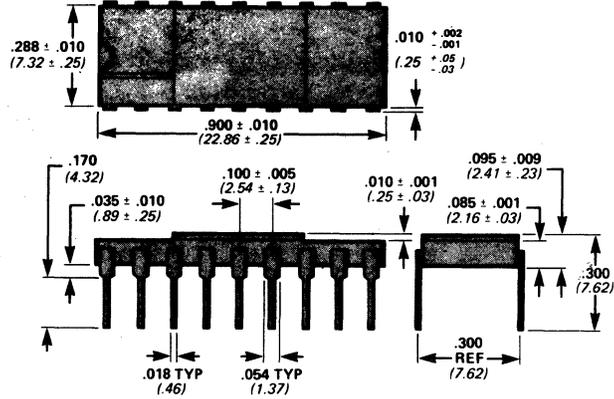
## ML239



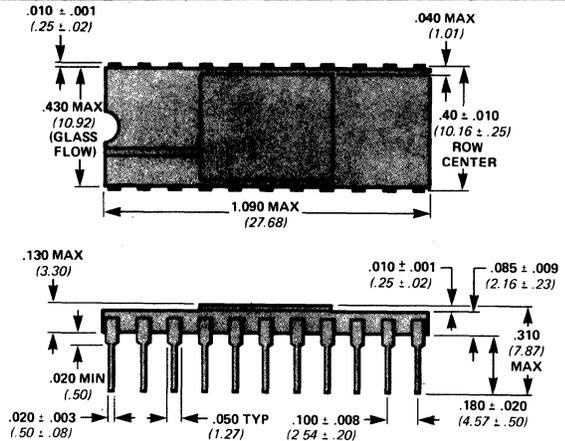
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

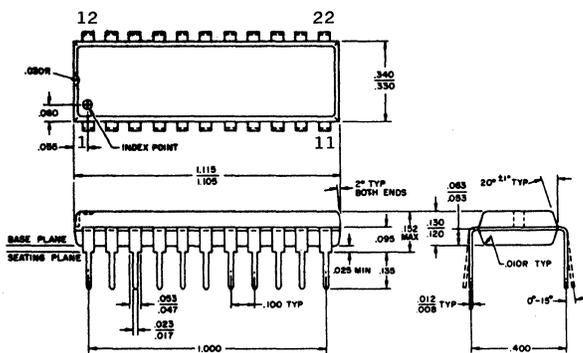
**ML240**



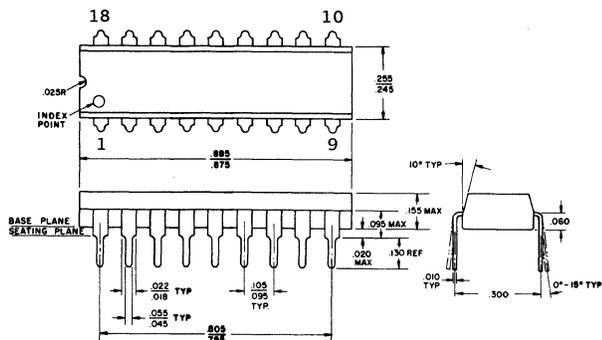
**ML241**



**ML242**



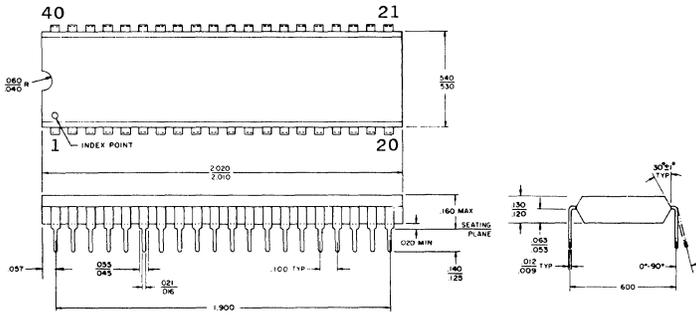
**ML244**



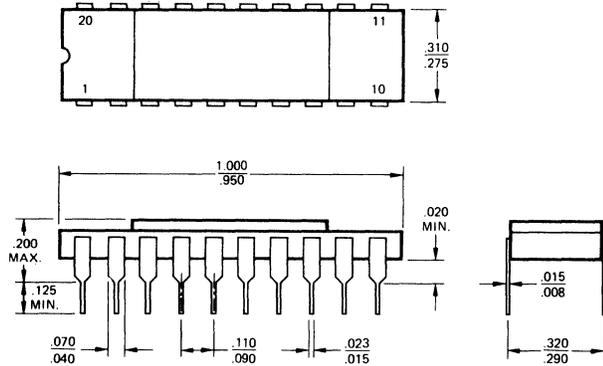
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

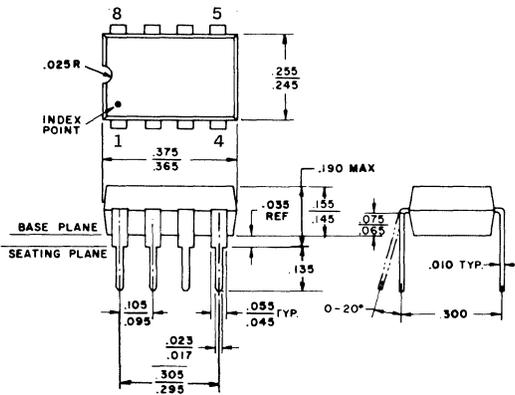
**ML246**



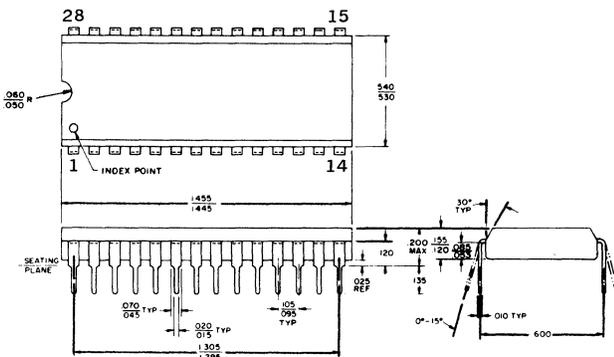
**ML248**



**ML250**



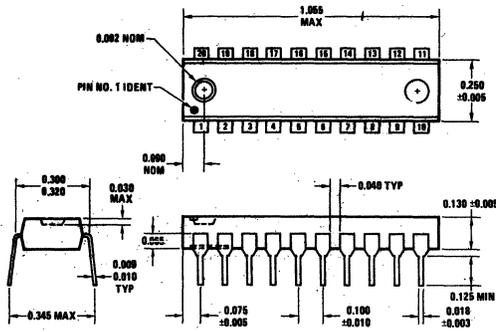
**ML252**



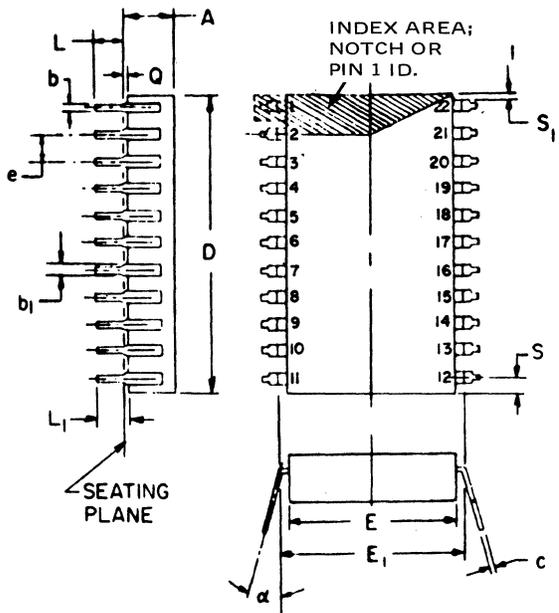
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

ML253



ML254

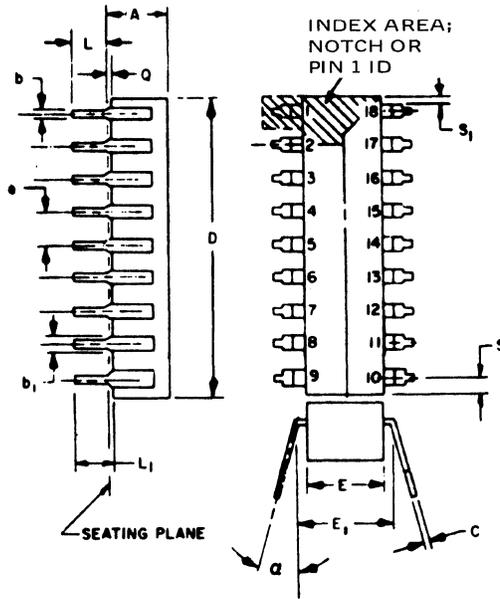


SYMBOL	DIMENSIONS			
	Inches		Millimeters	
	Min	Max	Min	Max
A		.225		5.72
b	.014	.023	.36	.58
b <sub>1</sub>	.030	.070	.76	1.78
c	.008	.015	.20	.38
D		1.100		27.94
E	.350	.390	8.89	9.91
E <sub>1</sub>	.390	.420	9.91	10.67
e	.100 BSC		2.54 BSC	
L	.120	.200	3.18	5.08
L <sub>1</sub>	.150		3.81	
Q	.015	.075	.38	1.91
S		.098		2.49
S <sub>1</sub>	.005		.13	
α	0°	15°	0°	15°

# 23. OUTLINE DRAWINGS

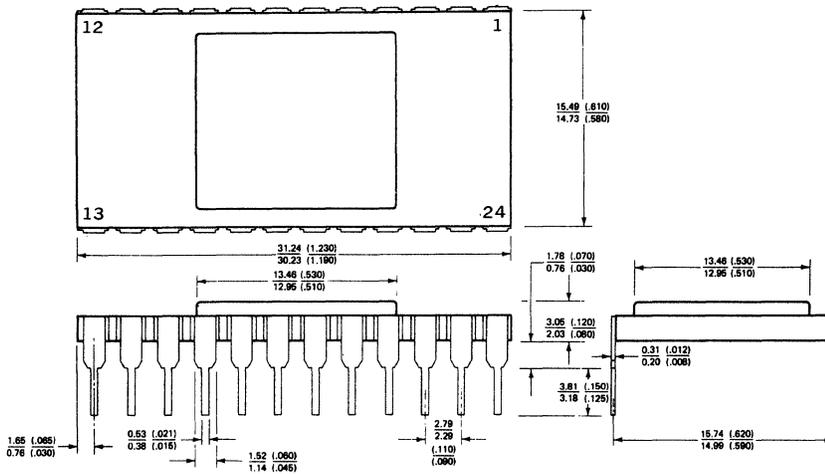
IN DRAWING NUMBER  
SEQUENCE

**ML255**



SYMBOL	DIMENSIONS			
	Inches		Millimeters	
	Min	Max	Min	Max
A		.200		5.08
b	.014	.023	.36	.58
b <sub>1</sub>	.030	.070	1.02	1.78
c	.008	.015	.20	.38
D		.096		25.29
E	.220	.310	5.59	7.87
E <sub>1</sub>	.290	.320	7.37	8.13
e	.100 BSC		2.54 BSC	
L	.125	.200	3.18	5.08
L <sub>1</sub>	.150		3.81	
Q	.015	.060	.38	1.52
S <sub>1</sub>	.005		.13	
S		.098		2.49
α	0°	15°	0°	15°

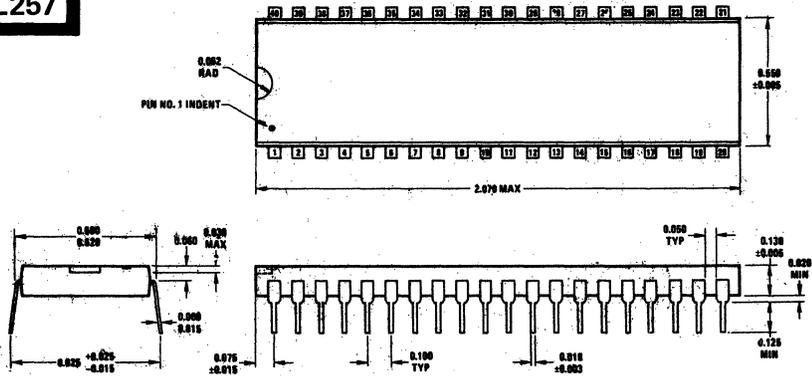
**ML256**



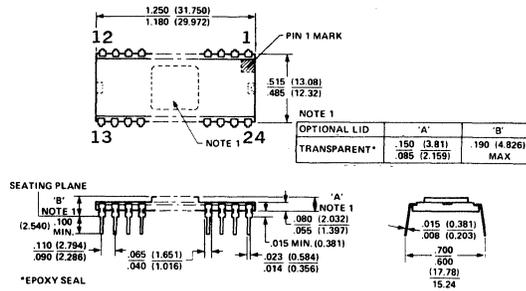
# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER  
SEQUENCE

**ML257**



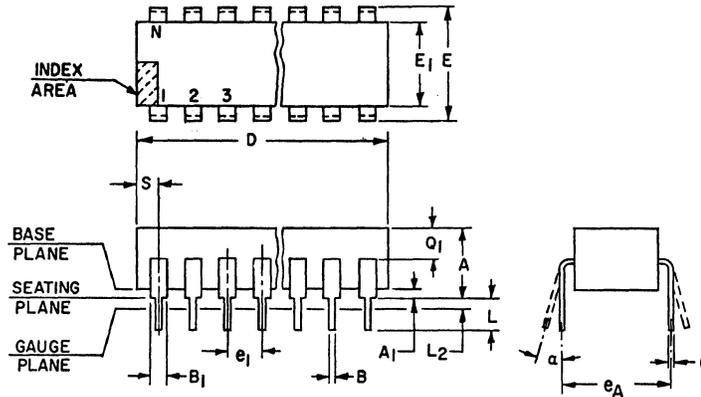
**ML258**



# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

MO001

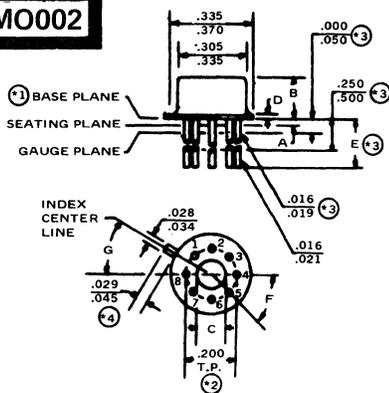


### NOTES:

1. Refer to applicable symbol list.
2. Dimensioning and tolerancing per ANSI Y14.5-1973.
3. Leads within .005 radius of True Position (TP) at gauge plane with maximum material condition and unit installed.
4.  $e_1$  and  $e_A$  applies in zone  $L_2$  when unit installed.
5.  $\alpha$  applies to spread leads prior to installation.
6. N is the maximum quantity of lead positions.
7.  $N_1$  is the allowable quantity of missing leads.
8.  $E_1$  does not include mold flash.
9. Outlines on which the seating plane is coincident with the base plane ( $A_1 = 0$ ) terminal lead stand-offs are not required, and  $B_1$  may equal B along any part of the lead above the seating/base plane.
10. Controlling Dimension: INCH

	A	A1	B	B1	C	D	E	E1	e1	eA	L	L2	s	N	N1	Q1	S	NOTES
MO001AA	.200	.020	.015	.030	.008	.660	.325	.220	.100	.300	.100	.000	0*	14	0			1,2,10
NOTES	MAX	MIN	.023	.070	.015	.785	MAX	.280	T.P.	T.P.	.150	.030	15*	5	6	7		
MO001AB	.155	.020	.014	.050	.008	.745	.300	.240	.100	.300	.125	.000	0*	14	0	.040	.065	1,2,10
NOTES	MAX	MIN	.020	.065	.012	.770	.325	.260	T.P.	T.P.	.150	.030	15*	5	6	7		
MO001AC	.155	.020	.014	.035	.008	.745	.300	.240	.100	.300	.125	.000	0*	16	0	.040	.015	1,2,10
NOTES	MAX	MIN	.020	.065	.012	.785	.325	.260	T.P.	T.P.	.150	.030	15*	5	6	7		
MO001AD	.120	.020	.014	.050	.008	.745	.300	.240	.100	.300	.125	.000	0*	14	0	.050	.065	1,2,10
NOTES	MAX	MIN	.020	.065	.012	.770	.325	.260	T.P.	T.P.	.150	.030	15*	5	6	7		
MO001AE	.120	.020	.014	.035	.008	.745	.300	.240	.100	.300	.125	.000	0*	16	0	.050	.015	1,2,10
NOTES	MAX	MIN	.020	.065	.012	.785	.325	.260	T.P.	T.P.	.150	.030	15*	5	6	7		
MO001AF	.165	.015	.015	.045	.009	.750	.295	.245	.100	.300	.120	.000	2*	14	0	.050	.060	1,2,10
NOTES	MAX	MIN	.045	.020	.070	.795	.325	.300	T.P.	T.P.	.160	.030	15*	5	6	7		
MO001AG	.165	.015	.015	.045	.009	.750	.295	.245	.100	.300	.120	.000	2*	16	0	.050	.010	1,2,10
NOTES	MAX	MIN	.045	.020	.070	.795	.325	.300	T.P.	T.P.	.160	.030	15*	5	6	7		
MO001AH	.140	.015	.014	.044	.008	.730	.290	.240	.100	.300	.115	.000	0*	14	0	.050	.055	1,2,10
NOTES	MAX	MIN	.020	.070	.012	.770	.320	.260	T.P.	T.P.	.155	.030	15*	5	6	7		
MO001AJ	.090	.020	.015	.035	.008	.685	.300	.240	.100	.300	.100	.000	0*	14	0	.060	.045	1,2,10
NOTES	MAX	MIN	.023	.055	.012	.760	.325	.285	T.P.	T.P.	.150	.030	15*	5	6	7		
MO001AK	.090	.020	.015	.035	.008	.800	.300	.240	.100	.300	.100	.000	0*	16	0	.060	.040	1,2,10
NOTES	MAX	MIN	.023	.055	.012	.840	.325	.285	T.P.	T.P.	.150	.030	15*	5	6	7		
MO001AL	.160	.020	.015	.044	.008	.815	.290	.240	.100	.300	.120	.000	0*	16	0	.050	.052	1,2,10
NOTES	MAX	MIN	.020	.070	.012	.890	.350	.260	T.P.	T.P.	.150	.030	15*	5	6	7		

MO002



### NOTES:

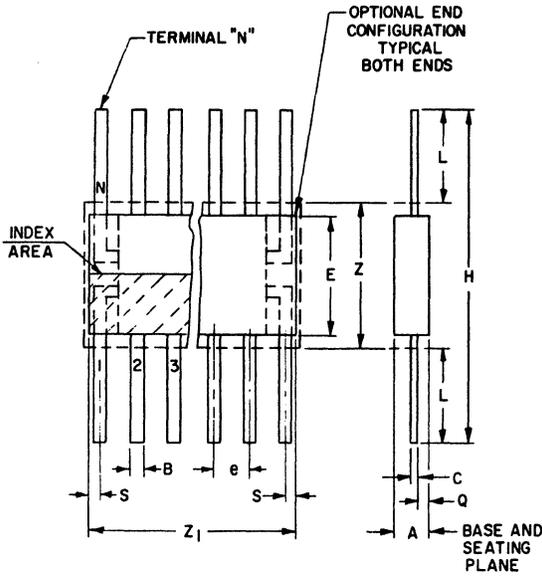
1. Refer to Rules for Dimensioning Axial Lead Product Outlines.
2. Leads at gauge plane within .007" radius of True Positions (TP) with maximum material condition.
3. Dim. .016" Min. and .019" Max. applies between .000" Min. and .050" Max. and .250" Min. and .500" Max. Dim. .016" Min. and .021" Max. applies between .250" Min. and .500" Max. and .500" from seating plane. Diameter is uncontrolled in .000" Min. and .050" Max. and .500".
4. Measured from Max. .370".

	A	B	C	D	E	P	NO. OF LEADS	NO. OF LEADS MISSING	G
MO002AA	.010	.240	.140	.040	.500	45*	8	1	22.5* TP
MO002AB	.040	.260	.160	MAX	MIN	TP	8	3	0* TP
MO002AC	0	.240	0	MAX	MIN	TP	12	1	15* TP
MO002AD	0	.240	0	.040	.500	36*	10	1	18* TP
MO002AE	0	.240	0	.040	.500	60*	6	1	0* TP
MO002AF	0	.260	0	MAX	MIN	TP	8	3	0* TP
MO002AG	0	.165	0	.040	.500	45*	8	3	0* TP
MO002AH	0	.160	0	.040	.500	45*	8	3	0* TP
MO002AJ	0	.085	0	.040	.500	45*	8	3	0* TP
MO002AK	.010	.165	.140	.040	.500	45*	8	3	0* TP
MO002AL	.010	.165	.125	.020	.500	45*	8	3	0* TP
	.050	.165	.160	.040	.562	TP			

# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

**MO004**

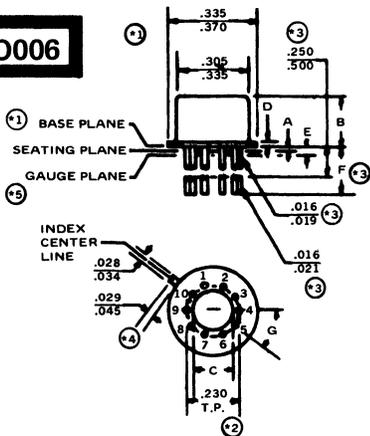


**NOTES:**

1. Refer to Rules for Dimensioning Peripheral Lead Outlines.
2. Leads within .005 radius of True Position (TP) at maximum material condition.
3. N is the maximum quantity of lead positions.
4. Z and Z<sub>1</sub> determine a zone within which all body and lead irregularities lie.
5. Controlling Dimensions: INCH

	A	B	C	e	E	H	L	N	Q	S	Z	Z <sub>1</sub>	NOTES
MO004AA	.008	.015	.003	.050	.200	.600	.150	14	.025	.000	.300	.350	1,2,6
NOTES	.100	.019	.006	TP	.300	1.000	.350	4	.050	.025	5	5	
MO004AB	.008	.013	.003	.050	.200	.600	.150	14	.005	.000	.300	.350	1,2,6
NOTES	.100	.017	.006	TP	.300	1.000	.350	4	.050	.025	5	5	
MO004AC	.008	.015	.003	.050	.200	.600	.150	14	.000	.000	.300	.350	1,2,6
NOTES	.100	.019	.006	TP	.300	1.000	.350	4	.097	.025	5	5	
MO004AD	.008	.016	.003	.050	.200	.600	.150	10	.005	.000	.300	.300	1,2,6
NOTES	.100	.019	.006	TP	.300	1.000	.350	4	.050	.050	5	5	
MO004AE	.008	.015	.003	.050	.200	.600	.150	10	.005	.000	.300	.250	1,2,6
NOTES	.100	.019	.006	TP	.300	1.000	.350	4	.050	.025	5	5	
MO004AF	.008	.015	.003	.050	.200	.600	.150	14	.005	.000	.300	.400	1,2,6
NOTES	.100	.019	.006	TP	.300	1.000	.350	4	.050	.050	5	5	
MO004AG	.008	.015	.003	.050	.200	.600	.150	14	.005	.000	.300	.400	1,2,6
NOTES	.100	.019	.006	TP	.300	1.000	.350	4	.050	.025	5	5	
MO004AH	.008	.015	.003	.050	.200	.600	.150	16	.005	.000	.300	.450	1,2,6
NOTES	.100	.019	.006	TP	.300	1.000	.350	4	.050	.050	5	5	

**MO006**



**NOTES:**

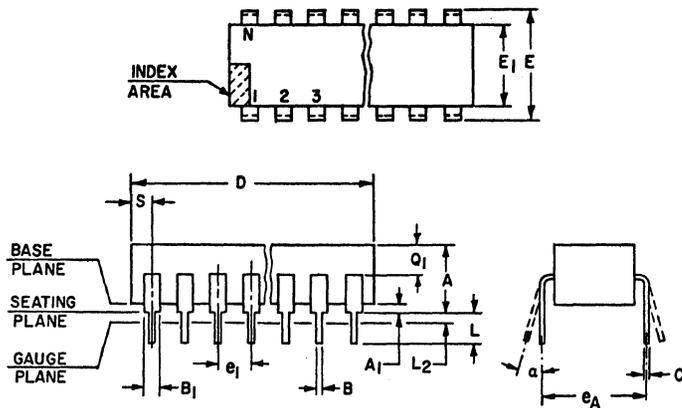
1. Refer to Rules for Dimensioning Axial Lead Product Outlines.
2. Leads at gauge plane within .007" radius of True Positions (TP) with maximum material condition.
3. Dim. .016" Min. and .019" Max. applies between .000" Min. and .050" Max. and .250" Min. and .500" Max. Dim. .016" Min. and .021" Max. applies between .250" Min. and .500" Max. and .500" from seating plane. Diameter is uncontrolled in .000" Min. and .050" Max. and .500".
4. Measured from Max. .370".
5. One (1) allowable missing lead.

	A	B	C	D	E	F	G	NO. OF LEADS
MO006AA	.010	.240	.140	.040	.050	.500	36*	10
	.040	.260	.160	MAX	MAX	MIN	TP	
MO006AB	0	.240	0	.040	.050	.500	36*	10
	0	.260	0	MAX	MAX	MIN	TP	
MO006AC	0	.140	0	.040	.050	.500	36*	10
	0	.160	0	MAX	MAX	MIN	TP	
MO006AD	.010	.165	.140	.040	.050	.500	36*	10
	.140	.185	.160	MAX	MAX	MIN	TP	
MO006AE	.010	.165	.140	.040	.050	.500	30*	12
	.040	.185	.160	MAX	MAX	MIN	TP	
MO006AF	0	.165	0	.020	.000	.500	36*	10
	0	.185	0	.040	.050	.562	TP	
MO006AG	0	.165	0	.020	.000	.500	30*	12
	0	.185	0	.040	.050	.562	TP	

# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

**MO015**

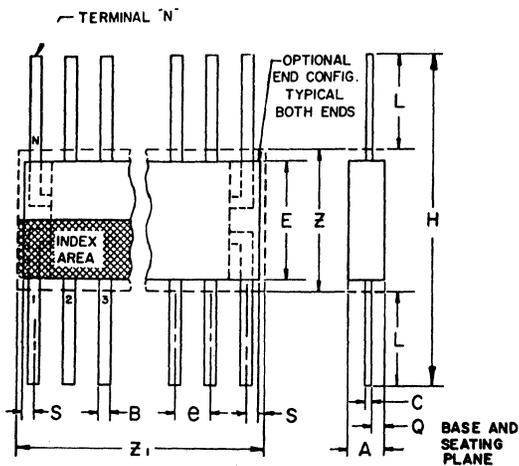


**NOTES:**

1. Refer to applicable symbol list.
2. Dimensioning and tolerancing per ANSI Y14.5-1973.
3. Leads within .127 radius of True Position (TP) at gauge plane with maximum material condition and unit installed.
4.  $e_1$  and  $e_A$  applies in zone  $L_2$  when unit installed.
5.  $\alpha$  applies to spread leads prior to installation.
6. N is the maximum quantity of lead positions.
7.  $N_1$  is the allowable quantity of missing leads.
8.  $E_1$  does not include mold flash.
9. Outlines on which the seating plane is coincident with the base plane ( $A_1 = 0$ ) terminal lead stand-offs are not required, and  $B_1$  may equal B along any part of the lead above the seating/base plane.
10. Controlling Dimension: INCH

	A	A1	B	B1	C	D	E	E1	e1	eA	L	L2	a	N	N1	Q1	S	NOTES
MO015AA	.120	.020	.018	.028	.008	1.200	.600	.515	.100	.600	.100	.000	0°	24	0	.040	.040	1,2,10
NOTES	.150	.070	.020	.070	.012	1.290	.625	.580	TP	TP	.200	.030	15°	5	6	7	.075	.100
MO015AB	.120	.020	.018	.028	.008	.700	.600	.515	.100	.600	.100	.000	0°	16	0	.040	.040	1,2,10
NOTES	.150	.070	.020	.070	.012	.840	.625	.580	TP	TP	.200	.030	15°	5	6	7	.075	.100
MO015AC	.120	.020	.018	.028	.008	1.800	.600	.515	.100	.600	.100	.000	0°	36	0	.065	.040	1,2,10
NOTES	.150	.070	.020	.070	.012	1.890	.625	.580	TP	TP	.200	.030	15°	5	6	7	.080	.100
MO015AD	.100	.000	.015	.015	.008	1.170	.600	.515	.100	.600	.100	.000	0°	24	0	.020	.025	1,2,10
NOTES	.120	.070	.020	.055	.012	1.210	.625	.580	TP	TP	.200	.030	15°	5	6	7	.080	.050
MO015AE	.100	.000	.015	.015	.008	.770	.600	.515	.100	.600	.100	.000	0°	16	0	.020	.025	1,2,10
NOTES	.120	.070	.020	.055	.012	.810	.625	.580	TP	TP	.200	.030	15°	5	6	7	.080	.050
MO015AF	.100	.000	.015	.015	.008	1.770	.600	.515	.100	.600	.100	.000	0°	36	0	.020	.025	1,2,10
NOTES	.120	.070	.020	.055	.012	1.810	.625	.580	TP	TP	.200	.030	15°	5	6	7	.080	.050
MO015AG	.090	.020	.014	.050	.008	1.220	.600	.520	.100	.600	.125	.000	0°	24	0	.020	.050	1,2,10
NOTES	.120	.070	.020	.054	.012	1.290	.625	.550	TP	TP	.150	.030	15°	5	6	7	.065	.100
MO015AH	.100	.000	.015	.015	.008	1.380	.600	.485	.100	.600	.100	.000	0°	28	0	.020	.040	1,2,10
NOTES	.120	.070	.020	.055	.012	1.420	.625	.515	TP	TP	.200	.030	15°	5	6	7	.070	.070
MO015AJ	.100	.000	.015	.015	.008	1.980	.600	.485	.100	.600	.100	.000	0°	40	0	.020	.040	1,2,10
NOTES	.120	.070	.020	.055	.012	2.020	.625	.515	TP	TP	.200	.030	15°	5	6	7	.070	.070
MO015AK	.145	.030	.015	.040	.008	1.240	.600	.540	.100	.600	.100	.000	0°	24	0	.045	.065	1,2,10
NOTES	.175	.050	.020	.050	.015	1.260	.625	.560	TP	TP	.140	.030	15°	5	6	7	.075	.085

**MO019**



	A	B	C	e	E	H	L	N	Q	S	Z	Z1	NOTES
MO019AA	.008	.015	.003	.050	.300	.700	.150	24	.005	.000	.400	.650	1,2,6
NOTES	.120	.019	.006	TP	.400	1.200	.400	4	.050	.050	5	5	
MO019AB	.008	.018	.003	.050	.300	.700	.150	28	.005	.000	.400	.800	1,2,6
NOTES	.120	.022	.006	TP	.400	1.200	.400	4	.050	.075	5	5	
MO019AC	.065	.015	.003	.050	.300	.940	.270	24	.005	.000	.400	.650	1,2,6
NOTES	.120	.019	.006	TP	.400	1.200	.400	4	.050	.050	5	5	
MO019AD	.065	.015	.003	.050	.300	.940	.270	28	.005	.000	.400	.800	1,2,6
NOTES	.120	.019	.006	TP	.400	1.200	.400	4	.050	.050	5	5	

**NOTES:**

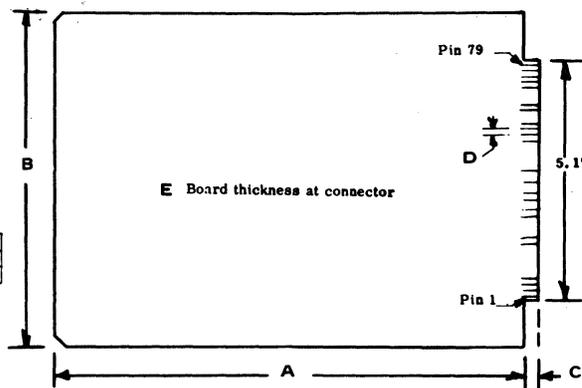
1. Refer to applicable symbol list.
2. Dimensioning and tolerancing per ANSI Y14.5-1973.
3. Leads within .005 radius of True Position (TP) at maximum material condition.
4. N is the maximum quantity of lead positions.
5. Z and Z1 determine a zone within which all body and lead irregularities lie.
6. Controlling dimensions: INCH.

# 23. OUTLINE DRAWINGS

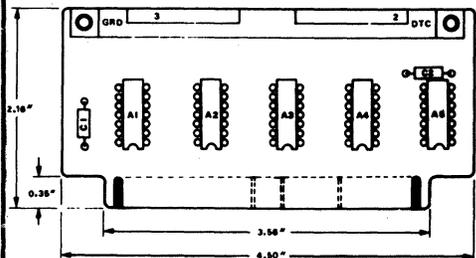
IN DRAWING NUMBER SEQUENCE

**PL2**

	A	S	C	D	E
PL2	9.70	7.00	.300	.125	.054 - .072
PL2a	7.50	6.00	.500		

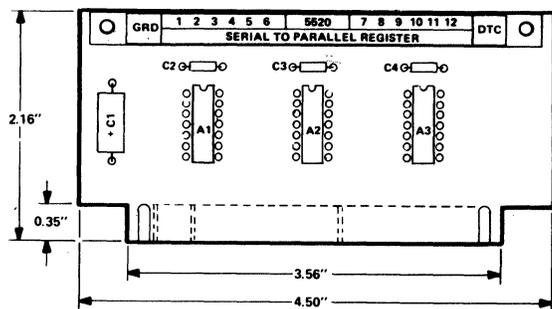


**PL8**



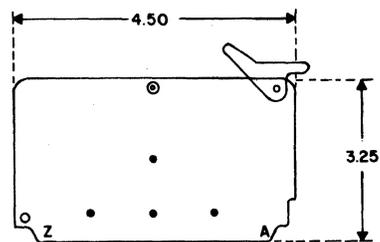
Key Slots Located Between Pins 10 & 11, 12 & 13, 16 & 16

**PL9**



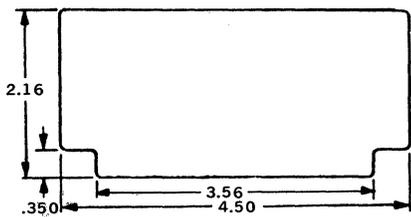
KEY SLOTS LOCATED BETWEEN PINS 1 & 2; 4 & 5; 13 & 14.

**PL10**

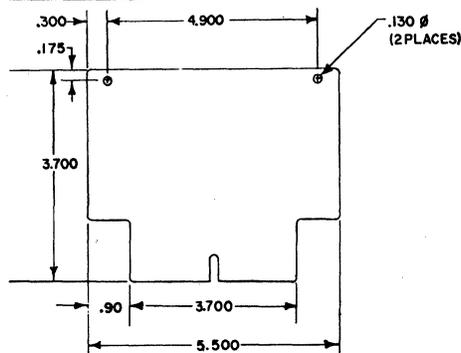


0625" THICK

**PL11**



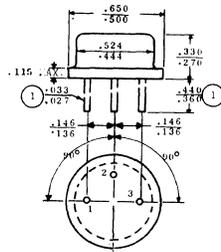
**PL12**



# 23. OUTLINE DRAWINGS

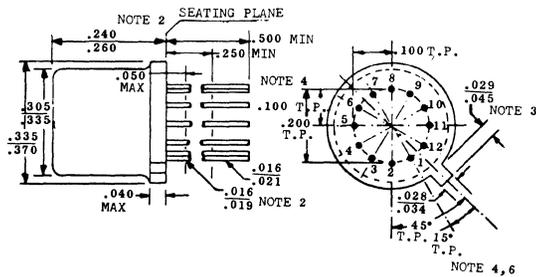
IN DRAWING NUMBER  
SEQUENCE

T08



T08 Note: 1. Three Leads

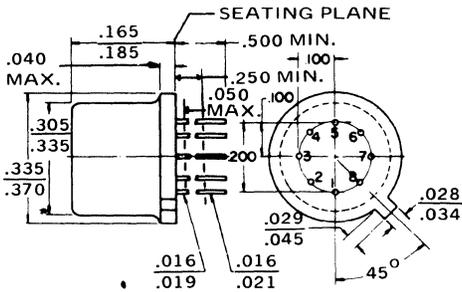
T073



NOTES:

- (Twelve Leads). Maximum number of leads omitted in this outline, "one" (1). The number and position of leads actually present are indicated in the product registration. Outline designation determined by the location and minimum angular spacing of any two adjacent leads.
- (All Leads) Dim. .016 Min and .019 Max applies between .050 Max and .250 Min. Dim. .016 Min and .021 Max applies between .250 Min and .500" (12.70 MM) from seating plane. Diameter is uncontrolled in Dim .050 Max and beyond .500" (12.70 MM) from seating plane.
- Measured from maximum diameter of the product.
- Leads having maximum diameter .019" (.483 MM) measured in gaging plane .054" (1.37 MM) + .001" (.025 MM) - .000" (.000 MM) below the seating plane of the product shall be within .007" (.178 MM) of their true position relative to a maximum width tab.
- The product may be measured by direct methods or by gage.
- Tab Centerline.

T078



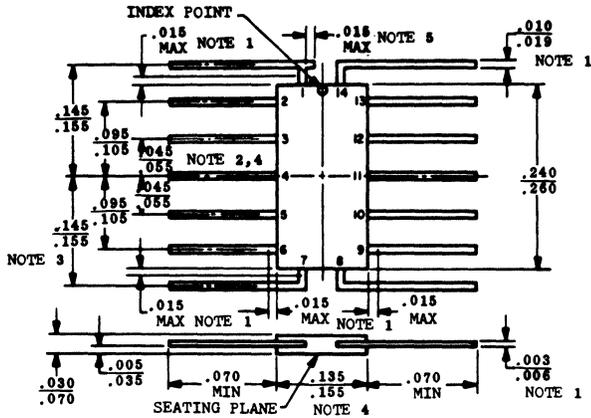
NOTES:

- (EIGHT LEADS). Maximum number of leads omitted in this outline, "three" (3). The number and position of leads actually present are indicated in the product registration. Outline designation determined by the location and minimum angular spacing of any two adjacent leads.
- (ALL LEADS) Dim. .016 Min and .019 Max. applies between Dim. .050 Max. and .250 Min. Dim. .016 Min and .021 Max. applies between .500" (12.70 MM) from seating plane. Diameter is uncontrolled in .050 Max. and beyond .500" (12.70 MM) from seating plane.
- Measured from maximum diameter of the product.
- Leads having maximum diameter .019" (.483 MM) measured in gaging plane .054" (1.37 MM) + .001" (.025 MM) - .000" (.000 MM) below the seating plane of the product shall be within .007" (.178 MM) of their true position relative to a maximum width tab.
- The product may be measured by direct methods or by gage.
- Tab Centerline.

# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

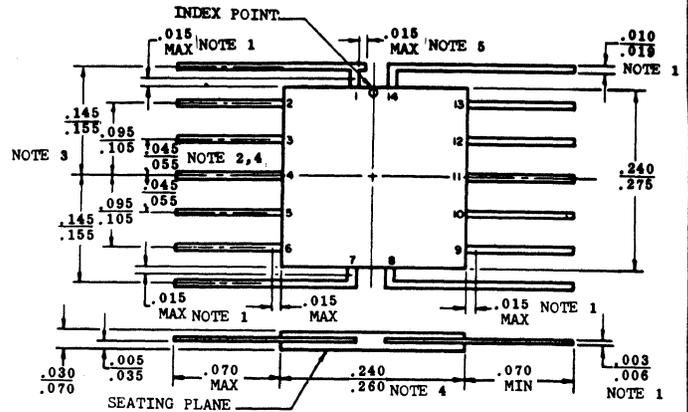
**T084**



NOTES:

1. Lead dimensions uncontrolled in this zone to allow for body and lead finish irregularities.
2. Leads missing from their designated positions shall also be counted when numbering leads for specific applications.
3. Spacing and Angle of the end leads at the point of emergence of body is not controlled.
4. Lead spacing shall be measured within .030 (.762 mm) from the point of emergence from the body or, as in the case of end lead, from the point where the extension of the body outline intersects the end leads.
5. Mechanical Index, Optional.

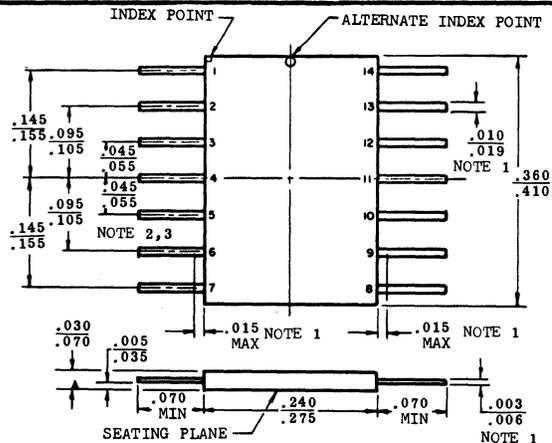
**T086**



NOTES:

1. Lead dimensions uncontrolled in this zone to allow for body and lead finish irregularities.
2. Leads missing from their designated positions shall also be counted when numbering leads for specific applications.
3. Spacing and Angle of the end leads at the point of emergence of body is not controlled.
4. Lead spacing shall be measured within .030 (.762 mm) from the point of emergence from the body or, as in the case of end lead, from the point where the extension of the body outline intersects the end leads.
5. Mechanical Index, Optional.

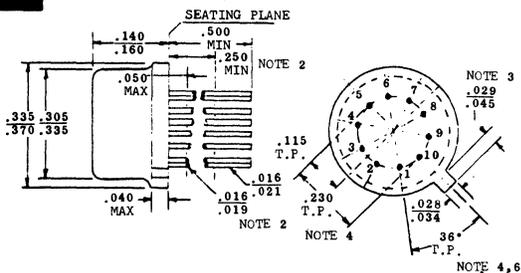
**T087**



NOTES:

1. Lead Dimensions uncontrolled in this zone to allow for body and lead finish irregularities.
2. Leads missing from their designated positions shall also be counted when numbering leads for specific applications.
3. Lead spacing shall be measured within .030 (.762 mm) from the point of emergence from the body.

**T097**



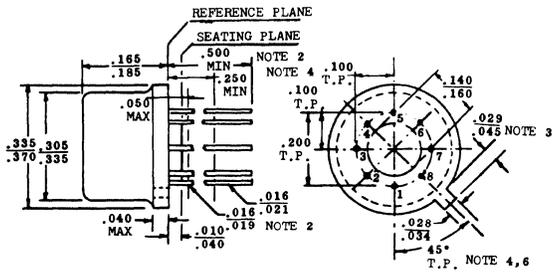
NOTES:

1. (Ten Leads). Maximum number of leads omitted in this outline, "one" (1). The number and position of leads actually present are indicated in the product registration. Outline designation determined by the location and minimum angular spacing of any two adjacent leads.
2. (All Leads) Dim. .016 Min and .019 Max applies between .050 Max and .250 Min. Dim. .016 Min and .021 Max applies between .250 Min and .500" (12.70 MM) from seating plane. Diameter is uncontrolled in Dim .050 Max and beyond .500" (12.70 MM) from seating plane.
3. Measured from maximum diameter of the product.
4. Leads having maximum diameter .019" (.483 MM) measured in gaging plane .054 (1.37 MM) + .001" (.025 MM) - .000" (.000 MM) below the seating plane of the product shall be within .007" (.178 MM) of their true position relative to a maximum width tab.
5. The product may be measured by direct methods or by gage.
6. Tab Centerline.

# 23. OUTLINE DRAWINGS

IN DRAWING NUMBER SEQUENCE

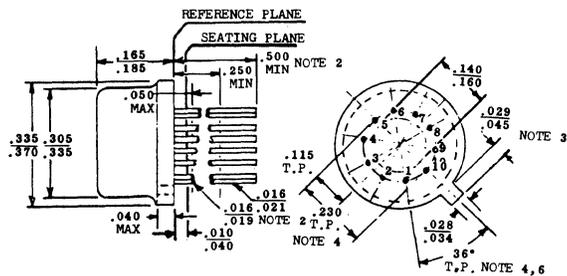
**T099**



NOTES:

- (Eight Leads). Maximum number of leads omitted in this outline, "one" (1). The number and position of leads actually present are indicated in the product registration. Outline designation determined by the location and minimum angular spacing of any two adjacent leads.
- (All Leads) Dim. .016 Min and .019 Max applies between .050 Max and .250 Min. Dim. .016 Min and .021 Max applies between .250 Min and .500" (12.70 MM) from seating plane. Diameter is uncontrolled in Dim .050 Max and beyond .500" (12.70 MM) from seating plane.
- Measured from maximum diameter of the product.
- Leads having maximum diameter .019" (.483 MM) measured in gaging plane .054 (1.37 MM) + .001" (.025 MM) - .000" (.000 MM) below the seating plane of the product shall be within .007" (.178 MM) of their true position relative to a maximum width tab.
- The product may be measured by direct methods or by gage.
- Tab Centerline.

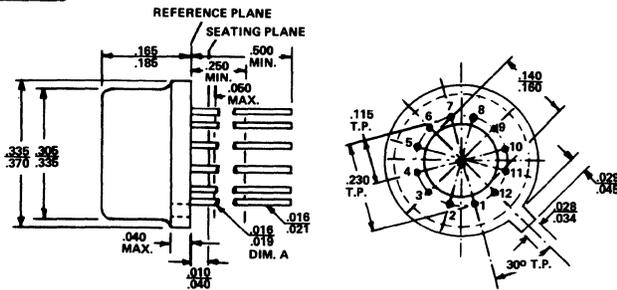
**T0100**



NOTES:

- (Ten Leads). Maximum number of leads omitted in this outline, "one" (1). The number and position of leads actually present are indicated in the product registration. Outline designation determined by the location and minimum angular spacing of any two adjacent leads.
- (All Leads) Dim. .016 Min and .019 Max applies between .050 Max and .250 Min. Dim. .016 Min and .021 Max applies between .250 Min and .500" (12.70 MM) from seating plane. Diameter is uncontrolled in Dim .050 Max and beyond .500" (12.70 MM) from seating plane.
- Measured from maximum diameter of the product.
- Leads having maximum diameter .019" (.483 MM) measured in gaging plane .054 (1.37 MM) + .001" (.025 MM) - .000" (.000 MM) below the seating plane of the product shall be within .007" (.178 MM) of their true position relative to a maximum width tab.
- The product may be measured by direct methods or by gage.
- Tab Centerline.

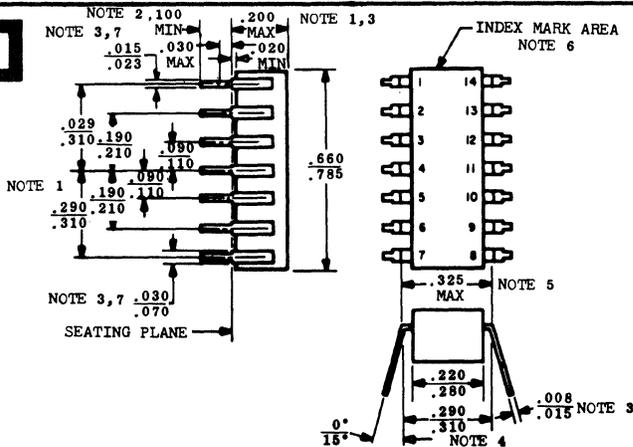
**T0101**



NOTES:

- (TWELVE LEADS). MAXIMUM NUMBER OF LEADS OMITTED IN THIS OUTLINE, "ONE" (1). THE NUMBER AND POSITION OF LEADS ACTUALLY PRESENT ARE INDICATED IN THE PRODUCT REGISTRATION. OUTLINE DESIGNATION DETERMINED BY THE LOCATION AND MINIMUM ANGULAR SPACING OF ANY TWO ADJACENT LEADS.
- (ALL LEADS). DIM. A APPLIES BETWEEN .050 MAX. AND .250 MIN. DIM. B APPLIES BETWEEN .250 MIN. AND .500" (12.70 MM) FROM REFERENCE PLANE. DIAMETER IS UNCONTROLLED IN .050 MAX. AND BEYOND .500" (12.70 MM) FROM REFERENCE PLANE.
- MEASURED FROM MAXIMUM DIAMETER OF THE PRODUCT.
- LEADS HAVING MAXIMUM DIAMETER .019" (.483 MM) MEASURED IN GAUGING PLANE .054" (1.37 MM) + .001" (.025 MM) - .000" (.000 MM) BELOW THE REFERENCE PLANE OF THE PRODUCT SHALL BE WITHIN .007" (.178 MM) OF THEIR TRUE POSITION RELATIVE TO A MAXIMUM WIDTH TAB.
- THE PRODUCT MAY BE MEASURED BY DIRECT METHODS OR BY GAUGE.
- TAB CENTERLINE.

**T0116**



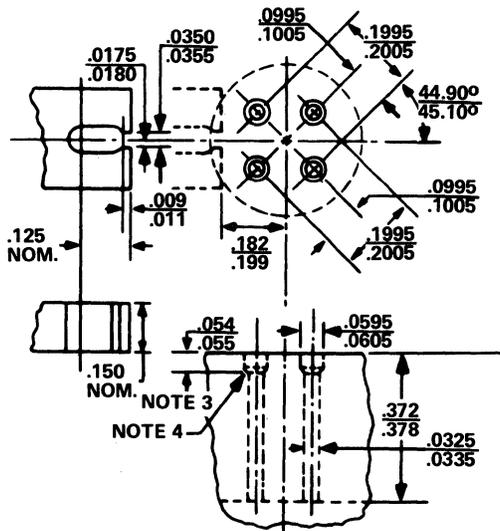
NOTES:

- Leads missing from their designated positions shall be counted when numbering leads for special applications.
- Lead spacing shall be measured within this zone.
- Typical all leads.
- Installed position of lead centers.
- Overall installed width.
- Index to be visible from top, this end only.
- Lead transition geometry from Dia .015 min to .023 max to Dia .030 min to .070 max optional on body side of seating plane.

## JEDEC GAUGE DESIGNATIONS

The Gauge Designations below are referenced in the JEDEC TO Outline Drawings

### GS1



**NOTES:**

1. THE LOCATION OF THE TAB LOCATOR WITHIN THE LIMITS INDICATED WILL BE DETERMINED BY THE TAB AND FLANGE DIMENSIONS OF THE DEVICE BEING CHECKED.

2. THE FOLLOWING GAUGING PROCEDURE SHALL BE USED:

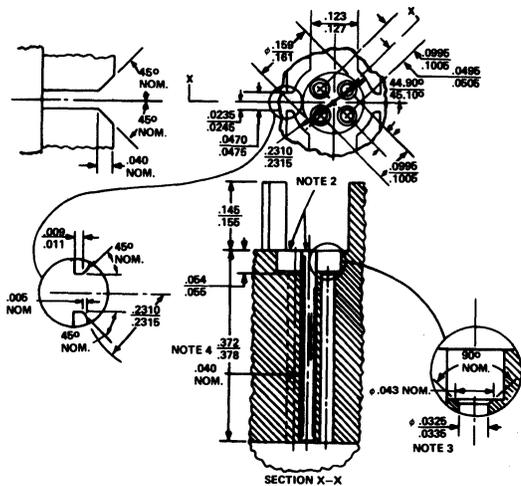
THE DEVICE BEING MEASURED SHALL BE INSERTED UNTIL ITS SEATING PLANE IS .125" (3.18 MM) ± .010" (.254MM) FROM THE SEATING SURFACE OF THE GAUGE. A FORCE OF 8 ± .5 OZ. SHALL THEN BE APPLIED PARALLEL AND SYMMETRICAL TO THE DEVICE'S CYLINDRICAL AXIS. WHEN EXAMINED VISUALLY AFTER THE FORCE APPLICATION (THE FORCE NEED NOT BE REMOVED) THE SEATING PLANE OF THE DEVICE SHALL BE SEATED AGAINST THE GAUGE.

THE USE OF A PIN STRAIGHTENER PRIOR TO INSERTION IN THE GAUGE IS PERMISSIBLE.

3. GAUGING PLANE.

4. DRILL ANGLE.

### GS2



**NOTE 1: THE FOLLOWING GAUGING PROCEDURE SHALL BE USED:**

THE DEVICE BEING MEASURED SHALL BE INSERTED UNTIL ITS SEATING PLANE IS 0.125" ± .010" FROM THE SEATING SURFACE OF THE GAUGE. A FORCE OF 8 ± 0.5 OZ. SHALL THEN BE APPLIED PARALLEL AND SYMMETRICAL TO THE DEVICE'S CYLINDRICAL AXIS. WHEN EXAMINED VISUALLY AFTER THE FORCE APPLICATION (THE FORCE NEED NOT BE REMOVED) THE SEATING PLANE OF THE DEVICE SHALL BE SEATED AGAINST THE GAUGE.

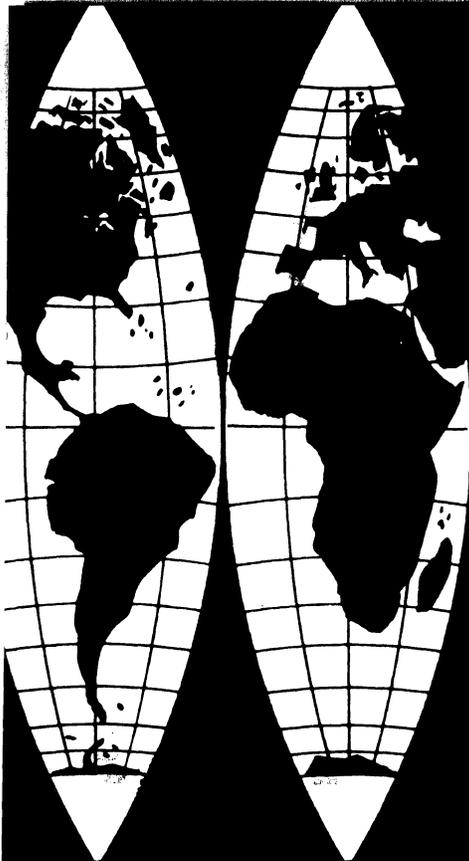
THE USE OF A PIN STRAIGHTENER PRIOR TO INSERTION IN THE GAUGE IS PERMISSIBLE.

A SPACER MAY BE USED TO OBTAIN THE 0.125" DISTANCE FROM THE GAUGE SEAT PRIOR TO FORCE APPLICATION.

**NOTE 2: THESE SURFACES TO BE PARALLEL AND IN SAME PLANE WITHIN ± .001"**

**NOTE 3: FOUR HOLES.**

**NOTE 4: PRESSED IN.**



## MSI-LSI MEMORY

### Manufacturers' Local Offices

These manufacturers have listed their local offices in this section for your convenience. Please contact the local office nearest you for any additional information you may need.

(MANUFACTURERS IN ORDER OF D.A.T.A. CODE LETTERS)

#### ALGG — AEG-TELEFUNKEN

	Zip Code	Telephone No.	Telex
Postfach 1109, Heilbronn, Germany .....	D7100	07131-8821	728746

#### FSC — FAIRCHILD SEMICONDUCTOR

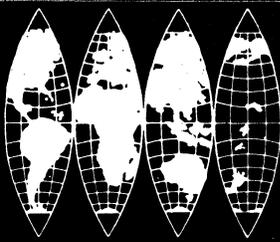
	Zip Code	Telephone No.	TWX
DIV. of FAIRCHILD CAMERA & INSTRUMENT CORP. 464 Ellis Street, Mountain View, California .....	94040	415-962-5011	910-379-6435 Cable FAIRSEMCO

#### ITL — INTEL CORPORATION

	Zip Code	Telephone No.	TWX
3065 Bowers Avenue, Santa Clara, California .....	95051	408-246-7501	910-338-0026 Telex 34-6372

##### U. S. SALES OFFICES

		Zip Code	Telephone No.	TWX	
CALIFORNIA .....	Santa Ana .....	Intel Corporation .....	92701	714-835-9642	910-595-1114
		1651 East 4th Street Suite 228			



## Manufacturers' Local Offices

### ITL — INTEL CORPORATION (Cont'd)

	Zip Code	Telephone No.	TWX
3065 Bowers Avenue, Santa Clara, California .....	95051	408-246-7501	910-338-0026 Telex 34-6372
ILLINOIS ..... Oakbrook ..... Intel Corporation ..... 1000 Jorie Boulevard	60521	312-325-9510	910-651-5881
MASSACHUSETTS ....Chelmsford ..... Intel Corporation ..... 187 Billerica Road Suite 14A	01824	617-256-6567	710-343-6333
TEXAS ..... Dallas ..... Intel Corporation ..... 2925 LBJ Freeway Suite 100	75234	214-241-9521	910-860-5487

#### EUROPEAN MARKETING OFFICE

BELGIUM ..... Brussels ..... Intel International ** ..... Rue du Moulin a Papier 51-Boite 1	B-1160	(02)66030 10	Telex 24814
---	--------	--------------	----------------

#### ORIENT MARKETING OFFICE

JAPAN ..... Tokyo ..... Intel Japan Corporation ** ..... Flower Hill Shinmachi E. Bldg. 1-23-9 Shinmachi, Setagaya-ku	154	(03)426-9261	781-28426
--	-----	--------------	-----------

\*\* Field Application Locations

---

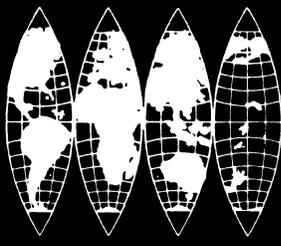
### MMI — MONOLITHIC MEMORIES INC.

1165 East Arques Avenue, Sunnyvale, California .....	Zip Code 94086	Telephone No. 408-739-3535	TWX 910-339-9229
--	-------------------	-------------------------------	---------------------

---

### NECM — NEC MICROCOMPUTERS, INC.

5 Militia Drive, Lexington, Massachusetts .....	Zip Code 02173	Telephone No. 617-862-6410	TWX 710-326-6520 Telex 923434
---	-------------------	-------------------------------	--



# Manufacturers' Local Offices

## NSC – NATIONAL SEMICONDUCTOR CORPORATION

2900 Semiconductor Drive, Santa Clara, California ..... Zip Code Telephone No. TWX  
 95051 408-737-5000 910-339-9240

### SALES OFFICES AND REPRESENTATIVES

**ALABAMA** ..... **Huntsville** ..... National Semiconductor ..... 35801 205-881-0622 810-726-2207  
 (Dixie Regional Office)  
 3322 Memorial Parkway, SW  
 Suite 14

Interep Associates, Inc. .... 35801 205-881-3677  
 3322 Memorial Parkway, SW  
 No. 67

**ARIZONA** ..... **Scottsdale** ..... National Semiconductor ..... 85251 602-945-8473 910-950-1195  
 (Rocky Mountain Regional Office)  
 7353 Sixth Avenue

Fred Board Associates ..... 85252 602-994-9388 910-950-1195  
 Post Office Box 1906

**CALIFORNIA** ..... **Santa Clara** ..... National Semiconductor ..... 95051 408-247-6397 910-338-0537  
 (Northwest Regional Office)  
 1333 Lawrence Expressway  
 Suite 258

Criterion Sales, Inc. .... 95050 408-243-3600  
 2225J Martin Avenue

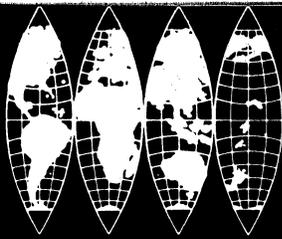
**Irvine** ..... National Semiconductor ..... 92714 714-957-1626  
 (Area Office)  
 17870 Sky Park Circle  
 No. 108

**Sherman Oaks** ..... National Semiconductor ..... 91403 213-783-8272 910-495-1773  
 (Los Angeles Regional Office)  
 Valley Freeway Center Building  
 15300 Ventura Boulevard  
 Suite 405

**San Diego** ..... National Semiconductor ..... 92111 714-565-8411 910-335-1566  
 (District Sales Office)  
 8333 Clairemont Mesa Blvd.

S. R. Electronics ..... 92121 714-455-0300 910-335-1566  
 10951 Sorrento Valley Road

**Tustin** ..... National Semiconductor ..... 92680 714-832-8113 910-595-1523  
 (Southern California Regional Office)  
 17452 Irvine Blvd.  
 Suite B

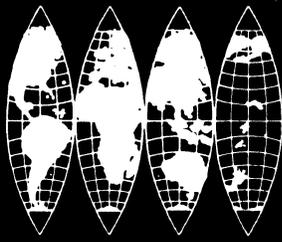


# Manufacturers' Local Offices

## NSC – NATIONAL SEMICONDUCTOR CORPORATION (Cont'd)

			Zip Code	Telephone No.	TWX
2900 Semiconductor Drive, Santa Clara, California .....			95051	408-737-5000	910-339-9240
<b>COLORADO</b> .....	<b>Denver</b> .....	Electrodyne, Inc. ** .....	80222	303-757-7679	910-931-0428
		4600 East Asbury Circle Suite 402			
<b>CONNECTICUT</b> .....	<b>Wilton</b> .....	National Semiconductor .....	06897	203-762-0378	710-479-3512
		(Northeast Area Sales Office) Piersall Building - Suite 415 Wilton Center			
	<b>Westport</b> .....	NRG Limited .....	06880	203-226-7527	710-457-2169
		50 Post Road			
<b>FLORIDA</b> .....	<b>Fort Lauderdale</b> ....	National Semiconductor .....	33309	305-772-6970	510-955-9708
		(Regional Office) 1001 NW 62nd Street Suite 100			
	<b>Maitland</b> .....	QXI .....	32751	305-647-1188	810-853-0260
		235 Maitland Avenue Suite 111			
	<b>St. Petersburg</b> .....	QXI .....	33713	813-821-2281	810-863-0354
		300 31st Street No. 319			
	<b>Tamarac</b> .....	QXI .....	33319	305-485-6030	
		4620 West Commercial Blvd. Suite C			
<b>GEORGIA</b> .....	<b>Atlanta</b> .....	Interep Associates, Inc. ....	30341	404-394-7756	810-757-0182
		7 Dunwoody Park Suite 112			
<b>ILLINOIS</b> .....	<b>Mt. Prospect</b> .....	National Semiconductor .....	60056	312-394-8040	910-689-3346
		(West-Central Regional Office) 800 East Northwest Highway Suite 203			
	<b>Arlington Heights</b> ...	Delta Technical Sales .....	60004	312-253-9440	910-687-2273
		3323 North Ridge Avenue			

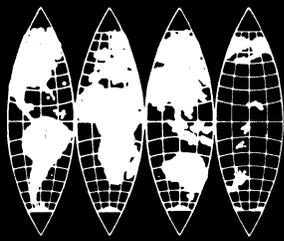
\*\* Applications Engineer Available



# Manufacturers' Local Offices

## NSC – NATIONAL SEMICONDUCTOR CORPORATION (Cont'd)

			Zip Code	Telephone No.	TWX
2900 Semiconductor Drive, Santa Clara, California .....			95051	408-737-5000	910-339-9240
<b>INDIANA</b> .....	<b>Indianapolis</b> .....	National Semiconductor .....	46240	317-255-5822	810-341-3300
		(North-Central Regional Office) Post Office Box 40073			
		Advanced Component Sales .....	46226	317-545-6441	810-341-3233
		5746 Brendon Way West Drive Post Office Box 26407			
	<b>Fort Wayne</b> .....	Advanced Component Sales .....	46805	219-484-0722	810-332-1472
		1010 Memorial Way Suite 1			
<b>IOWA</b> .....	<b>Cedar Rapids</b> .....	Gassner & Clark Company .....	52402	319-393-5763	910-525-2051
		1834 Blairs Ferry Road NE			
<b>MARYLAND</b> .....	<b>Glen Burnie</b> .....	National Semiconductor .....	21061	301-760-5220	710-867-0508
		(Capitol Regional Office) 95 Aquahart Road Suite 204			
		TRIMARK, Inc. ....	21061	301-768-2800	710-867-0508
		95 Aquahart Road Suite 204			
<b>MASSACHUSETTS</b> ....	<b>Lexington</b> .....	National Semiconductor .....	02173	617-861-6090	710-326-6979
		(North-East Regional Office) 9 Meriam Street Suite 16			
		A/D Systems Sales, Inc. ....	02173	617-861-6370	
		594 Marrett Road			
<b>MICHIGAN</b> .....	<b>Farmington Hills</b> ....	National Semiconductor .....	48018	313-553-0600	810-242-2902
		(District Sales Office) 27650 Farmington Road			
	<b>Grand Rapids</b> .....	Representative of Electronic Products .....	49506	616-942-1320	
		3501 Lake Eastbrook SE			
	<b>Southfield</b> .....	Representative of Electronic Products .....	48075	313-559-1080	810-224-4976
		North Park Office Plaza 17117 West 9-Mile Road Suite 420			

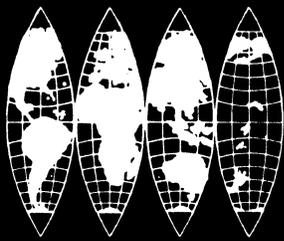


# Manufacturers' Local Offices

## NSC – NATIONAL SEMICONDUCTOR CORPORATION (Cont'd)

			Zip Code	Telephone No.	TWX
2900 Semiconductor Drive, Santa Clara, California .....			95051	408-737-5000	910-339-9240
<b>MINNESOTA</b> .....	<b>Minneapolis</b> .....	National Semiconductor .....	55431	612-888-3060	910-576-3415
		(Regional Office) 8200 Humboldt Avenue S.			
		Stan Clothier Company ** .....	55435	612-944-3456	910-576-3415
		7423 Washington Avenue S.			
<b>MISSOURI</b> .....	<b>Earth City</b> .....	Cen Tech .....	63045	314-731-4220	910-762-0638
		514 Earth City Plaza			
	<b>Raytown</b> .....	Cen Tech .....	64111	816-358-8100	910-777-2007
		6310 Ash			
<b>NEW JERSEY</b> .....	<b>Englewood Cliffs</b> .....	National Semiconductor .....	07632	201-461-2789	710-991-9734
		(Mid-Atlantic Regional Office) 140 Sylvan Avenue			
	<b>Fort Lee</b> .....	New Jersey NECCO .....	07024	201-461-2789	Telex 134-526
		2460 Lemoine Avenue			
<b>NEW MEXICO</b> .....	<b>Albuquerque</b> .....	A. O. Electronics .....	87107	505-883-1003	TWX 910-989-1653
		Post Office Box 6505			
<b>NEW YORK</b> .....	<b>Syracuse</b> .....	National Semiconductor .....	13211	315-455-5868	
(Upstate)		(CAN-AM Regional Office) 104 Pickard Drive			
		Electra Sales Corporation .....	13211	315-455-5783	710-541-0418
		104 Pickard Drive			
	<b>Poughkeepsie</b> .....	National Semiconductor .....	12601	914-462-2380	510-248-0043
		(Regional Office) 576 South Road Room 128			
	<b>Rochester</b> .....	Electra Sales Corporation.....	14619	716-436-4030	
		474 Thurston Road		716-436-4037	
<b>Metropolitan Area</b> .....	<b>Melville</b> .....	LEJ Component Sales .....	11746	516-694-9090	
		401 Broad Hollow Road			
		National Semiconductor .....		516-921-2589	710-479-3512
		(Mid-Atlantic Regional Office)			

\*\* Applications Engineer Available

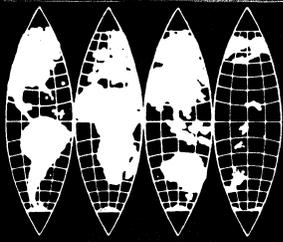


# Manufacturers' Local Offices

## NSC — NATIONAL SEMICONDUCTOR CORPORATION (Cont'd)

			Zip Code	Telephone No.	TWX
2900 Semiconductor Drive, Santa Clara, California.....			95051	408-737-5000	910-339-9240
<b>NORTH CAROLINA</b> ... Highpoint .....	Engineering Devices Corporation .....		27262	919-869-7200	
	Post Office Box 5067				
<b>OHIO</b> .....	Highland Heights ... National Semiconductor .....		44143	216-461-0191	810-427-2972
	(East Central Regional Office)				
	19 Alpha Park				
	Micro-Tec, Inc. ....		44143	216-461-0191	810-427-2972
	19 Alpha Park				
	Columbus .....	Micro-Tec, Inc. ....	43029	614-888-9761/2	
		6076 Busch Blvd.			
		Suite 3			
	Dayton .....	Micro-Tec, Inc. ....	45419	513-294-6441	810-459-1615
		1413 Acorn Drive			
<b>OREGON</b> .....	Beaverton .....	Vantage Corporation .....	97005	503-646-3466	
		3950 SW 102nd Street			
		Suite 122			
<b>PENNSYLVANIA</b> .....	Fort Washington ....	National Semiconductor .....	19034	215-628-8877	510-661-3986
		(Liberty Regional Office)			
		500 Office Center Drive			
	Huntington Valley ...	Omega Electronic Sales, Inc. ....	19006	215-947-4135	510-665-5485
		1 Fairway Palza			
		Philmont Avenue			
		Red Lion Road			
		Suite 210			
<b>TEXAS</b> .....	Dallas .....	National Semiconductor .....	75243	214-690-4552	910-867-4741
		(South-Central Regional Office)			
		13773 North Central Expressway			
		Suite 1132			
	El Paso .....	A. O. Electronics .....	79903	915-545-2363	
		2211 East Missouri Street			
		Suite N-218			
	Garland .....	Carter Associates, Inc. ....	75040	214-276-7151	910-860-5097
		Post Office Box 87			
	Houston .....	Carter Associates Inc. ** .....	77027	713-621-6930	
		3701 West Alabama Street			
		Suite 360			

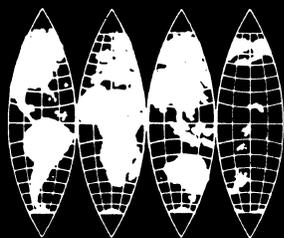
\*\* Applications Engineer Available



# Manufacturers' Local Offices

## NSC – NATIONAL SEMICONDUCTOR CORPORATION (Cont'd)

		Zip Code	Telephone No.	TWX
2900 Semiconductor Drive, Santa Clara, California .....		95051	408-737-5000	910-339-9240
<b>WASHINGTON</b> .....	<b>Bellevue</b> .....			
	National Semiconductor .....	98005	206-454-4600	
	(District Sales Office)			
	300 120th NE Avenue			
	Building 7 - Suite 207			
	Vantage Corporation .....	98005	206-455-3460	
	300 120th NE Avenue			
	Building 7 - Suite 207			
<b>CANADA</b> .....	<b>Bellevue</b> .....			
(Western Provinces)	(Washington)			
	National Semiconductor .....	98005	206-455-3460	
	(District Sales Office)			
	300 120th NE Avenue			
	Building 2 - Suite 205			
	Vantage Corporation .....	98005	206-455-3460	
	300 120th NE Avenue			
	Building 2 - Suite 207			
(Eastern Provinces) .....	<b>Downview</b> .....			
	(Ontario)			
	National Semiconductor .....	M3J 2N5	416-661-8022	
	(District Sales Office)			
	286 Wildcat Road			
	<b>Mississauga</b> .....			
	(Ontario)			
	Canadian Micro Sales, Inc. ....	L4T 1G3	416-677-6633	610-492-4012
	2780 Slough Street			

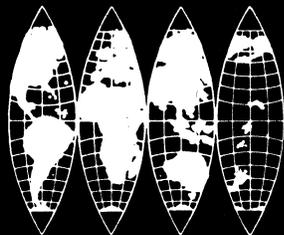


# Manufacturers' Local Offices

## PHIN — PHILIPS GLOEILAMPENFABRIEKEN

			Zip Code	Telephone No.	Cable
PRODUCT DIVISION ELCOMA					
Building BA, Eindhoven, Netherlands .....				<b>(040) 79 11 11</b>	<b>PHILIPS EINDHOVEN</b>
<b>ARGENTINA</b> .....	<b>Buenos Aires</b> .....	Fapasa I.y.C. .... Av. Crovara 2550		<b>652-3983</b>	
<b>AUSTRALIA</b> .....	<b>Lane Cove</b> .....	Philips Industries, Ltd. .... Elcoma Division 67 Mars Road	<b>2066</b>	<b>42 1261</b>	
			<b>N.S.W.</b>		
<b>AUSTRIA</b> .....	<b>Wien</b> .....	Osterreichische Philips .....	<b>A-1101</b>	<b>62 91 11</b>	
		Bauelemente Industrie G.m.b.H. Triesterstrasse 64			
<b>BELGIUM</b> .....	<b>Bruxelles</b> .....	M.B.L.E. .... 80 Rue des Deux Gares	<b>B-1070</b>	<b>523 00 00</b>	
<b>BRAZIL</b> .....	<b>Sao Paulo. SP</b> .....	Ibrape S.A. .... Av. Paulista 2073-S/Loja	<b>01311</b>	<b>278-7144</b>	
<b>CANADA</b> .....	<b>Scarborough</b> .....	Philips Electronics Ltd. ....	<b>M1B 1M8</b>	<b>416-292-5161</b>	Telex <b>06-2221</b>
	<b>(Ontario)</b>	Electron Devices Division 601 Milner Avenue			
<b>DENMARK</b> .....	<b>Kobenhavn NV</b> .....	Miniwatt A/S .....	<b>DK-2400</b>	<b>(01) 69 16 22</b>	
		Emdrupvej 115A			

\* Manufacturer Code inside ( ) can be found in Section 25,  
Manufacturers Code Names & Addresses

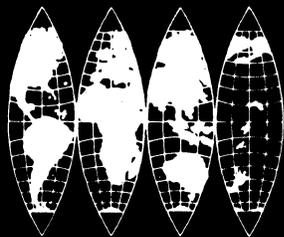


# Manufacturers' Local Offices

## PHIN — PHILIPS GLOEILAMPENFABRIEKEN (Cont'd)

			Zip Code	Telephone No.	Cable
PRODUCT DIVISION ELCOMA					
Building BA, Eindhoven, Netherlands .....				(040)79 11 11	PHILIPS EINDHOVEN
FINLAND .....	Helsinki 10 .....	Oy Philips Ab .....	SF-00100	1 72 71	
		Elcoma Division Kaivokatu 8			
FRANCE .....	Paris 11 .....	R. T. C. (RTCF)* .....	F-75540	355 44 99	
		La Radiotechnique Compelec 130 Avenue Ledru Rollin			
GERMANY .....	Hamburg 1 .....	VALVO (VALG)* .....	D-2	(040) 3296-1	
		UB Bauelemente der Philips GmbH Valvo Haus Burchardstrasse 19			
HONG KONG .....	Kwai Chung N.T. .... (K.T.C.L.)	Philips Hong Kong Ltd. ....	289	12 24 51 21	
		Components Dept. Philips Industrial Building Kung Yip Street			
ITALY .....	Milano .....	Philips S.p.A. ....	I-20124	6994	
		Sezione Elcoma Piazza IV Novembre 3			
JAPAN .....	Tokyo .....	Nihon Philips Corporation .....	108	(435)5204-5	
		Shuwa Shinagawa Bldg. 26-33 Takamawa, 3-chome Minato-ku			
KOREA .....	Seoul .....	Philips Electronics Korea Ltd. ....		44-4202	
		Philips House 260-199 Itaewon-dong Yongsan-ku			

\* Manufacturer Code inside ( ) can be found in Section 25,  
Manufacturers Code Names \* Addresses

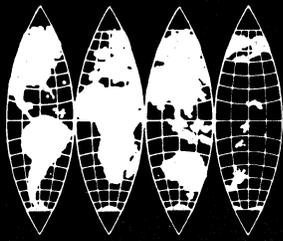


# Manufacturers' Local Offices

## PHIN — PHILIPS GLOEILAMPENFABRIEKEN (Cont'd)

		Zip Code	Telephone No.	Cable
<b>PRODUCT DIVISION ELCOMA</b>				
Building BA, Eindhoven, Netherlands .....			<b>(040)79 11 11</b>	<b>PHILIPS EINDHOVEN</b>
<b>MEXICO</b> .....	<b>Mexico 6, D.F.</b> .....	<b>Electronica S.A. de C.V.</b> .....	<b>5-33-11-80</b>	
		Varsovia No. 36		
<b>NETHERLANDS</b> .....	<b>Eindhoven</b> .....	<b>Philips Nederland B. V.</b> .....	<b>NL-4510</b>	<b>(040) 79 33 33</b>
		Afd. Elonco Boschdijk 525		
<b>NEW ZEALAND</b> .....	<b>Wellington</b> .....	<b>Philips Electronic Industries, Ltd.</b> .....	<b>873-156</b>	
		Elcoma Division 70-72 Kingsford Smith Street		
<b>NORWAY</b> .....	<b>Oslo 4</b> .....	<b>Electronica A/S</b> .....	<b>(02) 150590</b>	
		Vitaminveien 11		
<b>SOUTH AFRICA</b> .....	<b>Johannesburg</b> .....	<b>EDAC (Pty.) Ltd.</b> .....	<b>2001</b>	<b>24/6701</b>
		South Park Lane New Doornfontein		
<b>SPAIN</b> .....	<b>Barcelona 7</b> .....	<b>Copresa S.A.</b> .....	<b>329 63 12</b>	
		Balmes 22		
<b>SWEDEN</b> .....	<b>Stockholm 27</b> .....	<b>Elcoma A.B.</b> .....	<b>S-10250</b>	<b>08/679780</b>
		Lidingovagen 50		
<b>SWITZERLAND</b> .....	<b>Zurich</b> .....	<b>Philips A. G.</b> .....	<b>CH-8027</b>	<b>01/44 22 11</b>
		Elcoma Abteilung Edenstrasse 20		
<b>TAIWAN</b> .....	<b>Taipei</b> .....	<b>Philips Taiwan Ltd.</b> .....	<b>57 13231</b>	
		Elcoma Division San Min Bldg., 3rd Floor 57-1 Chung Shan N. Road		
<b>UNITED KINGDOM</b> ...	<b>London</b> .....	<b>Mullard Ltd. (MULB)*</b> .....	<b>WC1E 7HD</b>	<b>01-580-6633</b>
		Mullard House Torrington Place		
<b>UNITED STATES</b> .....	<b>California</b> .....	<b>Signetics Corporation (SIC)*</b> .....	<b>94086</b>	<b>408-739-7101</b>
		811 East Arques Avenue Sunnyvale		

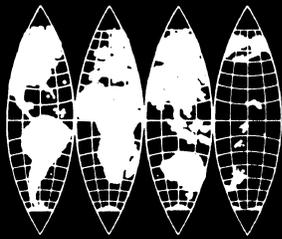
\* Manufacturer Code inside ( ) can be found in Section 25,  
Manufacturers Code Names & Addresses



## Manufacturers' Local Offices

### SGAI — SGS-ATES COMPONENTI ELETTRONICI S.P.A.

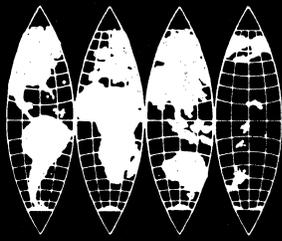
		Zip Code	Telephone No.	Telex	
Via C. Olivetti 2, Agrate Brianza, Italy .....		20041	39-650141	31436	
ENGLAND .....	Aylesbury Bucks .....	SGS-ATES (United Kingdom) Ltd. .... Walton Street	5977	83245	
FRANCE .....	Paris .....	SGS-ATES France SA .....	75643	5842730	(0/25938)
		Residence "Le Palatino" 17, Avenue de Choisy			
GERMANY.....	Wasserburg (Inn) .....	SGS-ATES Deutschland GmbH .....	809	08071-721	05-25143
		Postfach 1269			
ITALY .....	Milano .....	SGS-ATES Componenti Eletttronici S.p.A. ..	20149	4695651	31481
		Via Tempesta 2			
SINGAPORE .....	Singapore .....	SGS-ATES Singapore (PTE) Ltd. ....	12	531411	21412
		Lorong 4 and 6 Toa Payoh			
SWEDEN .....	Marsta .....	SGS-ATES Scandinavia AB .....	19501	0760/40120	10932
		Tingvallavagen 9J Postbox 30			
U.S.A. ....	Waltham .....	SGS-ATES Semiconductor Corporation.....	02154	617-891-3710	923495
	(Massachusetts)	79 Massasoit Street			



# Manufacturers' Local Offices

## SMI – EMM/SEMI, INC.

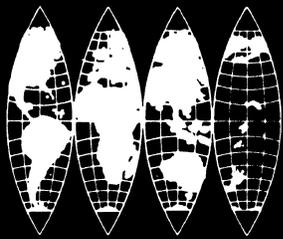
COMMERCIAL MEMORY PRODUCTS		Zip Code	Telephone No.
DIVISION OF ELECTRONIC MEMORIES & MAGNETICS CORP.			
3883 North 28th Avenue, Phoenix, Arizona .....		85017	602-263-0202
<b>CALIFORNIA</b> .....	<b>Los Alamitos</b> .....	<b>Electronic Memories</b> .....	<b>90720</b>
		3662 Katella Suite 216	<b>213-598-8705</b>
	<b>Burlingame</b> .....	<b>Electronic Memories</b> .....	<b>94101</b>
		1633 Bayshore Highway Suite 120	<b>415-692-4250</b>
<b>ILLINOIS</b> .....	<b>Des Plaines</b> .....	<b>Electronic Memories</b> .....	<b>60018</b>
		1400 East Touhy Avenue Suite 440	<b>312-297-7090</b>
<b>MASSACHUSETTS</b> .....	<b>Lexington</b> .....	<b>Electronic Memories</b> .....	<b>02173</b>
		2 Militia Drive	<b>617-861-9650</b>
<b>MINNESOTA</b> .....	<b>Hopkins</b> .....	<b>Electronic Memories</b> .....	<b>55343</b>
		810 South 1st Street Andrews Building, Suite 230	<b>612-933-7115</b>
<b>NEW JERSEY</b> .....	<b>Cherry Hill</b> .....	<b>Electronic Memories</b> .....	<b>08034</b>
		498 North Kings Highway Lafferty Plaza, Suite 105	<b>609-779-7911</b>
<b>NEW YORK</b> .....	<b>Melville</b> .....	<b>Electronic Memories</b> .....	<b>11746</b>
		150 Broad Hollow Road	<b>516-423-5800</b>
<b>TEXAS</b> .....	<b>Dallas</b> .....	<b>Electronic Memories</b> .....	<b>75243</b>
		13773 No. Central Expressway Keystone Park, Suite 1455	<b>214-231-2539</b>



# Manufacturers' Local Offices

## THCF — THOMSON CSF

			Zip Code	Telephone No.	Telex
DIVISION SEMICONDUCTEURS SESCOSEM 50, Rue Jean Pierre Timbaud, BP 120, Courbevoie, France .....			F-92403	788-50-01	SESCO 610560 F
AFRICA (South)	Dunswart	Allied Electric (Pty) Ltd. Post Office Box 6090	1508	52-8232/3	8-7823 Cable "SOLIDSTATE" DUNSWART
AUSTRALIA	Kingsgrove (NSW)	IRH Components Post Office Box 70	2208	50111	21123
AUSTRIA	Wien	Transalpina Electronica Ltd. Elisabethstrasse 8	A 1010	56.15.71	Inland 12 717
BELGIUM NETHERLANDS	Bruxelles 5	Thomson S. A. — N. V. Avenue Louise 363 Bte 10	B-1050	648 64 85	23 113
BRAZIL	Sao Paulo	Thomson CSF Componentes do Brasil ..... Caixa Postal 4854		616.483	TESAFIBRA EMBRATEL SP 309171 SAP PAULO
CANADA	Toronto (Ontario)	Canadian General Electric Co., Ltd. 189 Dufferin Street		416-537-4481	06-23238
DENMARK	Copenhagen	Scan Supply 20 Nannasgade	DK-2200	193 5030	9037 SCAPLY
ENGLAND	Basingstoke	Thomson-CSF (U.K.) Ltd. Ringway House - Bell Road Danneshill Hants		256 29 155	858865
FINLAND	Helsinki 25	OY Sufra AB Ruusulankatu 20 A 12		49.01.37	Pierrejoly Helsinki
FRANCE	Aix En Provence	Sescosem Service Commercial 15, rue Camille Pelletan	F-13102	(91) 27 98 15	410665
	Saint Egreve	Sescosem Service Commercial	F-38120	(76) 758112	204780
GERMANY	Munchen 25	Thomson CSF GmbH Fallstrasse 42	D-8000	89 76 751	522.916



# Manufacturers' Local Offices

## THCF — THOMSON CSF (Cont'd)

			Zip Code	Telephone No.	Telex
DIVISION SEMICONDUCTEURS SESCOSEM					
50, Rue Jean Pierre Timbaud, BP 120, Courbevoie, France .....			F-92403	788-50-01	SESCO 610560 F
ITALY .....	Milano .....	Sescosem Italiana .....	I-20.125	68 84 141	36301 Ducati
		Via Melchiorre Gioia, 72			
MOROCCO .....	Casablanca .....	SFRM .....		27-91-00	21924
		40 Blvd. de la Resistance		27-91-23	
		Palais Mirabeau			
NORWAY .....	Oslo 6 .....	Feiring AS .....		(2) 686360	16 435
		Post Office Box 101			
		Bryn			
PORTUGAL .....	Lisbon .....	Sd. Com Rualdo .....		P.P.C. 33725	16447 Rualdo
		Rua S. Jose 15			Lisbonne
SPAIN .....	San Juan Despi .....	Componentes Electronicos S.A. ....		319.46. 50	53077
	(Barcelona)	Poligono Industrial, Font Santa			
		Calle, H.S./N			
SWEDEN .....	Solna 3 .....	Elektrholm AB .....	S-17 103	82.02.80	19.389
		Dalvagen 12			
SWITZERLAND .....	Berne 9 .....	Modulator S. A. ....	CH-3000	23 21 42	32.431
		Fischerweg 11.13			
U. S. A. ....	California .....	Nucleonic Products Company, Inc. ....	91303	(213)887-1010	651.479
		6660 Variel Avenue			
		Canoga Park			

# 25. MANUFACTURERS CODES, NAMES & ADDRESSES

QPL  
MFR.  
DESIG.

FSCM  
No.

DATA  
MFRS.  
CODE

## MANUFACTURERS' CODES, NAMES, AND ADDRESSES

	ABA	—	Abacus Division, Information Control Corp., 9610 Bellanca Ave., Los Angeles, CA	90045
	D1597— <b>ALGG</b>	*—	<b>AEG-Telefunken, Postfach 1109, D7100 Heilbronn, Germany</b>	
	31471— AMI	*—	American Micro-Systems, Inc., 3800 Homestead Rd., Santa Clara, CA	95051
CDWN—	34335— AMV	*—	Advanced Micro Devices, Inc., 901 Thompson Pl., Sunnyvale, CA	94086
	14506— DTC	—	Data Tech, Div. Penril Corp., 2700 Fairview Rd., Santa Ana, CA	92704
	33297— EAI	*—	Electronic Arrays, Inc., 550 E. Middlefield Rd., Mountain View, CA	94043
	54800— ECD	—	Energy Conversion Devices, Inc., 1675 W. Maple Rd., Troy, MI	48084
	26611— FCAJ	*—	Fujitsu Ltd., IC Division, 1015 Kamikodanaka, Kawasaki, Japan	
	12264— FERB	*—	Ferranti Ltd., Electronics Dept., Gem Mill, Fields New Rd., Chadderton, Oldham OL9 8NP, England	
CFJ —	07263— <b>FSC</b>	*—	<b>Fairchild Semiconductor, 464 Ellis St., M/S 20-1050, Mountain View, CA</b>	<b>94040</b>
CAKK—	14936— GIC	*—	General Instrument Corp., 600 W. John St., Hicksville, NY	11802
CDWO—	HAS	*—	Harris Semiconductor, P.O. Box 883, Melbourne, FL	32901
	92645— HITJ	*—	Hitachi, Ltd., Semiconductor & IC Div., 1450 Josuihonmachi, Kodaira City, Tokyo, Japan	
CDPR —	32293— INL	*—	Intersil, Inc., 10900 No. Tantau Ave., Cupertino, CA	95014
	D8849— INTG	—	Intermetall, Halbleiterwerk der Deutsche ITT Inc., GmbH, 78 Freiburg, Hans-Bunte-Strasse 19, Germany	
	34649— ITL	*—	<b>Intel Corporation, 3065 Bowers Ave., Santa Clara, CA</b>	<b>95051</b>
CIT —	15238— ITT	*—	ITT Semiconductors, 74 Commerce Way, Woburn, MA	01801
	ITTB	—	ITT Semiconductors, Maidstone Rd., Foots Cray, Sidcup, Kent, England	
	01619— MATJ	*—	Matsushita Electronics Corp. (Panasonic), 1 Kotari-Yakemachi, Nagaokakyo, Kyoto 617, Japan	
	90144— MITJ	—	Mitsubishi Electric Corp., Kita-Itami Works, 4-1 Muzuhara, Itami-Shi, Hyogo-Ken, Post Code 664, Japan	
	50364— MMI	*—	<b>Monolithic Memories, Inc., 1165 E. Arques Ave., Sunnyvale, CA</b>	<b>94086</b>
	33214— MON	—	Aydin-Monitor, 401 Commerce Dr., Fort Washington, PA	19034
	50088— MOS	*—	Mostek Corp., 1215 W. Crosby Rd., Carrollton, TX	75000
CGG —	04713— MOTA	*—	Motorola Semiconductor Products, Inc., 5005 E. McDowell Rd., HO500, Phoenix, AZ	85017
	51284— MTY	*—	MOS Technology, Inc., Valley Forge Corporate Ctr., 950 Rittenhouse Rd., Norristown, PA	19401
	92726— MULB	*—	Mullard Ltd., Mullard House, Torrington Pl., London WC1E 7HD, England	
	94091— NECJ	*—	Nippon Electric Co., Ltd., 1753 Shimonumabe, Nakahara-ku, Kawasaki, Japan	

\* — See Section 26 for  
Manufacturers Logos

Manufacturers shown in bold print have local offices,  
which are included in Section 24 of this D.A.T.A. BOOK

# 25. MANUFACTURERS CODES, NAMES & ADDRESSES



## MANUFACTURERS' CODES, NAMES, AND ADDRESSES

QPL MFR. DESIG.	FSCM No.	DATA MFRS. CODE	MANUFACTURERS' CODES, NAMES, AND ADDRESSES
	<b>NECM</b>	*-	<b>NEC Microcomputers, Inc., Five Militia Dr., Lexington, MA 02173</b>
	54335 - NIT	*-	Nitron, 10420 Bubb Rd., Cupertino, CA 95014
	08257 - NPC	*-	Nucleonic Products Co., Inc., 6660 Variel Ave., Canoga Park, CA 91304
CCXP -	27014 - <b>NSC</b>	*-	<b>National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, CA 95051</b>
	S4071 - OKIJ	-	OKI Electric Industry Co., Ltd., Electronic Prod. Sec. Intl. Div., 10-3 Shibaura 4-chome, Minato-ku, Tokyo 108, Japan
	08967 - <b>PHIN</b>	-	<b>N. V. Philips Gloeilampenfabrieken, Product Div., Elcoma, Bldg. BA, Eindhoven, Netherlands</b>
	K0467 - PLSB	-	Plessey Semiconductors, Cheney Manor, Swindon, Wiltshire, England
		RAG	Ragen Semiconductor, Inc., 53 So. Jefferson Rd., Whippany, NJ 07981
CRC -	02735 - RCA	*-	RCA Corporation, Solid State Div., Route 202, Somerville, NJ 08876
	12556 - RTCF	-	R. T. C. LaRadiotechnique-Compelec, 130, Ave. Ledru-Rollin, 75540 Paris Cedex 11, France
CRP -	07933 - RTN	*-	Raytheon Company, 350 Ellis St., Mountain View, CA 94042
	A3500 - <b>SGAI</b>	*-	<b>SGS-ATES Componenti Elettronici S.p.A., Via C. Olivetti 2, 20041 Agrate Brianza, Milan, Italy</b>
CDKB -	18324 - SIC	-	Signetics Corp., 811 E. Arques Ave., Sunnyvale, CA 94086
	92346 - SIEG	*-	Siemens Aktiengesellschaft, Semicon. Div., Balanstrasse 73, D8000, Munich 8, Germany
		<b>SMI</b>	<b>EMM/SEMI, A Sub. of Electronic Memories, 3883 No. 28th Ave., Phoenix, AZ 85017</b>
CDGD -	22229 - SOD	*-	Solitron Devices, Inc., 8808 Balboa Ave., San Diego, CA 92123
	31019 - SSS	*-	Solid State Scientific, Inc., Montgomeryville, PA 18936
		SST	Solid State, Inc., 46 Farrand St., Bloomfield, NJ 07003
	27318 - SWM	-	Stewart-Warner Microcircuits, Inc., 730 E. Evelyn Ave., Sunnyvale, CA 94086
CCAB -	03877 - TEC	-	Transitron Electronic Corp., 168-182 Albion St., Wakefield, MA 01880
	F5602 - <b>THCF</b>	-	<b>Thomson CSF, Div. Semiconducteurs SESCOSEM, 50 rue Jean Pierre Timbaud, BP120, 92403 Courbevoie, France</b>
CGO -	01295 - TII	*-	Texas Instruments, Inc., Components Group, P.O. Box 5012, Dallas, TX 75222
		TRW	TRW Monolithic IC's, 300 W. "O" St., Ogalalla, NB 69153
	15818 - TSC	*-	Teledyne Semiconductor, 1300 Terra Bella Ave., Mountain View, CA 94043
	D2540 - VALG	-	Valvo GmbH, P.O. Box 993, D2000, Hamburg 1, Germany
		WDC	Western Digital Corp., 3128 Red Hill Ave., Box 2180, Newport Beach, CA 92663
	07764 - WLD	*-	Wyle Computer Products, 3200 Magruder Blvd., Hampton, VA 23666

\* - See Section 26 for  
Manufacturers Logos

Manufacturers shown in bold print have local offices,  
which are included in Section 24 of this D.A.T.A.BOOK

26. MANUFACTURERS LOGOS

IN MFR.  
CODE ORDER



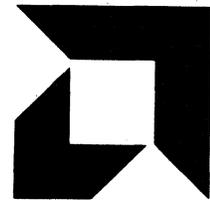
**TFK**

*(Product Identifier)*

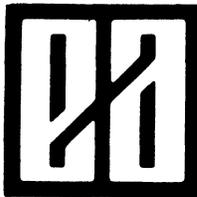
ALGG – AEG-Telefunken



AMI – American Microsystems, Inc.



AMV – Advanced Micro Devices, Inc.



EAI – Electronic Arrays, Inc.



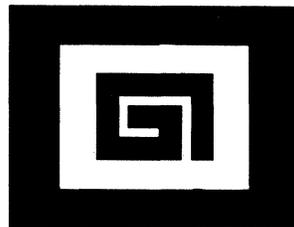
FCAJ – Fujitsu Ltd.



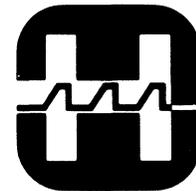
FERB – Ferranti, Ltd.



FSC – Fairchild Semiconductor



GIC – General Instrument Corp.



HAS – Harris Semiconductor

26. MANUFACTURERS LOGOS

IN MFR.  
CODE ORDER



HITJ – Hitachi, Ltd.



INL – Intersil, Inc.



ITL – Intel Corp.



ITT – ITT Semiconductors



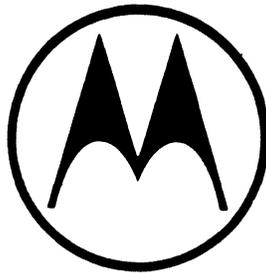
MATJ – Matsushita Electronics Corp.



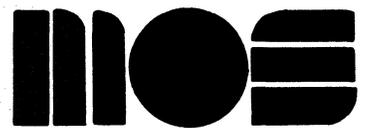
MMI – Monolithic Memories, Inc.



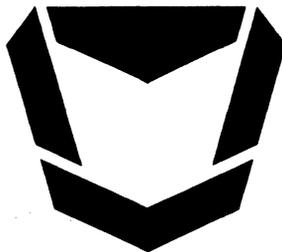
MOS – Mostek Corp.



MOTA – Motorola Semiconductor Products, Inc.

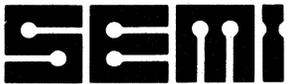


MTY – MOS Technology Inc.



MULB – Mullard Ltd.

**26. MANUFACTURERS LOGOS** IN MFR.  
CODE ORDER

 <p>NECJ – Nippon Electric Co., Ltd.</p>	<p>NEC microcomputers, inc.</p> <p>NECM – NEC Microcomputers, Inc.</p>	 <p>A DIVISION OF MCDONNELL DOUGLAS CORPORATION</p> <p>NIT – Nitron</p>
 <p>(Product Identifier)</p> <p>NPC – Nucleonic Products Co., Inc.</p>	 <p>NSC – National Semicon. Corp.</p>	 <p>RCA – RCA Corp., Solid State Div.</p>
 <p>RTN – Raytheon Company</p>	 <p>SGAI – SGS-ATES Componenti Elettronici S.p.A.</p>	<p>SIEMENS</p> <p>SIEG – Siemens Aktiengesellschaft, Semiconductor Div.</p>
	 <p>Unique Semiconductor Identifier</p>  <p>SMI – Semi, Inc., A Div. of E.M.&amp; M. Corp.</p>	

**26. MANUFACTURERS LOGOS**

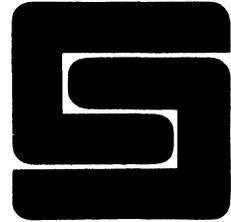
IN MFR.  
CODE ORDER



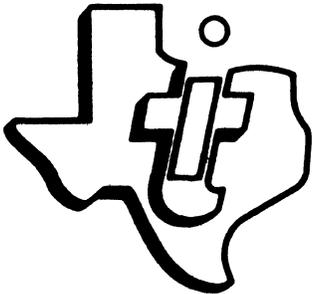
SOD – Solitron Devices, Inc.



SSS – Solid State Scientific, Inc.



SST – Solid State, Inc.



TII – Texas Instruments, Inc.



TSC – Teledyne Semiconductor



WDC – Western Digital Corp.

**WYLE COMPUTER PRODUCTS**  
A DIVISION OF WYLE LABORATORIES

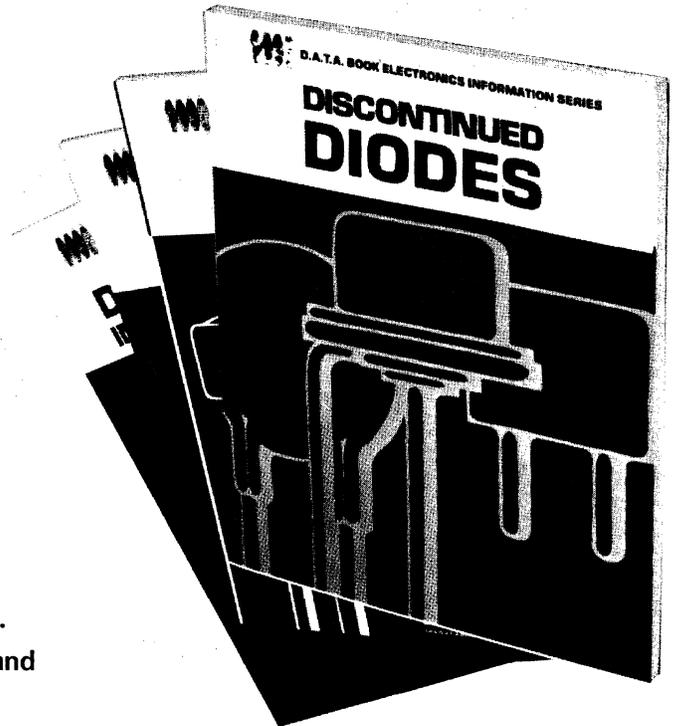
WLD – Wyle Computer Products

# How to find replacements

Compare electrical characteristics — not just type numbers!

Substituting for an obsolete device by type number alone can be difficult, frustrating and downright dangerous to equipment. You need the complete electrical and physical characteristics of the obsolete device to be sure of your substitution. And that's what the four D.A.T.A.BOOKS of discontinued devices give you.

They are the only comprehensive sources for information on devices no longer manufactured. The technical data presentation coincides with that of the current D.A.T.A.BOOK in the same field, providing you with the fastest, most accurate method of selecting optimum substitutions and replacements for discontinued types. All ex-manufacturers are identified. Each book is updated and published annually.



## D.A.T.A.BOOK OF DISCONTINUED TRANSISTORS

More than 11,500 types — along with characteristics — which have become obsolete since 1956.

Technical data presentation coincides with that of the TRANSISTOR D.A.T.A.BOOK to facilitate substitutions. Together they provide the fastest, most accurate method of selecting optimum replacement for discontinued types.

**\$20.50**

## D.A.T.A.BOOK OF DISCONTINUED THYRISTORS

Provides you with technical information on SCR's and PNP devices which are no longer manufactured. 7100 discontinued SCR's from all known manufacturers which appeared at any time in the THYRISTOR D.A.T.A.BOOK.

**\$15.50**

## D.A.T.A.BOOK OF DISCONTINUED INTEGRATED CIRCUITS

More than 18,000 worldwide Digital and Linear IC's — along with characteristics — which have become obsolete since 1965 — are included. Technical presentation coincides with that of the DIGITAL IC, LINEAR IC and MEMORY D.A.T.A.BOOKS to facilitate substitution and replacement.

**\$20.50**

## D.A.T.A.BOOK OF DISCONTINUED SEMICONDUCTOR DIODES

Facilitates substitution when used with the SEMICONDUCTOR DIODE D.A.T.A.BOOK. Lists over 24,000 types no longer manufactured — reference diodes, general purpose, standard/fast recovery rectifiers, MW mixer and video detectors, varactors, tunnel diodes and more. A "must" for complete replacement data.

**\$20.50**

Check your needs and order on THE D.A.T.A.BOOKS order card in front of book.

**CORDURA**

**D.A.T.A., INC.**

A Cordura Company

45 U.S. Highway 46  
Pine Brook, New Jersey 07058  
Telephone 201-227-3740

# INTERPRETER SYMBOLS & CODES

## TYPE No. CROSS INDEX & TECHNICAL SECTIONS

△ Indicators of separate manufacturers producing same type number (non-JEDEC) whose characteristics are not the same.  
 # This manufacturer-identifying symbol (assigned by D.A.T.A.) is an integral part of the type number (in Type No. Cross Index, Technical Data Sections) to avoid the possibility of confusing the device of one manufacturer with the devices of the others.

Example:	Type No.	Manufacturer	Description
(simulated information)	DD31	CCD	Shift Register
	DD31	CLC	RAM
	DD31	ZEL	ROM

# 1, #2... The modifier is designated by D.A.T.A. to distinguish between type no. designations which give only one type no. but have more than one electrical function or package.  
 %... (Sect. 4 & 6) Device requires companion device to complete code; see logic drawing.  
 - PR... Suffix indicates device is a preliminary type.  
 - RT... Suffix indicates device is a replacement type.

LINE NO.
▼ - New Type
◆ - Revised Specification
# - Manufactured Outside U.S.A.

## SYMBOLS & CODES COMMON TO ALL TECHNICAL SECTIONS

NOTE: UNLESS OTHERWISE INDICATED, ALL CHARACTERISTICS APPLY OVER THE ENTIRE OPERATING TEMPERATURE RANGE.

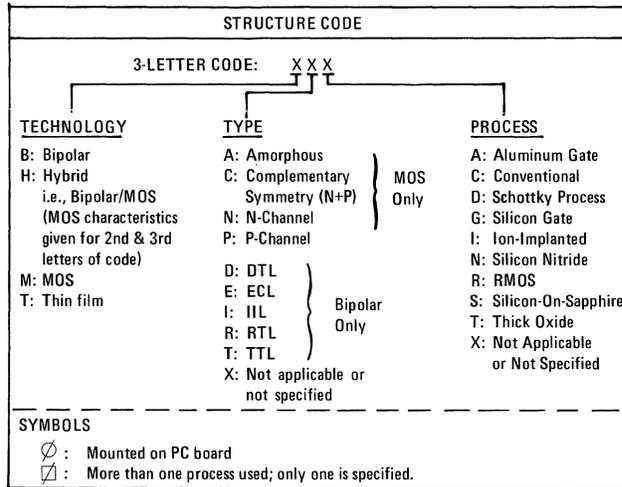
MAX. OPERATING POWER DISSIPATION
† - Typical
* - Minimum
% - Per bit
◆ - Quiescent power dissipation
◇ - Absolute maximum
∅ - At 25°C

INPUT LOGIC LEVEL: MAX. '0'
† - Typical
* - Minimum
% - Output (not Input) value given. (This also applies for value given for '1' level.)
◆ - Bipolar load only; can be adjusted for the MOS load. (Applies for '1' level value, also)
∅ - At 25°C

MINIMUM OUTPUT SINK CURRENT
† - Typical
# - Maximum
◆ - Minimum output source current
◇ - Minimum output high current
△ - Maximum output leakage current
∅ - Minimum driving (fanout) current
◇ - Absolute max. rated output current
∅ - At 25°C

OUTLINE DRAWINGS
CY - TO 5-type (non-JEDEC)
CH - Chip Package
FL - Flat package (non-JEDEC)
ML - Molded or encapsulated package not included in other categories.
MO - Standard JEDEC outline
PL - Printed circuit board
TO - Standard JEDEC outline
☑ - Package style only shown; no dimensions.

LOGIC/BLOCK DRAWINGS	
A - RAMs	E - Code Converters
B - ROMs	F - Shift Registers
C - Character Generators	Z - Special Devices
§ - Optional Terminal Connections available; consult manufacturer	



**OPERATING TEMPERATURE CODE**

0- 0 up to 10°C	▼ - USED IN NEGATIVE COLUMN TO INDICATE VALUE IS POSITIVE
1- 10 up to 20°C	
2- 20 up to 30°C	EXAMPLES OF OPERATING TEMP. RANGE CODE:
3- 30 up to 40°C	
4- 40 up to 50°C	
5- 50 up to 60°C	
6- 60 up to 70°C	
7- 70 up to 80°C	
8- 80 up to 90°C	
9- 90 up to 100°C	
A- 100 up to 110°C	
B- 110 up to 120°C	
C- 120 up to 130°C	
D- 130 up to 140°C	
E- 140 up to 150°C	
F- 150 up to 160°C	
G- 160 up to 170°C	
H- 170 up to 180°C	
J- 180 up to 190°C	
K- 190 up to 200°C	

## 2. READ-WRITE MEMORIES (RAMS)

LINE No.	TYPE No.	ORGANIZATION		STRUCTURE CODE	MAX ACCESS TIME (s)	MAX WRITE CYCLE TIME (s)	MAX OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MIN OUTPUT SINK CURRENT (A)	MIN CLOCK FREQ. (Hz)	MIN OPER. TEMP. RANGE (°C)	DRAWINGS
		1 No. WORDS	2 BITS PER WORD					NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)				
3	4	5	7	8	10	11	13	15	16						

**3** § - No. of words is variable; types listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type no.  
 § - More than one circuit

**4** § - No. of bits/word variable  
 △ - Multi-word output

• SEE SYMBOLS AND CODES COMMON TO ALL TECHNICAL SECTIONS

▼ TYPE NO. SYMBOLS AND CODES AT TOP OF INTERPRETER CARD

**5** LETTER  
 D - Dynamic  
 S - Static

**SYMBOL**  
 % - Type can be operated in either mode (dynamic or static); listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type no.  
 § - Multifunction circuit; see circuit diagram

**7** † - Typical  
 \* - Minimum  
 § - Propagation delay  
 ∅ - At 25°C  
 ◆ - Other than 25°C

**8** † - Typical  
 \* - Minimum  
 § - Min. write-pulse width  
 △ - Max. read-write cycle time  
 ∅ - At 25°C  
 % - Sum of min. write-pulse width and max. write-pulse delay time

**10 11** # - Absolute max.

**13** † - Typical  
 # - Maximum  
 △ - Open collector/drain output  
 § - Three-state output

**15** ◆ - V<sub>in</sub>

**16** † - Typical  
 # - Maximum  
 △ - Max. refresh time (inverted)  
 ∅ - At 25°C

NOTE: This column applies for dynamic (not static) devices.

# INTERPRETER SYMBOLS & CODES

## 3. READ ONLY MEMORIES (ROMS)

LINE No.	TYPE No.	ORGANIZATION		OP. MODE	MAX. ACCESS TIME (s)	MAX. OPER. POWER DISS. (W)	RATED POWER SUP. SPAN	INPUT LOGIC LEVELS		MIN. OUTPUT SINK CURRENT (A)	OPER. TEMP. RANGE (°C)	GENERAL DESCRIPTION	DRAWINGS	
		1. No. WORDS	2. BITS PER WORD					3. PROG. CODE	4. STRUCTURE CODE				MAX. '0' (V)	MIN. '1' (V)
3	▼	3	4	5	7	9	10	12	14	17				

IN ORDER OF (1) No. WORDS (2) No. BITS/WORD (3) OP. MODE (4) STRUCT. CODE (5) MAX. ACC. TIME (6) TYPE No.

**3** \$ - No. of words is variable; types listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type no.  
§ - More than one circuit

**4** \$ - No. of bits/word is variable

**14** ◆ -  $V_{in}$

**5** 2-LETTER CODE: X X

<p><b>OPERATING MODE</b></p> <p>D: Dynamic (see description column for max. refresh time or min. clock freq.)</p> <p>S: Static</p>	<p><b>PROGRAM CODE</b></p> <p>C: Mask programmable: custom program</p> <p>E: Electrically programmable</p> <p>S: Mask programmable: standard program (see the description column for program).</p>
--	--

---

**SYMBOLS**

‰: Type can be operated in either mode (dynamic or static): listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type number; see Cross Index.

**7** † - Typical  
\* - Minimum  
\$ - Propagation delay  
Δ - Cycle time  
⊙ - At 25°C  
◆ - Other than 25°C

**17** PR - Program  
fc - Min. clock frequency  
TR - Max. refresh time  
Vol - Volatile  
FO - Fanout  
CS - Chip select  
Std - Standard  
KE - Key encoder  
TA - Transistor Array device  
RMM - Read mostly memory  
PLA - Programmable Logic Array - no. of words represents no. of product terms  
EPROM - Electrically programmable ROM

**9 10** # - Absolute max.

**12** † - Typical  
# - Maximum  
Δ - Open collector/drain output  
§ - Three-state output

## 4. CHARACTER GENERATORS

LINE No.	TYPE No.	USE CODE	No. CHARACTERS	BITS PER CHAR.	No. OUTPUTS	STRUCTURE CODE	MAX. ACCESS TIME (s)	MAX. OPER. POWER DISS. (W)	RATED POWER SUP. SPAN	INPUT LOGIC LEVELS		MIN. OUTPUT SINK CURRENT (A)	OPER. TEMP. RANGE (°C)	DRAWINGS
										3. No. BITS/CHAR.	4. No. OUTPUTS			
3	▼	3	4	5	6	8	10	11	13	15				

IN ORDER OF (1) USE CODE (2) No. CHARACTERS (3) No. BITS/CHAR. (4) No. OUTPUTS (5) STRUCT. CODE (6) TYPE No.

**3** 3-LETTER CODE X X X

FONT

<p><b>OPERATING MODE</b></p> <p>D: Dynamic</p> <p>S: Static</p>	<p>A: ASCII</p> <p>B: Alpha</p> <p>C: Custom</p> <p>E: EBCDIC</p> <p>H: Hollerith (compressed)</p> <p>N: Numeric</p> <p>S: Selectric</p>	<p><b>DISPLAY</b></p> <p>A: Row or Column scan</p> <p>C: Raster: Column Scan</p> <p>R: Raster: Row Scan</p> <p>S: Segment or dot</p> <p>T: CRT</p>
---	--	--

---

**SYMBOLS**

§ - Does not scan complete line; scans characters by sections

☐ - Scans two lines at the same time.

‰ - Operates in more than one mode; indicated by the addition of D.A.T.A. modifier (#1, #2, etc.) to type number

\$ - Includes Japanese font

⊙ - Includes Greek font

**4** \$ - No. of characters variable; types listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type number.  
☐ - No. shown represents only half of complete code; other half is generated by companion device.

**8** † - Typical  
\* - Minimum  
\$ - Propagation delay  
Δ - Cycle time  
⊙ - At 25°C  
◆ - Other than 25°C

**5** \$ - No. of bits/character is variable.  
☐ - Two devices required to generate complete scan.  
§ - No. of bits/character includes shift control.

**10 11** # - Maximum

**6** LETTER CODE:  
A: 7 x 8 Array

**13** † - Typical  
# - Maximum  
Δ - Open collector/drain output  
§ - Three-state output

**SYMBOLS:**  
§ : Individual characters scanned by one-half the outputs

**15** ◆ -  $V_{in}$

• SEE SYMBOLS AND CODES COMMON TO ALL TECHNICAL SECTIONS

▼ TYPE NO. SYMBOLS AND CODES AT TOP OF FIRST INTERPRETER CARD

INTERPRETER  
SYMBOLS & CODES

**SYMBOLS & CODES COMMON  
TO ALL TECHNICAL SECTIONS**

LINE NO.
▼ - New Type
◆ - Revised Specification
# - Manufactured Outside U.S.A.

NOTE: UNLESS OTHERWISE INDICATED, ALL CHARACTERISTICS APPLY OVER THE ENTIRE OPERATING TEMPERATURE RANGE.

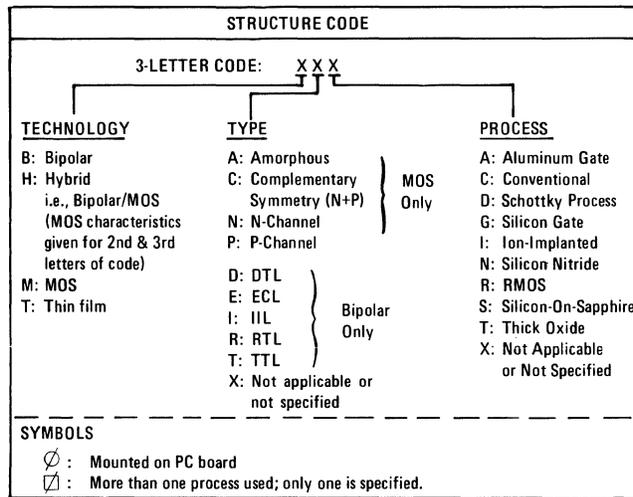
OUTLINE DRAWINGS	
CY	- TO 5-type (non-JEDEC)
CH	- Chip Package
FL	- Flat package (non-JEDEC)
ML	- Molded or encapsulated package not included in other categories.
MO	- Standard JEDEC outline
PL	- Printed circuit board
TO	- Standard JEDEC outline
☑	- Package style only shown; no dimensions.

MAX. OPERATING POWER DISSIPATION	
†	- Typical
*	- Minimum
%	- Per bit
◆	- Quiescent power dissipation
☑	- Absolute maximum
⊘	- At 25°C

INPUT LOGIC LEVEL: MAX. '0'	
†	- Typical
*	- Minimum
%	- Output (not Input) value given. (This also applies for value given for '1' level.)
◆	- Bipolar load only; can be adjusted for the MOS load. (Applies for '1' level value, also)
⊘	- At 25°C

MINIMUM OUTPUT SINK CURRENT	
†	- Typical
#	- Maximum
\$	- Minimum output source current
△	- Minimum output high current
◆	- Maximum output leakage current
%	- Minimum driving (fanout) current
☑	- Absolute max. rated output current
⊘	- At 25°C

LOGIC/BLOCK DRAWINGS	
A	- RAMs
B	- ROMs
C	- Character Generators
E	- Code Converters
F	- Shift Registers
Z	- Special Devices
§	- Optional Terminal Connections available; consult manufacturer



**OPERATING TEMPERATURE CODE**

▼ - USED IN NEGATIVE COLUMN TO INDICATE VALUE IS POSITIVE

EXAMPLES OF OPERATING TEMP. RANGE CODE:

5 C	
Min. value Lies between -50°C and -60°C	Max. value Lies between +120°C and +130°C
O R	
1 ▼ 8	
Min. value Lies between +10°C and +20°C	Max. value Lies between +80°C and +90°C

0- 0 up to 10°C  
1- 10 up to 20°C  
2- 20 up to 30°C  
3- 30 up to 40°C  
4- 40 up to 50°C  
5- 50 up to 60°C  
6- 60 up to 70°C  
7- 70 up to 80°C  
8- 80 up to 90°C  
9- 90 up to 100°C  
A- 100 up to 110°C  
B- 110 up to 120°C  
C- 120 up to 130°C  
D- 130 up to 140°C  
E- 140 up to 150°C  
F- 150 up to 160°C  
G- 160 up to 170°C  
H- 170 up to 180°C  
J- 180 up to 190°C  
K- 190 up to 200°C

**6. CODE CONVERTERS**

LINE No.	TYPE No.	CONVERSION CODE	No. WORDS	No. CODE BITS	M O D	STRUC TURE CODE	MAX ACCESS TIME (s)	MAX OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MIN OUTPUT CURRENT (A)	OPER. TEMP. RANGE CODE	LOGIC/BLOCK DRAWINGS
									NEG. (V)	POS. (V)	MAX '0' (V)	MIN '1' (V)			
3	4	5	6	7	8	10	12	13	15	17					

**3 4 NUMBER:**

- 1 - USASCII
- 2 - EBCDIC
- 3 - Selectric
- 4 - BCD
- 5 - Binary
- 6 - Hollerith
- 7 - 96-column
- 8 - Key Encoded
- 9 - Custom
- 10 - Baudot
- 11 - EIA RS244A

SYMBOL: ("From" column)

% - Device has more than one conversion capability; listed on separate lines.

**5**

- \$ - No. of words is variable; types listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type no.
- § - More than one circuit
- ◆ - No. of words per separate code conversion

**6 7**

- ☑ - Includes even parity bit
- ⊘ - Includes odd parity bit
- § - Includes both odd and even parity bits

**8**

- D - Dynamic
- S - Static

**SYMBOL**

- % - Type can be operated in either mode (dynamic or static); listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type no.

**10**

- † - Typical
- \* - Minimum
- \$ - Propagation delay
- △ - Cycle time
- ⊘ - At 25°C
- ◆ - Other than 25°C
- % - Key bounce delay

**12 13**

- # - Absolute max.

**15**

- † - Typical
- # - Maximum
- △ - Open collector/drain Output
- § - Three-state output

**17**

- ◆ - V<sub>in</sub>

• SEE SYMBOLS AND CODES COMMON TO ALL TECHNICAL SECTIONS

▼ TYPE NO. SYMBOLS AND CODES AT TOP OF INTERPRETER CARD

# INTERPRETER SYMBOLS & CODES

## 7. SHIFT REGISTERS

LINE No.	TYPE No.	ORGANIZATION		OPER. CODE	MAX. WORST CASE FREQ. (Hz)	MAX. STRUCTURE CODE	MAX. OPER. POWER DISS. (W)	RATED POWER SUP. SPAN		INPUT LOGIC LEVELS		MAX. PROP. DELAY (s)	MIN. OUTPUT SINK CURRENT (A)	MIN. OPER. CLOCK FREQ. (Hz)	OPER. TEMP. RANGE CODE	DRAWINGS	
		1. BITS PER REGISTER	2. No. REGS					NEG. (V)	POS. (V)	MAX. '0' (V)	MIN. '1' (V)					LOGIC/BLOCK	OUTLINE
3	4	5	6	9	10	12	13	15	16								

IN ORDER OF: (1) No. BITS/REG (2) No. REGS (3) OP. CODE (4) MAX. W/C FREQ. (5) STRUCTURE CODE (6) TYPE No.

- 3** ‡ - No. of bits/register made variable by internal gating
- § - Individual registers contain different numbers of bits; max. no. is specified (see schematic)
- △ - Accumulator
- ◻ - No. of bits/register made variable by custom programming; max. no. is specified

- 4** ‡ - Separate input and/or output is made available for connection to intermediate stages

- 6** † - Typical
- \* - Minimum
- △ - Max. clock rate
- ⊘ - Max. toggle freq.
- ⊘ - At 25°C
- ◻ - Data repetition rate

- 9 10** # - Absolute max.

- 12** † - Typical
- # - Maximum
- △ - Open collector/drain output
- § - Three-state output

- 13** † - Typical
- \* - Minimum
- △ - Transition time
- § - Average propagation delay
- ⊘ - At 25°C
- ◻ - Read Access Time

- 15** ◆ -  $V_{in}$

- 16** † - Typical
- # - Maximum
- △ - Max. refresh time (inverted)
- ⊘ - At 25°C

NOTE: This column applies for dynamic (not static) devices

**5** 3-LETTER CODE: X X X

<b>INPUT</b> P: Parallel S: Series	<b>OUTPUT</b> P: Parallel S: Series	<b>OPERATING MODE</b> D: Dynamic S: Static C: Charge Coupled (CCD)
--	---	---

---

**SYMBOLS**

- ‡ - Chip contains associated circuitry
- § - Multifunction circuit; application depends on external connections
- ⊘ - Type can be used in either dynamic or static mode; listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type no. (see Cross Index)
- ▼ - FIFO Memory (1st In, 1st Out)
- \* - Device contains additional memory storage (see logic diagram)

## 20. SPECIAL MEMORY DEVICES

LINE No.	TYPE No.	FUNCTION CODE	ORGANIZATION		MODESTRUC. CODE	MAX. ACCESS TIME (s)	MAX. OPER. PWR. DISS. (W)	RATED PWR. SUPPLY SPAN		INPUT LOGIC LEVELS		MIN. SINK CURRENT (A)	OUTPUT CURRENT (A)	TEMP. RANGE CODE	GENERAL DESCRIPTION	DRAWINGS	
			1. No. WORDS	2. No. WORDS				3. BITS per WORD	4. OP. CODE	5. PROG. CODE	NEG. (V)					POS. (V)	MAX. '0' (V)
3	4	5	6	8	10	11	13	15	18								

IN ORDER OF: (1) FUNCTION CODE (2) NO. WORDS (3) BITS/WD (4) OP. MODE (5) STRUCTURE CODE (6) TYPE NO.

- 3** ATN - Arc Tan
- CAM - Content Addressable Memory (CAM)
- COS - Cosine
- MUL - Multipliers
- PLA - Programmable Logic Array (PLA)
- QBF - Quick Brown Fox
- RYG - Rhythm
- SCN - Sine-Cosine
- SIN - Sine
- TRA - Transistor Array
- SYS - Special Memory Systems and Subsystems

- 4** ‡ - No. of words is variable; types listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type no.
- § - More than one circuit

- 5** ‡ - No. of bits/word is variable

**6** 2-LETTER CODE: X X

<b>OPERATING MODE</b> D: Dynamic (see description column for max. refresh time or min. clock freq.) S: Static	<b>PROGRAM CODE</b> C: Mask programmable: custom program E: Electrically programmable S: Mask programmable: standard program (see the description column for program) W: Addressable writing
---	--

---

**SYMBOLS**

- ⊘ : Type can be operated in either mode (dynamic or static); listed on separate lines with D.A.T.A. modifiers (#1, #2, etc.) added to type number; see Cross Index.

- 8** † - Typical
- \* - Minimum
- § - Propagation delay
- △ - Cycle time
- ⊘ - At 25°C
- ◆ - Other than 25°C

- 10 11** # - Absolute max.

- 13** † - Typical
- # - Maximum
- △ - Open collector/drain output
- § - Three-state output

- 15** ◆ -  $V_{in}$

- 18** RHY - Rhythms
- VAR - Variable
- VOL - Volatile

• SEE SYMBOLS AND CODES COMMON TO ALL TECHNICAL SECTIONS

▼ TYPE NO. SYMBOLS AND CODES AT TOP OF FIRST INTERPRETER CARD

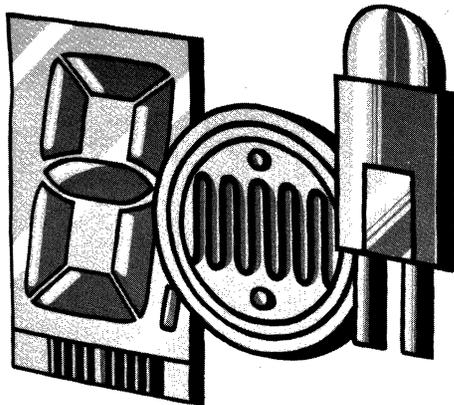
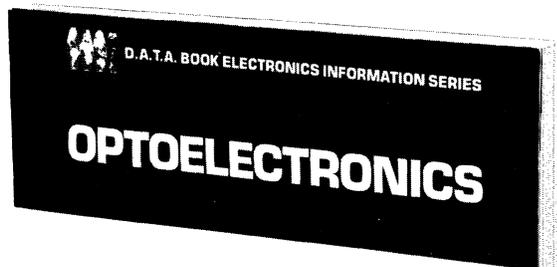
The D.A.T.A.BOOK® of worldwide

# OPTOELECTRONICS

## A COMPREHENSIVE REFERENCE TO INTERNATIONAL DEVICES AND ASSEMBLIES.

WITH THIS BOOK ONLY—you can compare all available optoelectronics devices manufactured in the U.S. and around the world — analyze how they function, see what they look like, know who makes them.

With the explosion of interest in optoelectronics devices comes the demand for a single, comprehensive, easy-to-use engineering guide to international devices and assemblies. D.A.T.A., Inc. meets the need with OPTOELECTRONICS.



### 1. Easy to Use Technical Sections

Optoelectronic products are organized in standardized technical sections. Within sections, devices are sequenced in order of key parameters for easy comparison. The specific electrical, optical and physical characteristics of each device are fully detailed.

### 2. Thorough Coverage of the Industry

You stay up-to-date — book is published new every six months — with detailed information about. . .

#### Emitters:

- visible LED - red, green, orange, yellow
- infrared LED
- LED arrays

#### Photocouplers:

Visible & infrared source coupled to:

- LDR
  - transistors
  - diodes
  - darlington
  - thyristors
  - circuits
- TO-, Axial, DIP cases

#### Displays:

7-segment, hexadecimal, dot matrix, special codes/symbols:

- LED
- liquid crystal
- gas discharge
- display arrays

#### Sensors:

- photodiode
- phototransistor
- photocircuits
- photothyristor
- photodarlington
- pin photodiodes
- LDR (photoconductive cells)
- photovoltaic cells & arrays

#### Special Detectors:

- gap detectors
- edge contact
- reflex types
- card/paper tape arrays
- image sensors

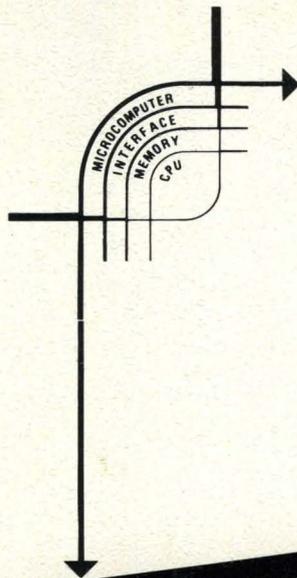
#### Supplementary Sections:

- JEDEC and Military Types
- Manufacturers Names and Addresses
- Schematic/Outline Drawings

3. Schematic Drawings complete the electrical data on all products.

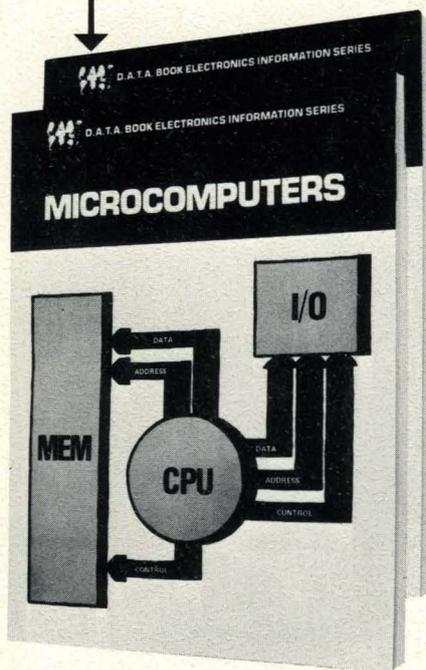
4. Outline Drawings provide fast answers to your packaging questions.

ORDER ON D.A.T.A.BOOKS® ORDER CARD IN FRONT OF BOOK



# The MICROCOMPUTER D.A.T.A.BOOK®

The best first-step you can take toward applying MICROPROCESSOR/MICROCOMPUTER technology in your design decisions!



Start with specific, detailed, easy-to-use information about all the microprocessor/microcomputer systems, hardware, and software being produced or in development around the world today. Find it all in the new MICROCOMPUTER D.A.T.A.BOOK.

Here--in one volume, conveniently indexed and cross referenced--is the information you need to compare and evaluate the designs and functions of

- microprocessor chips, chip-sets, cards
- compatible memory options
- interface and control devices
- systems and applications software
- micro-instruction sets
- family-related systems and components
- complete "stand-alone" microcomputers

Six technical drawing sections offer tremendous help in completing the information picture.

We guarantee the MICROCOMPUTER D.A.T.A.BOOK to be your best first-step in researching this remarkable technological achievement. It cuts through the mountain of catalog and data sheet information to get you right to the comparative details. And you can try this information system free for 30 days.

*Only you can appreciate having complete, worldwide information readily available in a single, easy-to-use reference. That's why D.A.T.A. wants you to evaluate this new D.A.T.A.BOOK, on the job, at no risk for 30 days.*

Order your subscription (two semiannual editions) by checking MC on the D.A.T.A.BOOK order card in the front of the book.