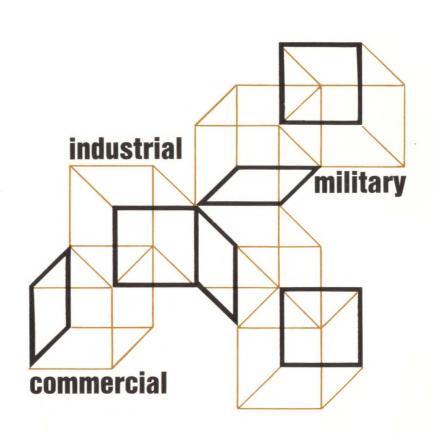
A-product guide

new dimensions with mos technology



American Micro-systems, Inc.

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American Micro-systems, Inc. MI



Programmable Automatic Functional Tester (PAFT), designed and built at AMI to meet exacting test requirements for complex arrays, uses customer's program sent by TWX or computergenerated paper tape.



Calcomp plotter used in AMI's computeraided design program to provide a direct translation from logic programs to rubylith artwork.



Life testing equipment for high reliability devices such as those used in telemetry systems in NASA's Interplanetary Platform (IMP) satellites. Mounting boards are designed to permit device operation and testing at 125°C.

Leading specialists in MOS LSI technology

Total capability from custom-circuit development to large-scale production of standard and custom MOS devices

Off-the-shelf shift registers, memories, multiplexers, and logic circuits designed for Military as well as Industrial/Commercial applications

Automated design and production in ultra-clean environments

A well-balanced scientific and engineering team of acknowledged leaders in the field

AMI equals MOS expertise and new dimensions with MOS technology.

capabilities and facilities

AMI has a total capability which ranges from research in MOS technology to the production of developmental devices and standard products. Its services may be tailored to specific customer requirements at any desired interface point, be it system organization, system partitioning, composite layout, artwork and mask making, processing, packaging, testing, or failure analysis. This approach offers economy of design effort, privacy of design, timely delivery, low production costs, highlevel technical competence, and complete control of design philosophies and quality.

To maintain its technological leadership, AMI supports continuing programs devoted to in-depth

studies of system and sub-system designs, to the development of advanced processing methods, and to the improvement of existing design and processing techniques. These programs are augmented by a substantial volume of R&D projects under contract with military agencies and industrial concerns.

American Micro-systems, Inc., is expanding its current facilities in Santa Clara, to include the largest production facility exclusively designed for large-scale integrated MOS circuits. The new 90,400 sq. ft. facility will be on line in mid-1969 and will incorporate the most modern capabilities available in the industry.

design and manufacturing

The quality and reliability of AMI MOS devices result from:

- 1. A single, rigorously controlled manufacturing process which insures uniform products with a continuous record of reliable performance.
- 2. Strictly enforced design rules matched to this process to provide optimum characteristics.
- 3. Fabrication and assembly in ultra-clean environments.
- 4. Detailed inspection by qualified MOS specialists throughout the manufacturing process.

As a result of continuing efforts to refine existing methods and develop new techniques, AMI's processing engineers have developed the ultra thick oxide (UTO) process which produces higher yields, higher density, and more reliable circuits than possible with other methods.

Another process has been developed to produce low voltage MOS (LMOS). LMOS devices operate from a +5 volt to -12 volt power supply and interface directly with DTL/TTL logic levels for bipolar IC and hybrid applications.

reliability and quality assurance

The AMI Product Assurance Division supports all research, development, engineering and manufacturing activities. This group maintains AMI's product integrity by enforcing rigid inspection and test standards ranging from the inspection of vendor's facilities and materials to final testing of the packaged MOS product. All manufacturing operations, tests, and inspections are fully documented to provide complete traceability of raw materials, processes, and devices. As a result, the standards

used at AMI exceed the specifications of even the most demanding system requirements.

AMI's product integrity has been proven in a number of high-reliability applications. Typical is NASA's interplanetary platform (IMP) satellite program. Thousands of AMI MOS devices have been built into space-borne packages and other systems having equal need for the ultimate in circuit reliability.

custom design

AMI's capabilities and facilities are ideally suited for the development and production of custom-made MOS devices. The complex nature of these large-scale arrays necessitates close cooperation between the customer — the systems designer — and AMI's engineering team in the design, layout, and test procedures for the device. Augmenting the AMI scientific and engineering team are experienced application engineers based at key locations throughout the United States. These "on-the-spot" engineers are ready to acquaint present and potential AMI customers with the many uses of MOS devices and assist the customer in exploiting the MOS technology to the fullest extent in his particular application.

Supporting AMI's extensive custom-design experience (over 400 custom circuits) is an advanced computer-aided design system. A logic simulation capability is available to assist customers in initial design efforts. Rapid and accurate chip design is

routinely accomplished on large-scale computers through AMI's Placement, Routing, and Folding (PRF) program. A large library of proven cells is available to meet most customer requirements with computer-aided design. When special cells are required, AMI's experienced design group immediately generates cells that are tailor-made to the customer's requirements.

An automated plotting facility interfaces with the computer-aided design output to provide rapid and accurate composite drawings and rubylith artwork for the preparation of masks in the photolithography facility. Concurrently, the outputs of the automated design and artwork programs are used as inputs to provide a thorough-going function test sequence for the new designs.

AMI's computer-aided design facility reduces design time, lowers development costs, and optimizes circuit design.

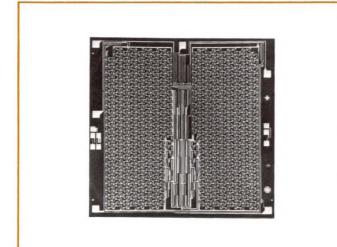
standard product lines

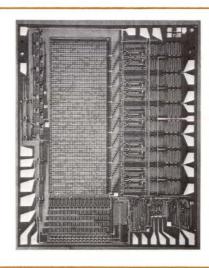
In addition to its involvement in custom circuits, AMI produces standard, off-the-shelf MOS circuits for a wide spectrum of applications. Generally these standard products fall into the two major categories: 1) Military applications (-55 to $+125^{\circ}$ C) and 2) Industrial/Commercial applications (-25 to $+75^{\circ}$ C). Each of the two categories includes dynamic and static shift registers, memories, multiplexers, standard arrays, and low power arrays.

A family of standard MOS P-channel transistors $(-55 \text{ to } +125^{\circ}\text{C})$ is also available.

Derived from the AMI custom-circuit capability, these standard arrays are processed and assembled with the same painstaking attention to process control, cleanliness, and total product assurance as the custom arrays.

The standard circuits are listed on the following pages along with packaging information, functions, and pertinent operating parameters.





shift registers/memories/multiplex arrays/logic circuits

DESIGNED TO MILITARY TEMPERATURE RANGE / -55°C TO + 125°C

SHIFT REGISTERS — Two Phase

PART NO.	NO. BITS	DESCRIPTION	TYPE PKG.	NO. LEADS	DATA RATE	POWER (1 MHz)	SUPPLY VOLTS	CLOCK VOLTS
DYNAMIC								
RD02C	40	Dual 20	FP	22	1 MHz	48 mW	-12 to -18	−24 to −30
RD05D	100	Dual 50 (PL5R100 Equivalent)	FP	14	1 MHz	120 mW	−12 to −18	−24 to −30
RD05G	100	Dual 50 (PL5R100 Equivalent)	ТО	10	1 MHz	120 mW	−12 to −18	−24 to −30
RD07C	100	Dual 50	FP	22	5 MHz	120 mW	-12 to -18	−24 to −30
RD07F	100	Dual 50	TO	12	5 MHz	120 mW	-12 to -18	−24 to −30
RD08D	100	Dual 50 with Stream Select	FP	14	5 MHz	120 mW	-12 to -18	−24 to −30
RD08F	100	Dual 50 with Stream Select	TO	12	5 MHz	120 mW	-12 to -18	-24 to -30
RD10C	100	Dual 50	FP	22	10 MHz	120 mW	−12 to −18	-24 to -30
RD10F	100	Dual 50	TO	12	10 MHz	120 mW	−12 to −18	-24 to -30
RD12C	100	Dual 50, Low V _⊺	FP	22	1 MHz	10 mW	-4.8 to -5.3	−9.8 to −10.3
RD12F	100	Dual 50, Low V _⊺	TO	12	1 MHz	10 mW	-4.8 to -5.3	−9.8 to −10.3
RD12H	100	Dual 50, Low V₁	DIP	16	1 MHz	10 mW	-4.8 to -5.3	−9.8 to −10.3
RD13G	198	Triple 66	TO	10	1 MHz	240 mW	−12 to −18	−24 to −30
RD14G	256	Single 256	TO	10	2 MHz	50 mW	−12 to −18	−24 to −30
RD14H	256	Single 256	DIP	16	2 MHz	50 mW	−12 to −18	−24 to −30
RD15D	426	Single 426, Low V₁	FP	14	2 MHz	110 mW	−5 to −10	−14 to −16
RD15G	426	Single 426, Low V₁	TO	10	2 MHz	110 mW	−5 to −10	−14 to −16
STATIC								
RS03G	80	Dual 40	TO	10	1 MHz	140 mW	−12 to −18	−24 to −30
SP01C	12	Serial/Parallel Converter	FP	22	2 MHz	270 mW	−24 to −30	−24 to −30

MEMORIES

PART	NO.	DESCRIPTION	TYPE	NO.	DATA	POWER	SUPPLY	CLOCK
NO.	BITS		PKG.	LEADS	RATE	(1 MHz)	VOLTS	VOLTS
MA01M	2560	256x10 ROM	DIP	40	1 MHz	140 mW	-15 to -17	-15 to -17
MB01I	2048	2048x1 ROM	DIP	28	1 MHz	150 mW	-24 to -30	-24 to -30

STANDARD ARRAYS

PART NO.	DESCRIPTION	TYPE PKG.	NO. LEADS	PROP. DELAY	POWER (1 MHz)	SUPPLY VOLTS	CLOCK VOLTS
UL02C	Expandable NAND Gate	FP	22	350 ns	40 mW	−12 to −18	−24 to −30
UL03C	Expandable NOR Gate	FP	22	100 ns	120 mW	-12 to -18	−24 to −30
UL04C	Gating and Storage	FP	22	1000 ns	60 mW	−12 to −18	−24 to −30

LOW POWER ARRAYS

PART NO.	DESCRIPTION	TYPE PKG.	NO. LEADS	PROP. DELAY	POWER (100 kHz)	SUPPLY Volts	CLOCK VOLTS
LP04A	Binary	ТО	8	3.5 μs	0.5 mW	−9 to −30	_
LP05A	3-Input NOR	ТО	8	2.0 μs	0.1 mW	−9 to −30	_
LP06A	3-Input NAND	ТО	8	2.0 μs	0.1 mW	−9 to −30	_

MULTIPLEX ARRAYS

PART NO.	DESCRIPTION	TYPE PKG.	NO. Leads	RON LEADS	IDSS (A)	SUPPLY VOLTS	C _{GD} (pF)	BV _{DSS} (Volts)
MX02D	6 Channel MUX (MEM 2009 Equivalent)	FP	14	150	1.0	_	1.5	-30
MX03C	10 Channel MUX	FP	22	150	1.0	_	1.5	-30
MX04C	4 Channel MUX	FP	22	50	5.0	−24 to −30	_	-30

^{*}Last letter of part # identifies package type. Please turn to pages 7 and 8 for package dimensions and lead configurations.

^{**}PC01 and PC01-T are designed for use with the standard array family to facilitate breadboarding.

shift registers/memories/multiplex arrays/logic circuits

DESIGNED TO INDUSTRIAL-COMMERCIAL TEMPERATURE RANGE / -25°C TO +75°C

SHIFT REGISTERS — Two Phase

PART NO.	NO. BITS	DESCRIPTION	TYPE PKG.	NO. LEADS	DATA RATE	POWER (1 MHz)	SUPPLY VOLTS	CLOCK VOLTS
DYNAMIC	68900							
RD52C	40	Dual 20	FP	22	1 MHz	48 mW	-12 to -18	-24 to -30
RD55D	100	Dual 50 (PL5R100 Equivalent)	FP	14	1 MHz	120 mW	-12 to -18	-24 to -30
RD55G	100	Dual 50 (PL5R100 Equivalent)	то	10	1 MHz	120 mW	−12 to −18	-24 to -30
RD57C	100	Dual 50	FP	22	1 MHz	120 mW	-12 to -18	-24 to -30
RD57F	100	Dual 50	TO	12	1 MHz	120 mW	-12 to -18	-24 to -30
RD58D	100	Dual 50 with Stream Select	FP	14	2 MHz	120 mW	-12 to -18	-24 to -30
RD58F	100	Dual 50 with Stream Select	TO	12	2 MHz	120 mW	-12 to -18	-24 to -30
RD60C	100	Dual 50	FP	22	3 MHz	120 mW	-12 to -18	-24 to -30
RD60F	100	Dual 50	TO	12	3 MHz	120 mW	-12 to -18	-24 to -30
RD62C	100	Dual 50, Low V _T	FP -	22	1 MHz	10 mW	-4.8 to -5.3	-9.8 to -10.3
RD62F	100	Dual 50, Low V _T	TO	12	1 MHz	10 mW	-4.8 to -5.3	-9.8 to -10.3
RD62H	100	Dual 50, Low V₁	DIP	16	1 MHz	10 mW	-4.8 to -5.3	-9.8 to -10.3
RD63G	198	Triple 66	TO	10	1 MHz	240 mW	-12 to -18	-24 to -30
RD64G	256	Single 256	TO	10	1 MHz	50 mW	-12 to -18	-24 to -30
RD64H	256	Single 256	DIP	16	1 MHz	50 mW	-12 to -18	-24 to -30
RD65D	426	Single 426, Low V _T	FP	14	1 MHz	110 mW	−5 to −10	-14 to -16
RD65G	426	Single 426, Low V _T	TO	10	1 MHz	110 mW	−5 to −10	-14 to -16
STATIC		94 2						
RS53G	80	Dual 40	TO	10	1 MHz	140 mW	-12 to -18	-24 to -30
SP51C	12	Serial/Parallel Converter	FP	22	1 MHz	270 mW	-24 to -30	-24 to -30

MEMORIES

PART NO.	NO. BITS	DESCRIPTION	TYPE PKG.	NO. LEADS	DATA RATE	POWER (1 MHz)	SUPPLY VOLTS	CLOCK VOLTS
MA51M	2560	256x10 ROM	DIP	40	1 MHz	140 mW	-15 to -17	-15 to -17
MB51I	2048	2048x1 ROM	DIP	28	1 MHz	150 mW	-24 to -30	-24 to -30

STANDARD ARRAYS

PART NO.	DESCRIPTION	TYPE PKG.	NO. LEADS	PROP. DELAY	POWER (1 MHz)	SUPPLY VOLTS	CLOCK VOLTS
UL52C	Expandable NAND Gate	FP	22	350 ns	40 mW	-12 to -18	-24 to -30
UL53C	Expandable NOR Gate	FP	22	100 ns	120 mW	-12 to -18	-24 to -30
UL54C	Gating and Storage	FP	22	1000 ns	60 mW	-12 to -18	-24 to -30

LOW POWER ARRAYS

PART NO.	DESCRIPTION	TYPE PKG.	NO. LEADS	PROP. DELAY	POWER (100 kHz)	SUPPLY VOLTS	CLOCK
LP54A	Binary	ТО	8	3.5 μs	0.5 mW	−9 to −30	
LP55A	3-Input NOR	TO	8	2.0 μs	0.1 mW	−9 to −30	
LP56A	3-Input NAND	TO	8	2.0 μs	0.1 mW	−9 to −30	

MULTIPLEX ARRAYS

PART NO.	DESCRIPTION	TYPE PKG.	NO. LEADS	Ron LEADS	loss (A)	SUPPLY VOLTS	C _{GD} (pF)	BV _{DSS} (Volts)
MX52D	6 Channel MUX (MEM 2009 Equivalent)	FP	14	150	1.0		1.5	-30
MX53C	10 Channel MUX	FP	22	150	1.0		1.5	-30
MX54C	4 Channel MUX	FP	22	50	5.0	-24 to -30		-30

nos "P" channel transistors

TEMPERATURE RANGE -55°C TO +125°C

ТҮРЕ	FUNCTION	VT (Volts)	V _{T1} —V _{T2} (mV)	Υ _{fs} (μ mho)	IDS(ON) (mA)	R ON (Ω)	C _{gd} (pF)	BV _{DSS} (Volts)	BVGSS (Volts)	I _{DSS} (nA)
DM01B	Dual Matched $<$ 50mV V _{GST} $g_m = 800 \mu mho R_{ON} = 1250 \Omega$	-4.5	< 50	800	4.6	1250	0.4	-36	>±70	0.50
DM02B	Dual Matched $<$ 100mV V _{GST} $g_m = 800 \mu mho R_{ON} = 1250 \Omega$	-4.5	<100	800	4.6	1250	0.4	-36	>±70	0.50
DM03B	Dual Matched $<$ 150mV V _{GST} $g_m = 800 \mu \text{mho R}_{ON} = 1250 \Omega$	-4.5	<150	800	4.6	1250	0.4	-36	>±70	0.50
DM05A	Dual W/Protective Diode $g_m = 2700 \mu \text{mho R}_{ON} = 250\Omega$	-4.3	50	2700	22.0	250	0.3	-41	-41	0.03
DM06A	Dual W/O Protective Diode $g_m = 2700 \mu mho R_{ON} = 250\Omega$	-4.3	50	2700	22.0	250	0.3	-41	>±70	0.03
DD07K	Single W/Protective Diode $g_m = 5400 \mu \text{mho R}_{ON} = 125\Omega$	-4.3	_	5400	44.0	125	0.6	-41	-41	0.05
DD08K	Single W/O Protective Diode $g_m = 5400 \mu \text{mho R}_{ON} = 125\Omega$	-4.3	_	5400	44.0	125	0.6	-41	>±70	0.05
DD09K	Single W/Protective Diode $g_m = 2700 \mu \text{mho R}_{ON} = 250\Omega$	-4.3	-	2700	22.0	250	0.3	-41	-41	0.03
DD10K	Low V_T Single W/Protec. Diode $g_m = 8000 \mu mho R_{ON} = 125\Omega$	-2.0	-	8000‡	33.0‡	125‡	0.8	-20	-20	0.05
DD11K	Single W/Protective Diode $g_m = 1400 \mu \text{mho R}_{ON} = 700\Omega$	-4.6	_	1400	10.0	700	0.3	-41	-41	0.30
DD12J	Hi g _m Single W/Protective Diode $g_m = 17000 \mu mho R_{ON} = 32\Omega$	-4.3	_	17,000	35.5‡	32	2.3	-41	-41	0.04
DD13K	Hi g _m Single W/Protective Diode $g_m = 17000 \mu mho R_{ON} = 32 \Omega$	-4.3	_	17,000	35.5‡	32	2.3	-41	-41	0.04

NOTE: DM is a dual-discrete. DD is a single-discrete.

mos course

In line with the efforts to acquaint its customers with MOS devices and their applications, AMI conducts an intensive Engineering Course in MOS technology at regular intervals. This course is instructed by AMI staff members—experts in MOS.

The five-day course is tailored to provide systems and circuit design engineers with a comprehensive background in complex MOS arrays.

Material supplied by AMI includes an engineering course manual and set of lecture notes. The manual covers the theoretical background and the practical aspects of MOS technology and the application of MOS circuits in new and existing systems.

The basic course outline is as follows:

Basic Theory of P-MOS Devices Implementation of Digital Logic Functions Implementation of Linear Functions

applications

Multiplexing and analog switching applications are "naturals" of the MOS Technology because of the high "off" resistance and the approximately zero offset voltage. However, the major advantage of MOS is that large, complex logic arrays can be built at a much lower cost than possible with other technologies. The process is ideally suited for the implementation of economical large read-only (ROM) and random-access (RAM) memories. For the first time, content-addressable memories (CAM) become an economic reality.

Applications for large-scale custom arrays continue

Partitioning Theoretical MOS Design Equations and Graphs Design Procedures Array Topology **Design Automation** MOS Specifications and Applications Large-Scale-Array Testing Reliability of MOS Large-Scale-Arrays Design of Medium Complexity Standard Cell—Class Participation Plant Tour and Discussions

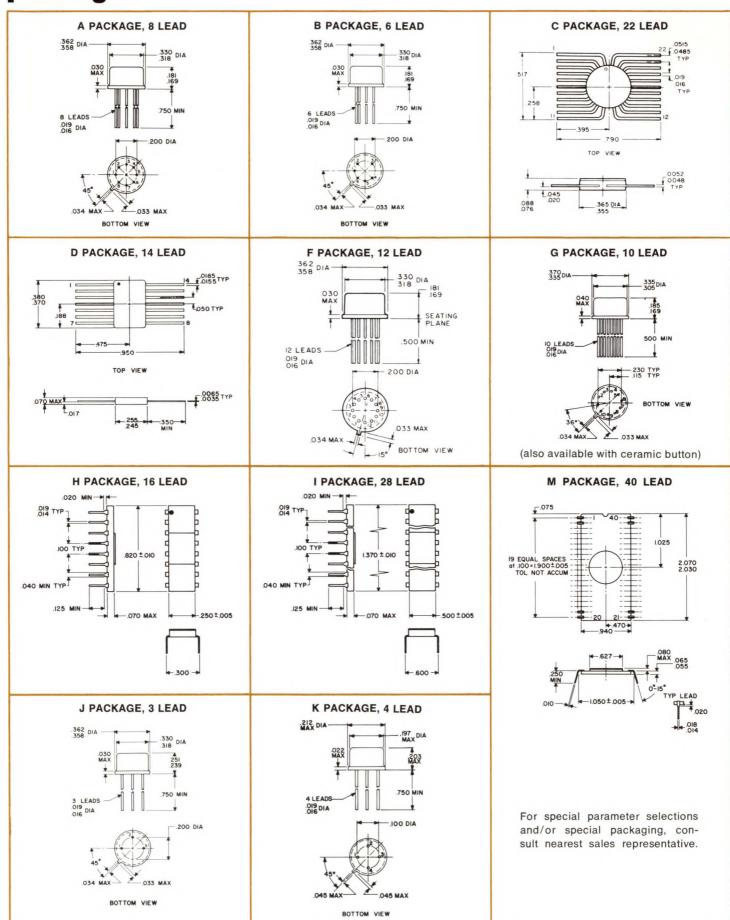
Enrollment fee for the five-day course — including the engineering course manual and a set of lecture notes — is \$500 per attendee.

For further information about the AMI MOS technology engineering course and a schedule of upcoming sessions, complete the attached, selfaddressed reply card and mail it to AMI.

to grow at a rapid rate, and the ease of designing and fabricating such arrays with MOS technology makes this approach ideal for complex digital systems. MOS arrays are particularly suitable for digital applications such as timers and counters of all types from consumer products to precision artillery fuzes; communications, data-transmission and switching equipment; tape recorders; business machines; industrial controls; small scientific computers; computer peripheral equipment; and instrumentation. Linear applications include analog switching devices, low-power amplifiers, and active-filter networks.

 Y_{fs} and I_{DS} (ON) measured at: $V_{GS} = V_{DS} = -15$ volts; $R_{(ON)}$ measured at: $V_{GS} = -15$ volts, $V_{DS} = -0.1$ volts. Exceptions as indicated $\pm Y_{fs}$ and I_{DS} (ON) measured at: $V_{GS} = V_{DS} = -10$ volts; $R_{(ON)}$ measured at: $V_{GS} = -10$ volts, $V_{DS} = -0.1$ volts.

packages





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DD07A

DD08A

38. DD09K

39. DD10K

40. DD11K

41. DD12J

DD13K

ОНЮ

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data sheets

u	ata Siic	Glo
1.	RD02C	Dual 20-Bit Register
2.	RD05D (G)	Dual 50-Bit Registers
	RD07C (F)	Dual 50-Bit Registers
4.	RD08D (F)	Dual 50-Bit Registers with Stream Select
5.	RD10C(F)	Dual 50-Bit Registers
6.	RD12C (F)	50-Bit Registers Low V _™
	RD12H	50-Bit Register Low V _T
	RD13G	Triple 66-Bit Register
8.	RD14G (H)	256-Bit Registers
	RD15D (G)	
	SP01C	12-Bit Serial/Parallel Converter DC
	RS03G	Dual 40-Bit Register DC
12.	MX02D	6 Channel Multiplexer
	MX03C	10 Channel Multiplexer
	MX04C	4 Channel Multiplexer Switch
	UL02C	Quad NAND Gate
	UL03C	Expandable Gate
	UL04C	Gating and Storage Element
17.	LP04A	Binary
	LP05A LP06A	3-Input NOR
18.		3-Input NAND Dual 20-Bit Register
	RD55D (G)	Dual 50-Bit Registers
	RD57C (F)	Dual 50-Bit Registers
21.		Dual 50-Bit Registers with
	NDOOD (I)	Stream Select
22.	RD60C (F)	Dual 50-Bit Registers Low V _↑
	RD62C (F)	Dual 50-Bit Registers Low V _™
	RD62H	Dual 50-Bit Register Low V _T
25.	RD63G	Triple 66-Bit Register
26.	RD64G (H)	256-Bit Registers
27.	RD65D (G)	426-Bit Registers Low V _™
28.	SP51C	12-Bit Serial/Parallel Converter DC
29.		Dual 40-Bit Register
30.		6 Channel Multiplexer
2.5	MX53C	10 Channel Multiplexer
31.		4 Channel Multiplexer Switch
	UL52C	Quad NAND Gate
	UL53C	Expandable Gate
	UL54C	Gating and Storage Element
35.	LP54A	Binary
	LP55A	3-Input NOR
36.	LP56A	3-Input NAND
50.	DM01B DM02B	Dual Matched P-Channel FET Dual Matched P-Channel FET
	DM02B	Dual Matched P-Channel FET
37.	DM05A	Medium Conductance P-Channel FET
57.	DM05A	Medium Conductance P-Channel FET
	DROZA	Medium Conductance P Channel FET

Medium Conductance P-Channel FET

Medium Conductance P-Channel FET

Medium Conductance P-Channel FET

High Conductance P-Channel FET

High Conductance P-Channel FET

Medium Conductance Low Voltage FET

Medium Conductance Low Capacitance FET



availability

All AMI standard MOS devices are "off-the-shelf" products available for immediate delivery. To support large-quantity deliveries, in-depth inventories are maintained at AMI's plant.

In addition, AMI has a nation-wide network of stocking distributors stocked to provide 24-hour delivery of all standard products. (See listing of the AMI distributors and their locations).

terms and conditions of sale

All sales are made in accordance with AMI's STANDARD TERMS AND CONDITIONS. Prices are quoted FOB AMI's plant or distributor's location, and do not include applicable Federal, state or local taxes.

Terms of payment in the USA is thirty (30) days net.

When placing an order, customer has complete privilege to mix circuits within the MOS complex array family or within the MOS FET family. This privilege does not extend to intermixing of families or the combination of Military and Industrial/Commercial products.

shipping and packaging

Shipments are made directly from the AMI plant or from local distributors. Unless specifically requested otherwise, AMI will select the most economical and serviceable method of shipment.

All AMI products are carefully packaged in specially designed containers to prevent in-route damage.

warranty

AMI warrants its products against defects in materials and workmanship for a period of one year after delivery. This warranty is limited to replacement or repair of products which prove to be defective during the warranty period, provided they are returned to AMI and the defect is found not to be caused by misuse, neglect, improper installation, repair, alteration, or accident. No other warranty is expressed or implied. AMI is not liable for consequential damages.



New, ultra-modern, 90,400 sq. ft. AMI facility is designed specifically for high-volume production of large-scale integrated arrays.



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